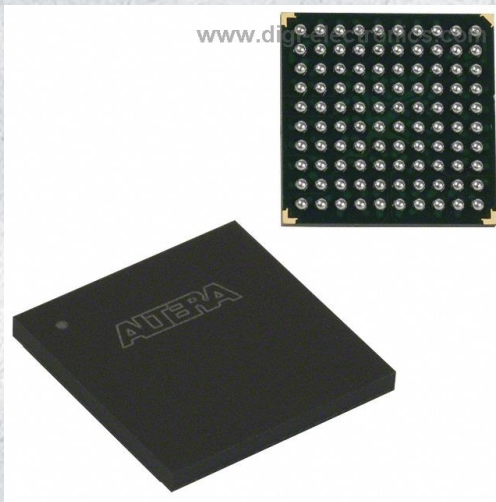


EPM240GF100C5N Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	EPM240GF100C5N-DG
Manufacturer	Intel
Manufacturer Product Number	EPM240GF100C5N
Description	IC CPLD 192MC 4.7NS 100FBGA
Detailed Description	Embedded, Integrated Circuits (ICs)

This model EPM240GF100C5N is available at DiGi.Electronics.

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We welcome your inquiries regarding pricing, lead time, or other product-related questions.

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Purchase and inquiry

Manufacturer Product Number:

EPM240GF100C5N

Series:

MAX® II

DiGi-Electronics Programmable:

Not Verified

Delay Time tpd(1) Max:

4.7 ns

Number of Logic Elements/Blocks:

240

Number of I/O:

80

Mounting Type:

Surface Mount

Supplier Device Package:

100-FBGA (11x11)

Manufacturer:

Intel

Product Status:

Active

Programmable Type:

In System Programmable

Voltage Supply - Internal:

1.71V ~ 1.89V

Number of Macrocells:

192

Operating Temperature:

0°C ~ 85°C (TJ)

Package / Case:

100-LBGA

Base Product Number:

EPM240

Environmental & Export classification

RoHS Status:

RoHS Compliant

REACH Status:

REACH Unaffected

HTSUS:

8542.39.0001

Moisture Sensitivity Level (MSL):

3 (168 Hours)

ECCN:

3A991D



6. Reference and Ordering Information

MII51006-1.6

Software

MAX® II devices are supported by the Altera® Quartus® II design software with new, optional MAX+PLUS® II look and feel, which provides HDL and schematic design entry, compilation and logic synthesis, full simulation and advanced timing analysis, and device programming. Refer to the Design Software Selector Guide for more details about the Quartus II software features.

The Quartus II software supports the Windows XP/2000/NT, Sun Solaris, Linux Red Hat v8.0, and HP-UX operating systems. It also supports seamless integration with industry-leading EDA tools through the NativeLink interface.

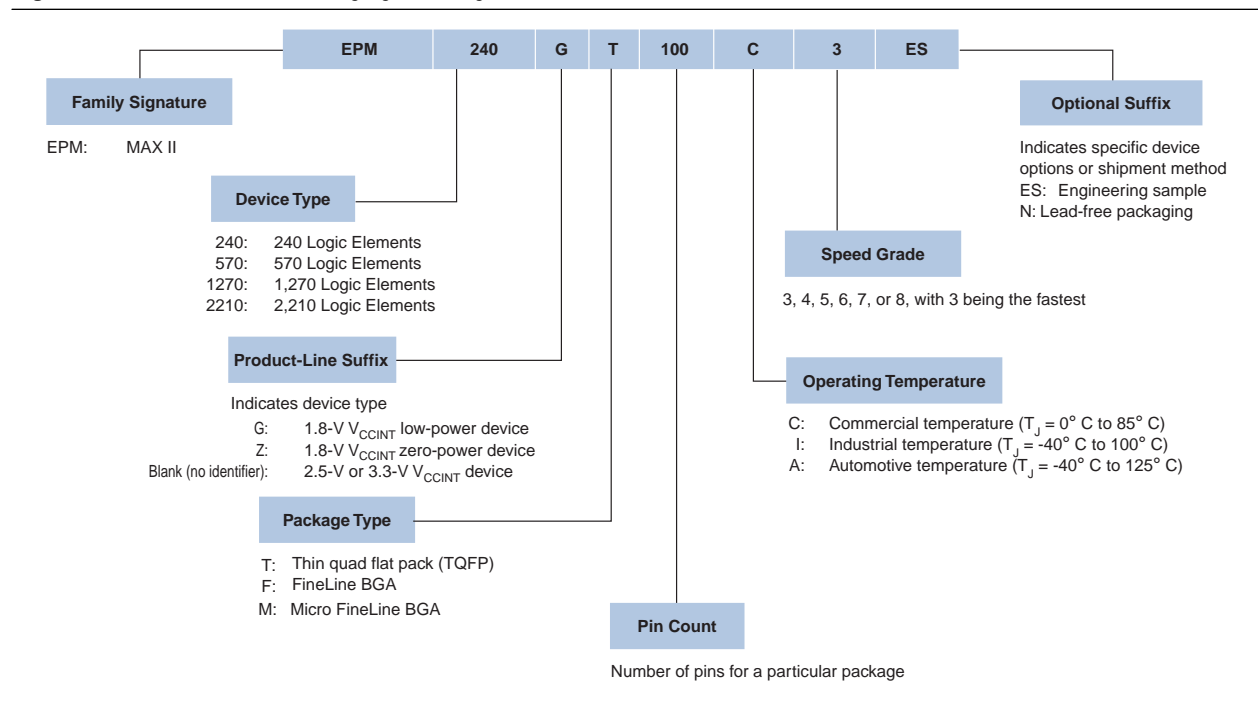
Device Pin-Outs

Printed device pin-outs for MAX II devices are available on the Altera website (www.altera.com).

Ordering Information

Figure 6–1 describes the ordering codes for MAX II devices. For more information about a specific package, refer to the *Package Information* chapter in the *MAX II Device Handbook*.

Figure 6–1. MAX II Device Packaging Ordering Information



Referenced Documents

This chapter references the following document:

- *Package Information* chapter in the *MAX II Device Handbook*

Document Revision History

Table 6–1 shows the revision history for this chapter.

Table 6–1. Document Revision History

Date and Revision	Changes Made	Summary of Changes
August 2009, version 1.6	<ul style="list-style-type: none"> ■ Updated Figure 6–1. 	Added information for speed grade –8
October 2008, version 1.5	<ul style="list-style-type: none"> ■ Updated New Document Format. 	—
December 2007, version 1.4	<ul style="list-style-type: none"> ■ Added “Referenced Documents” section. ■ Updated Figure 6–1. 	Updated document with MAX IIZ information.
December 2006, version 1.3	<ul style="list-style-type: none"> ■ Added document revision history. 	—
October 2006, version 1.2	<ul style="list-style-type: none"> ■ Updated Figure 6-1. 	—
June 2005, version 1.1	<ul style="list-style-type: none"> ■ Removed Dual Marking section. 	—

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