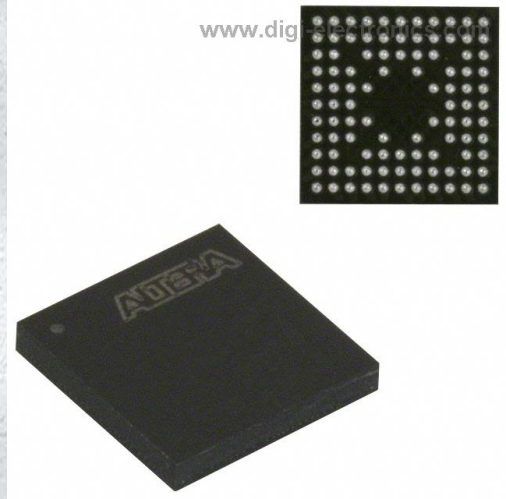
EPM240ZM100C7N Datasheet



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DiGi Electronics Part Number	EPM240ZM100C7N-DG
Manufacturer	Intel
Manufacturer Product Number	EPM240ZM100C7N
Description	IC CPLD 192MC 7.5NS 100MBGA
Detailed Description	Embedded, Integrated Circuits (ICs)

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Manufacturer Product Number:	Manufacturer:
EPM240ZM100C7N	Intel
Series:	Product Status:
MAX® II	Active
DiGi-Electronics Programmable:	Programmable Type:
Not Verified	In System Programmable
Delay Time tpd(1) Max:	Voltage Supply - Internal:
7.5 ns	1.71V ~ 1.89V
Number of Logic Elements/Blocks:	Number of Macrocells:
240	192
Number of I/O:	Operating Temperature:
80	0°C ~ 85°C (TJ)
Mounting Type:	Package / Case:
Surface Mount	100-TFBGA
Supplier Device Package:	Base Product Number:
100-MBGA (6x6)	EPM240

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
RoHS Compliant	3 (168 Hours)
REACH Status:	ECCN:
REACH Unaffected	3A991D
HTSUS:	
8542.39.0001	

EPM240ZM100C7N Intel IC CPLD 192MC 7.5NS 100MBGA

6. Reference and Ordering Information

MII51006-1.6

Software

MAX® II devices are supported by the Altera® Quartus® II design software with new, optional MAX+PLUS® II look and feel, which provides HDL and schematic design entry, compilation and logic synthesis, full simulation and advanced timing analysis, and device programming. Refer to the Design Software Selector Guide for more details about the Quartus II software features.

The Quartus II software supports the Windows XP/2000/NT, Sun Solaris, Linux Red Hat v8.0, and HP-UX operating systems. It also supports seamless integration with industry-leading EDA tools through the NativeLink interface.

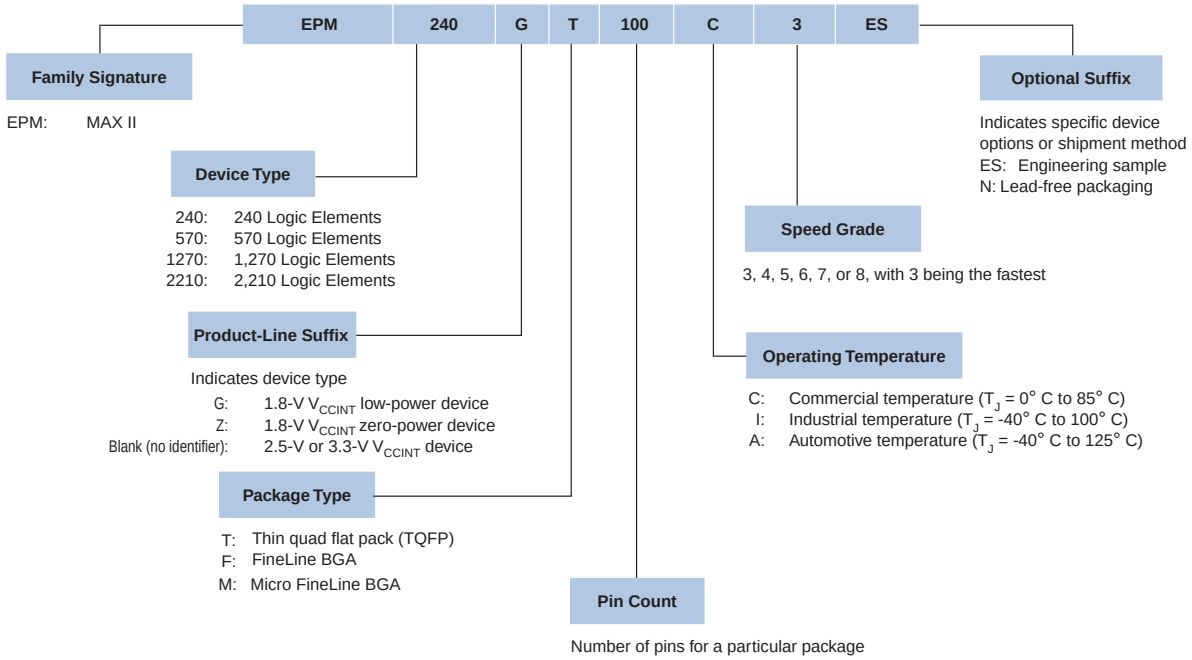
Device Pin-Outs

Printed device pin-outs for MAX II devices are available on the Altera website (www.altera.com).

Ordering Information

Figure 6–1 describes the ordering codes for MAX II devices. For more information about a specific package, refer to the *Package Information* chapter in the *MAX II Device Handbook*.

Figure 6–1. MAX II Device Packaging Ordering Information



EPM240ZM100C7N Intel IC CPLD 192MC 7.5NS 100MBGA

Referenced Documents

This chapter references the following document:

- *Package Information* chapter in the *MAX II Device Handbook*

Document Revision History

Table 6–1 shows the revision history for this chapter.

Table 6–1. Document Revision History

Date and Revision	Changes Made	Summary of Changes
August 2009, version 1.6	■ Updated Figure 6–1.	Added information for speed grade –8
October 2008, version 1.5	■ Updated New Document Format.	—
December 2007, version 1.4	■ Added “Referenced Documents” section. ■ Updated Figure 6–1.	Updated document with MAX IIZ information.
December 2006, version 1.3	■ Added document revision history.	—
October 2006, version 1.2	■ Updated Figure 6-1.	—
June 2005, version 1.1	■ Removed Dual Marking section.	—

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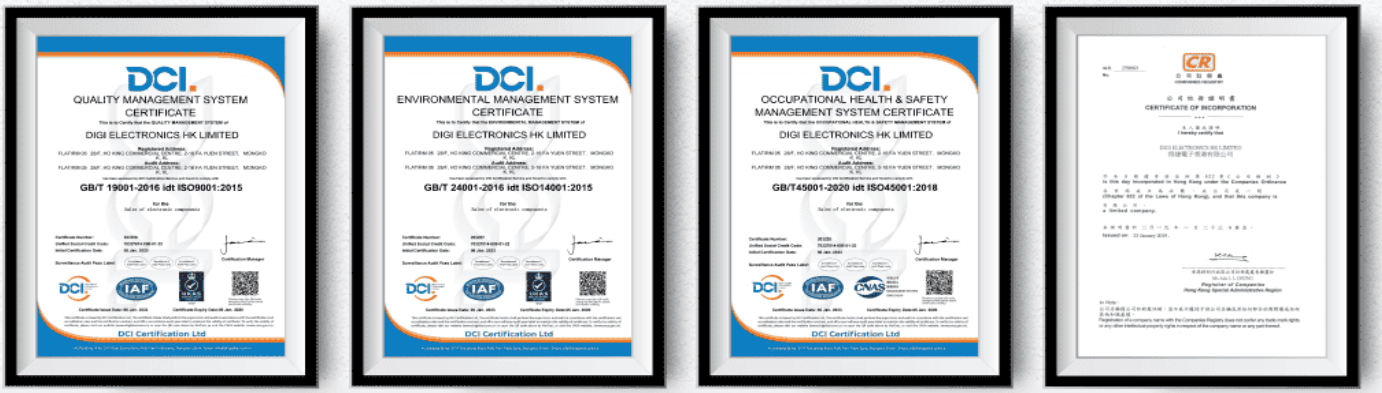

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