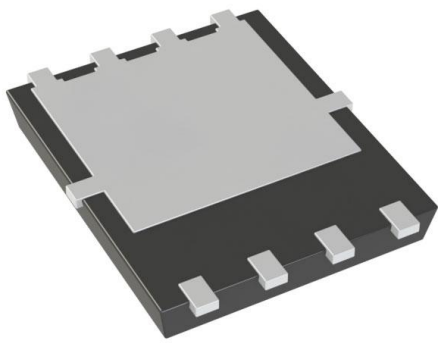


AONS21309C Datasheet

www.digi-electronics.com



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	AONS21309C-DG
Manufacturer	Alpha & Omega Semiconductor Inc.
Manufacturer Product Number	AONS21309C
Description	MOSFET P-CH 30V 70A 5X6 DFN
Detailed Description	P-Channel 30 V 70A (Tc) 125W (Tc) Surface Mount 8-DFN (5x6)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

AONS21309C

Series:

-

FET Type:

P-Channel

Drain to Source Voltage (Vdss):

30 V

Drive Voltage (Max Rds On, Min Rds On):

4.5V, 10V

Vgs(th) (Max) @ Id:

2.3V @ 250µA

Vgs (Max):

±20V

FET Feature:

-

Operating Temperature:

-55°C ~ 150°C (Tj)

Supplier Device Package:

8-DFN (5x6)

Base Product Number:

AONS213

Manufacturer:

Alpha & Omega Semiconductor Inc.

Product Status:

Active

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

70A (Tc)

Rds On (Max) @ Id, Vgs:

3.8mOhm @ 20A, 10V

Gate Charge (Qg) (Max) @ Vgs:

200 nC @ 10 V

Input Capacitance (Ciss) (Max) @ Vds:

7300 pF @ 15 V

Power Dissipation (Max):

125W (Tc)

Mounting Type:

Surface Mount

Package / Case:

8-PowerSMD, Flat Leads

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.29.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



AONS21309C

30V P-Channel MOSFET

General Description

- Latest advanced trench technology
- Low $R_{DS(ON)}$
- High Current Capability
- RoHS 2.0 and Halogen-Free Compliant

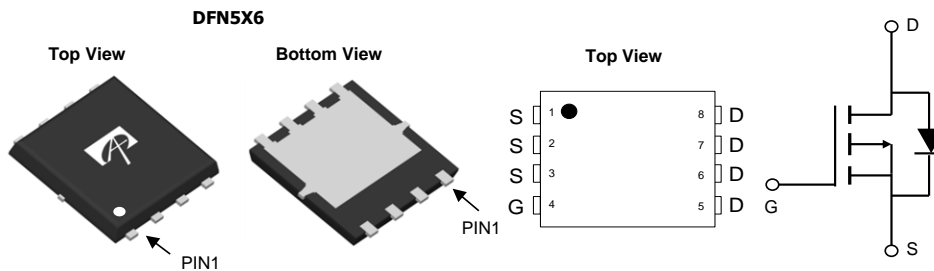
Applications

- Notebook AC-in load switch
- Battery protection charge/discharge

Product Summary

V_{DS}	-30V
I_D (at $V_{GS}=-10V$)	-70A
$R_{DS(ON)}$ (at $V_{GS}=-10V$)	< 3.8m Ω
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	< 5.7m Ω

100% UIS Tested
100% Rg Tested



Orderable Part Number	Package Type	Form	Minimum Order Quantity
AONS21309C	DFN 5x6	Tape & Reel	3000

Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^G	I_D	$T_C=25^\circ\text{C}$	-70
		$T_C=100^\circ\text{C}$	-70
Pulsed Drain Current ^C	I_{DM}	-280	A
Continuous Drain Current ^G	I_{DSM}	$T_A=25^\circ\text{C}$	-29
		$T_A=70^\circ\text{C}$	-23
Avalanche Current ^C	I_{AS}	-55	A
Avalanche energy $L=0.1\text{mH}$ ^C	E_{AS}	151	mJ
Power Dissipation ^B	P_D	$T_C=25^\circ\text{C}$	125
		$T_C=100^\circ\text{C}$	50
Power Dissipation ^A	P_{DSM}	$T_A=25^\circ\text{C}$	5.0
		$T_A=70^\circ\text{C}$	3.2
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	20	25	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Ambient ^{A,D}		45	55	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Case	$R_{\theta JC}$	0.7	1	$^\circ\text{C}/\text{W}$

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =-250μA, V _{GS} =0V	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-30V, V _{GS} =0V T _J =55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±20V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.3	-1.8	-2.3	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-20A T _J =125°C		3.1	3.8	mΩ
		V _{GS} =-4.5V, I _D =-20A		4.4	5.4	
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-20A		73		S
V _{SD}	Diode Forward Voltage	I _S =-1A, V _{GS} =0V		-0.7	-1	V
I _S	Maximum Body-Diode Continuous Current ^G				-70	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, f=1MHz		7300		pF
C _{oss}	Output Capacitance			830		pF
C _{riss}	Reverse Transfer Capacitance			700		pF
R _g	Gate resistance	f=1MHz		2.3	4.6	Ω
SWITCHING PARAMETERS						
Q _{g(10V)}	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-20A		130	200	nC
Q _{g(4.5V)}	Total Gate Charge			60	90	
Q _{gs}	Gate Source Charge			20		
Q _{gd}	Gate Drain Charge			24		
t _{D(on)}	Turn-On DelayTime	V _{GS} =-10V, V _{DS} =-15V, R _L =0.75Ω, R _{GEN} =3Ω		21		ns
t _r	Turn-On Rise Time			29		
t _{D(off)}	Turn-Off DelayTime			110		
t _f	Turn-Off Fall Time			32		
t _{rr}	Body Diode Reverse Recovery Time	I _F =-20A, di/dt=500A/μs		18		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-20A, di/dt=500A/μs		48		nC

A. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C. The Power dissipation P_{DSM} is based on R_{θJA} ≤ 10s and the maximum allowed junction temperature of 150° C. The value in any given application depends on the user's specific board design.

B. The power dissipation P_D is based on T_{J(MAX)}=150° C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

C. Single pulse width limited by junction temperature T_{J(MAX)}=150° C.

D. The R_{θJA} is the sum of the thermal impedance from junction to case R_{θJC} and case to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=150° C. The SOA curve provides a single pulse rating.

G. The maximum current rating is package limited.

H. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

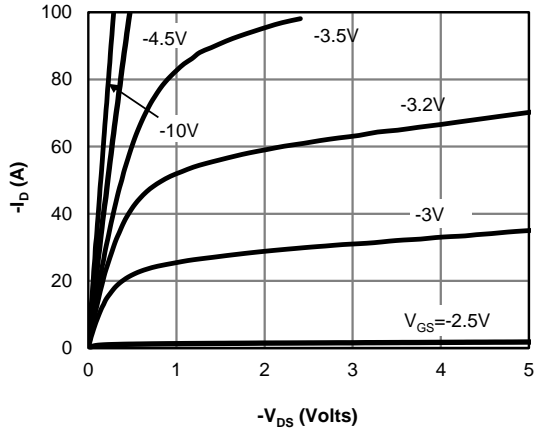


Figure 1: On-Region Characteristics (Note E)

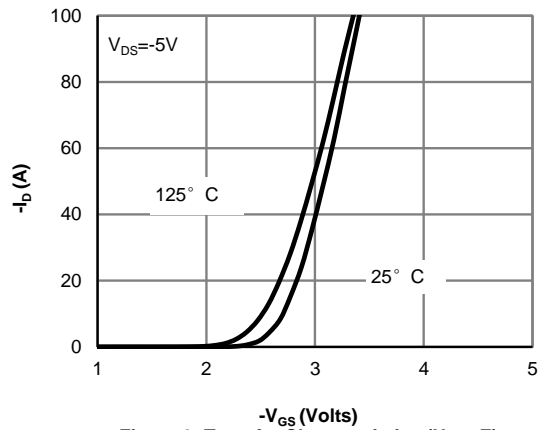


Figure 2: Transfer Characteristics (Note E)

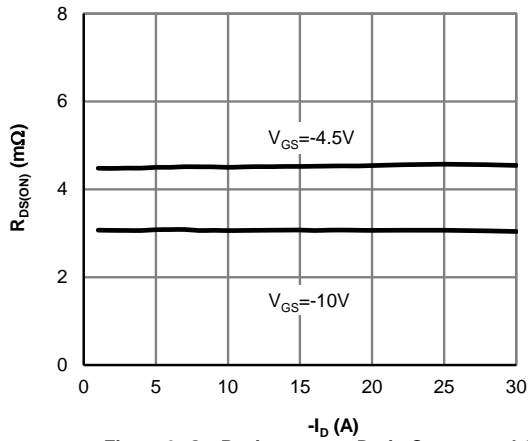


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

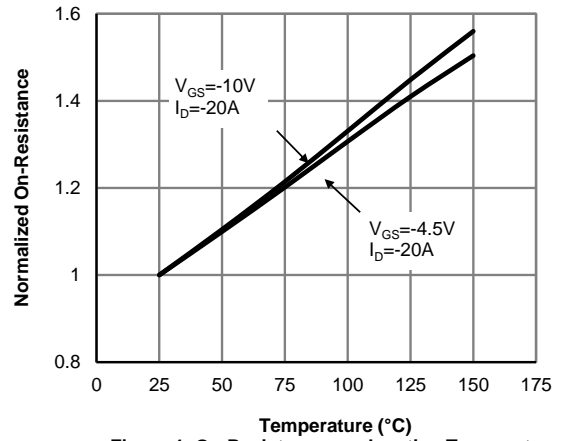


Figure 4: On-Resistance vs. Junction Temperature (Note E)

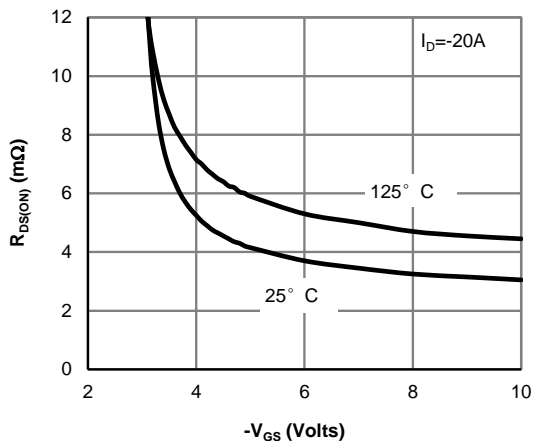


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

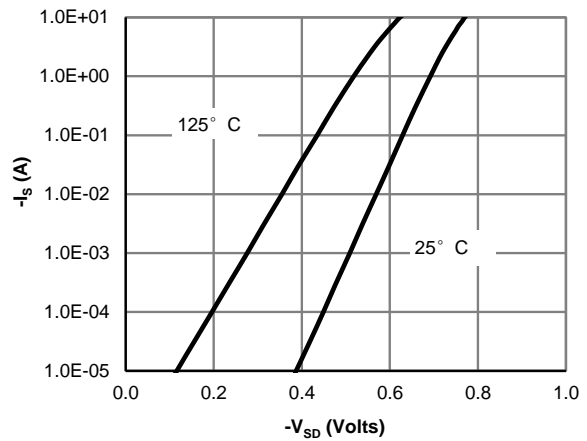


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

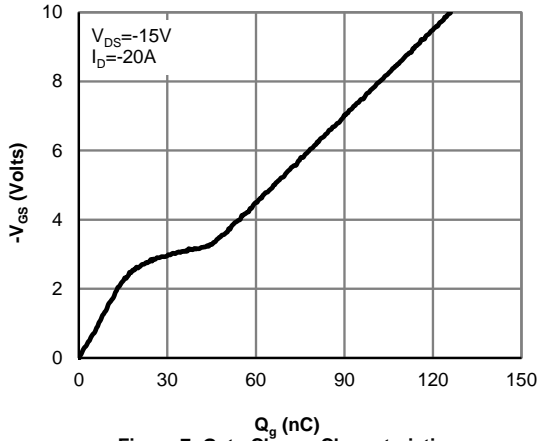


Figure 7: Gate-Charge Characteristics

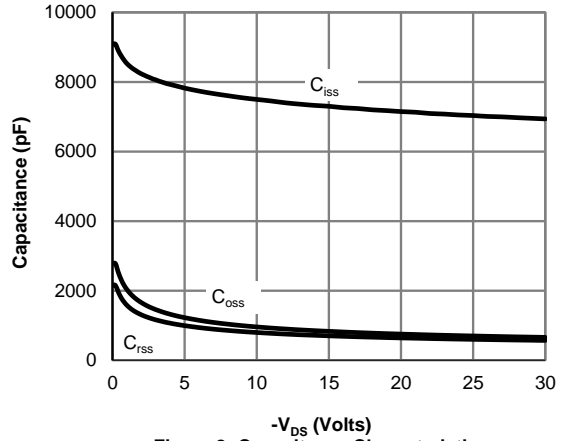


Figure 8: Capacitance Characteristics

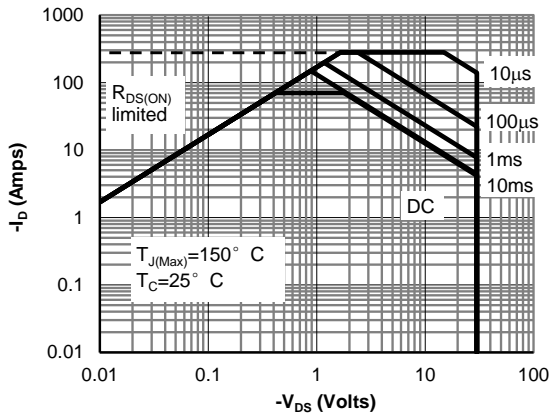


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

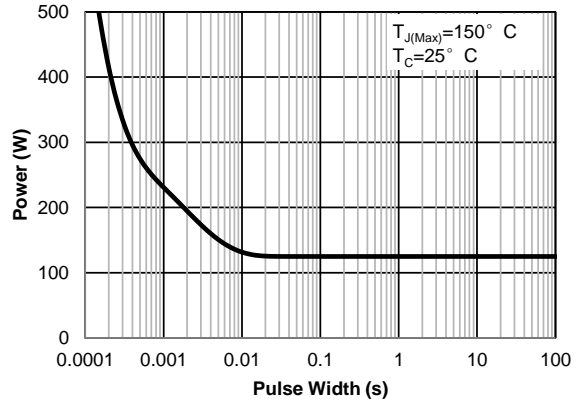


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

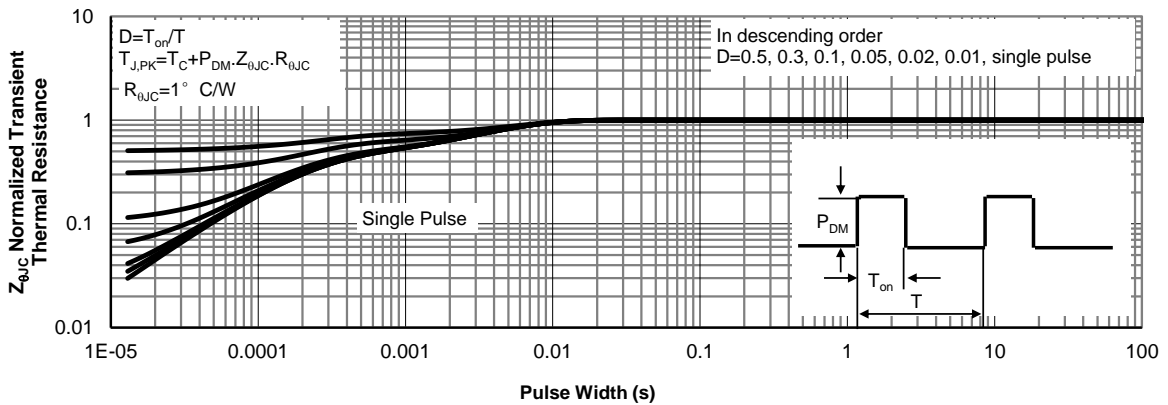


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

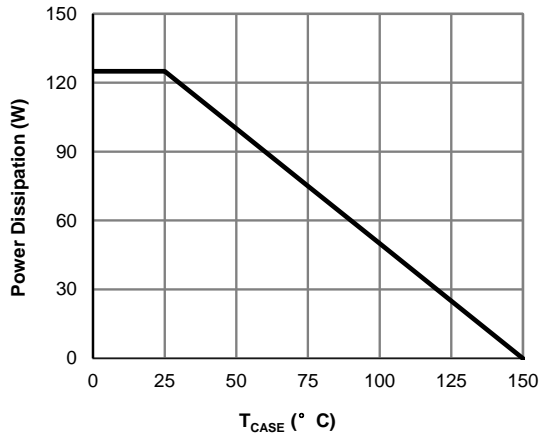


Figure 12: Power De-rating (Note F)

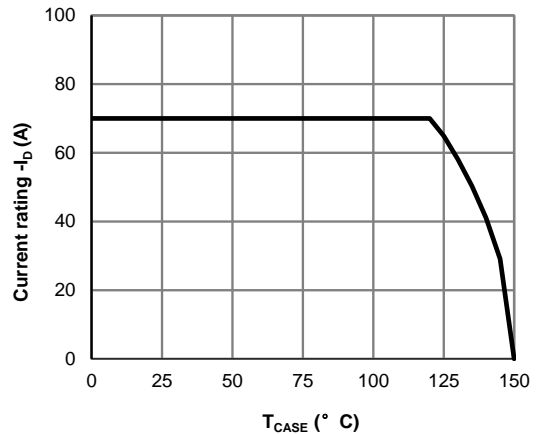


Figure 13: Current De-rating (Note F)

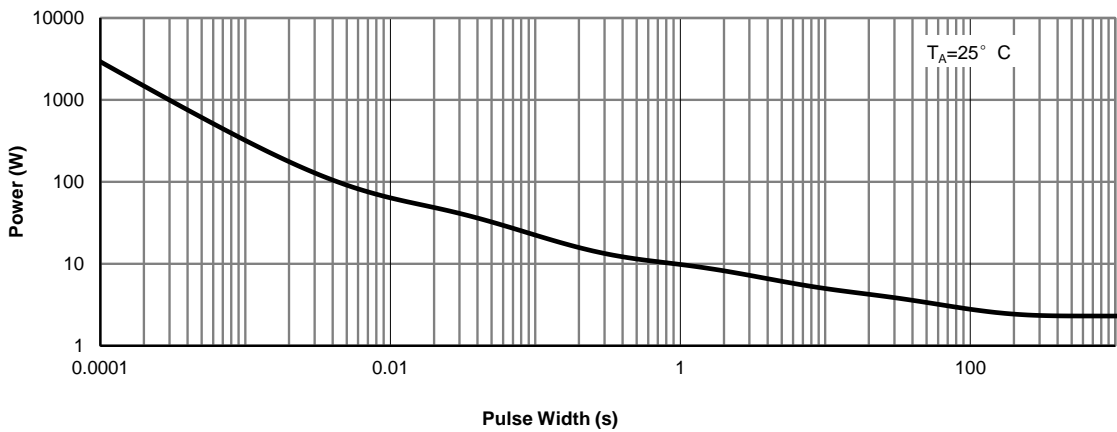


Figure 14: Single Pulse Power Rating Junction-to-Ambient (Note H)

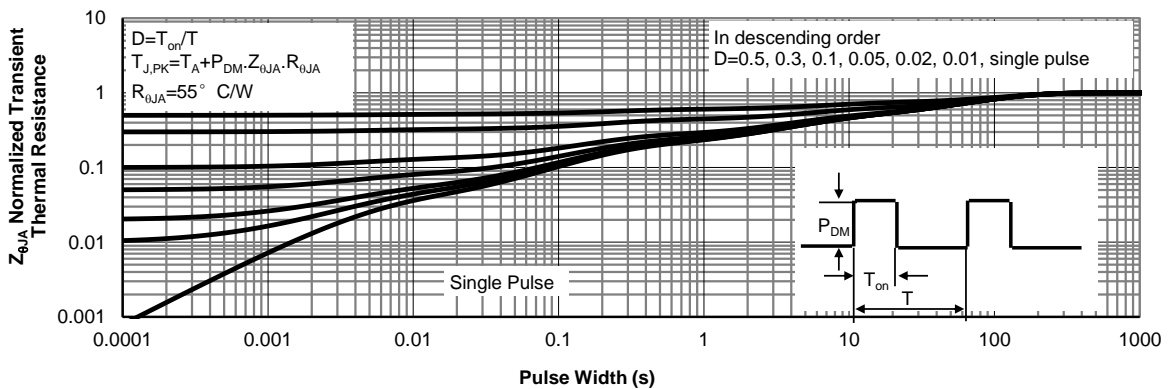
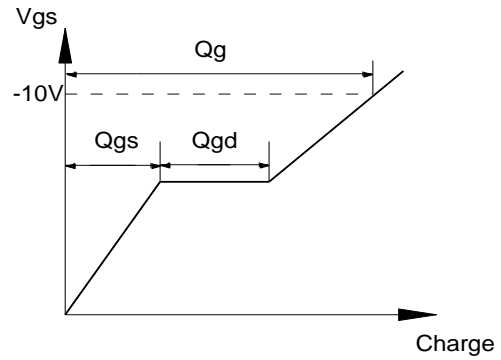
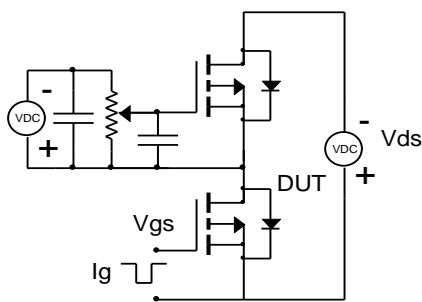
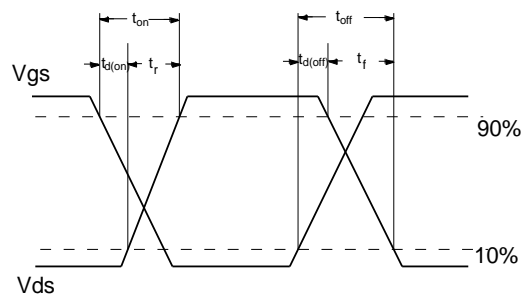
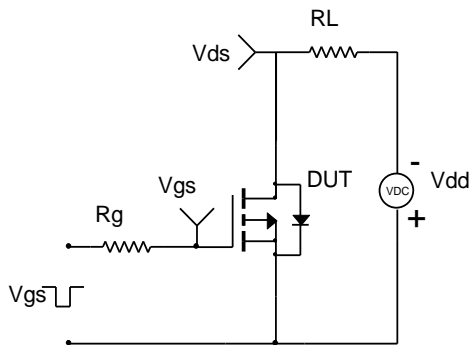


Figure 15: Normalized Maximum Transient Thermal Impedance (Note H)

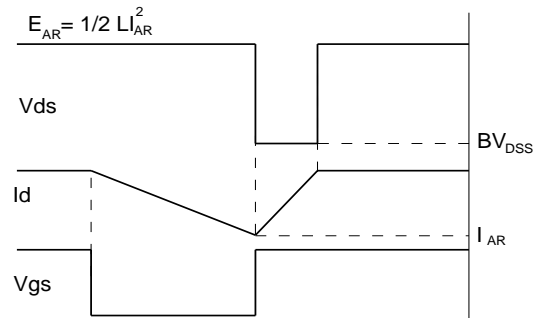
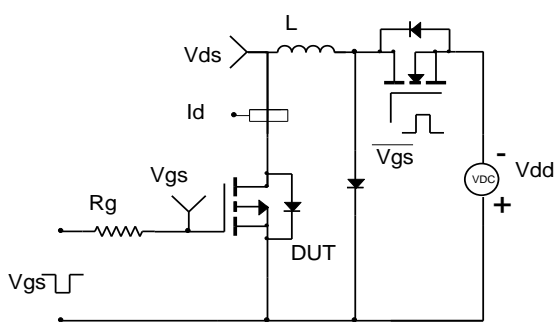
Gate Charge Test Circuit & Waveform



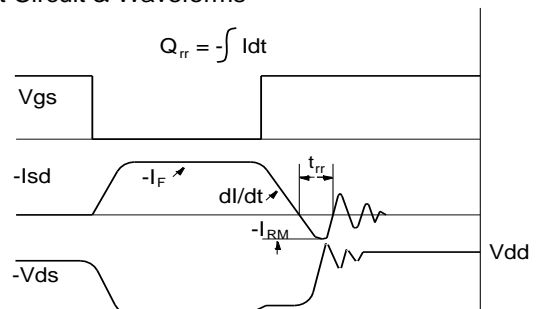
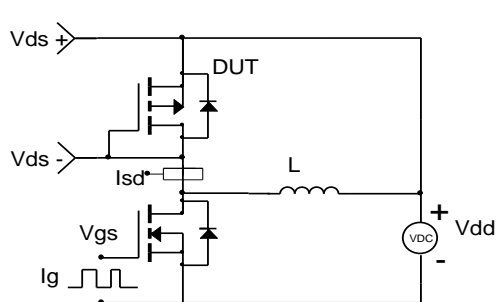
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



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