IPD15N06S2L64ATMA1 Datasheet

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DiGi Electronics Part Number	IPD15N06S2L64ATMA1-DG
Manufacturer	Infineon Technologies
Manufacturer Product Number	IPD15N06S2L64ATMA1
Description	MOSFET N-CH 55V 19A TO252-3
Detailed Description	N-Channel 55 V 19A (Tc) 47W (Tc) Surface Mount PG -TO252-3-11

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
IPD15N06S2L64ATMA1	Infineon Technologies
Series:	Product Status:
OptiMOS™	Obsolete
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
55 V	19A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
4.5V, 10V	64mOhm @ 13A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
2V @ 14µA	13 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	354 pF @ 25 V
FET Feature:	Power Dissipation (Max):
-	47W (Tc)
Operating Temperature:	Mounting Type:
-55°C ~ 175°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
PG-TO252-3-11	TO-252-3, DPAK (2 Leads + Tab), SC-63
Base Product Number:	
IPD15N	

Environmental & Export classification

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
EAR99	8541.29.0095

IPD15N06S2L-64

OptiMOS® Power-Transistor

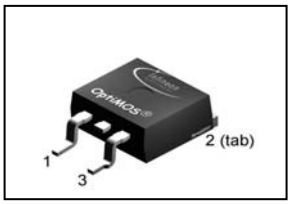
Features

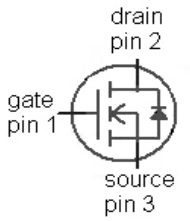
- N-channel Logic Level - Enhancement mode
- Automotive AEC Q101 qualified
- MSL1 up to 260°C peak reflow
- 175°C operating temperature
- Green package (lead free)
- Ultra low Rds(on)
- 100% Avalanche tested

Product Summary

V_{DS}	55	V
$R_{DS(on),max}$ (SMD version)	64	mΩ
I_D	19	A

PG-TO252-3-11





Type	Package	Marking
IPD15N06S2L-64	PG-TO252-3-11	2N06L64

Maximum ratings, at T_j=25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	T_C=25 °C, V_{GS}=10 V	19	A
		T_C=100 °C, V_{GS}=10 V[1)]	13	
Pulsed drain current[1)]	$I_{D,pulse}$	T_C=25 °C	76	
Avalanche energy, single pulse	E_{AS}	I_D=15A	43	mJ
Gate source voltage	V_{GS}		±20	V
Power dissipation	P_{tot}	T_C=25 °C	47	W
Operating and storage temperature	T_j, T_{stg}		-55 ... +175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	R_{thJC}		-	-	3.2	K/W
Thermal resistance, junction - ambient, leaded	R_{thJA}		-	-	100	
SMD version, device on PCB	R_{thJA}	minimal footprint	-	-	75	
		6 cm^2 cooling area[2)]	-	-	50	
Electrical characteristics, at T_j=25 °C, unless otherwise specified						
Static characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS}=0 V, I_D= 1 mA	55	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=14 µA	1.2	1.6	2.0	
Zero gate voltage drain current	I_{DSS}	V_{DS}=55 V, V_{GS}=0 V, T_j=25 °C	-	0.01	1	µA
		V_{DS}=55 V, V_{GS}=0 V, T_j=125 °C[1)]	-	1	100	
Gate-source leakage current	I_{GSS}	V_{GS}=20 V, V_{DS}=0 V	-	1	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=4.5 V, I_D=13 A	-	61	85	mΩ
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=10 V, I_D=13 A	-	47	64	mΩ

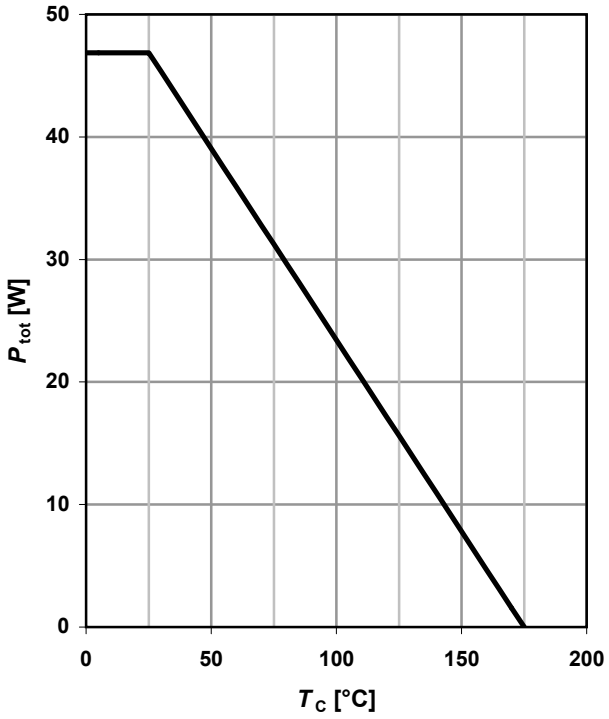
Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Dynamic characteristics[1]						
Input capacitance	C_{iss}	V_{GS}=0 V, V_{DS}=25 V, f=1 MHz	-	354	-	pF
Output capacitance	C_{oss}		-	103	-	
Reverse transfer capacitance	C_{rss}		-	38	-	
Turn-on delay time	$t_{d(on)}$	V_{DD}=30 V, V_{GS}=10 V, I_D=15 A, R_G=20 Ω	-	4	-	ns
Rise time	t_r		-	14	-	
Turn-off delay time	$t_{d(off)}$		-	21	-	
Fall time	t_f		-	12	-	
Gate Charge Characteristics[1]						
Gate to source charge	Q_{gs}	V_{DD}=44 V, I_D=19 A, V_{GS}=0 to 10 V	-	1	1.5	nC
Gate to drain charge	Q_{gd}		-	4	5	
Gate charge total	Q_g		-	11	13	
Gate plateau voltage	$V_{plateau}$		-	3.8	-	V
Reverse Diode						
Diode continous forward current[1]	I_S	T_C=25 °C	-	-	19	A
Diode pulse current[1]	$I_{S,pulse}$		-	-	76	
Diode forward voltage	V_{SD}	V_{GS}=0 V, I_F=15 A, T_j=25 °C	-	0.93	1.3	V
Reverse recovery time[1]	t_{rr}	V_R=30 V, I_F=I_S, di_F/dt=100 A/µs	-	34	-	ns
Reverse recovery charge[1]	Q_{rr}	V_R=30 V, I_F=I_S, di_F/dt=100 A/µs	-	32	-	nC

[1] Defined by design. Not subject to production test.

[2] Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm^2 (one layer, 70 µm thick) copper area for drain connection. PCB is vertical in still air.

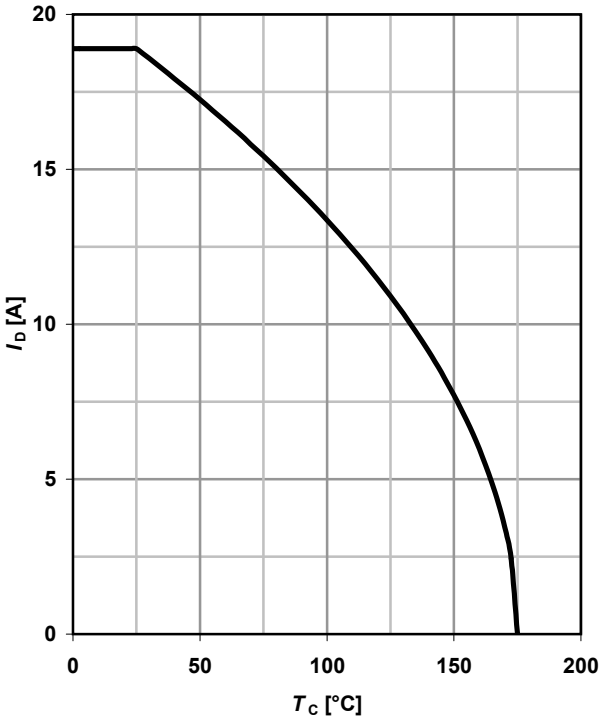
1 Power dissipation

$P_{tot} = f(T_C)$; $V_{GS} \geq 4$ V



2 Drain current

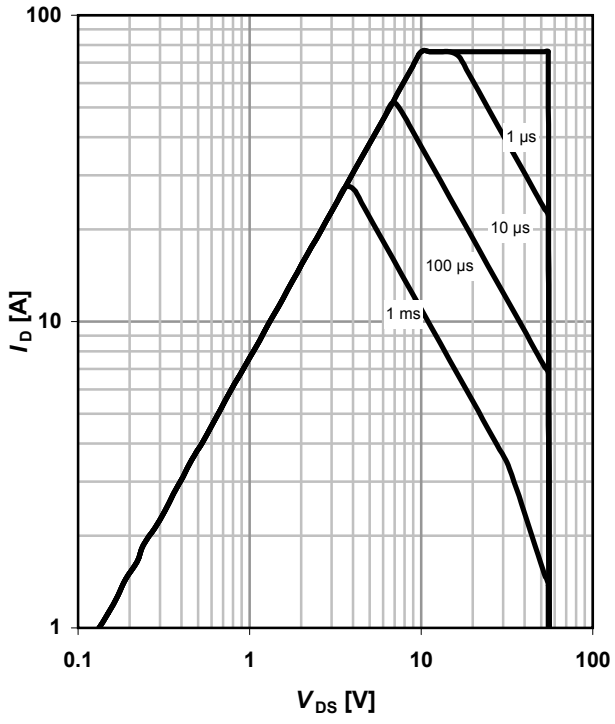
$I_D = f(T_C)$; $V_{GS} \geq 10$ V



3 Safe operating area

$I_D = f(V_{DS})$; $T_C = 25$ °C; $D = 0$

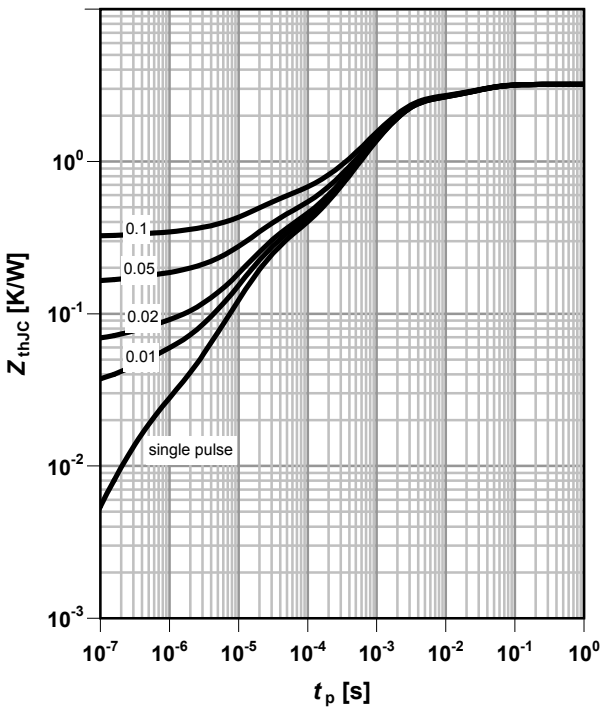
parameter: t_p



4 Max. transient thermal impedance

$Z_{thJC} = f(t_p)$

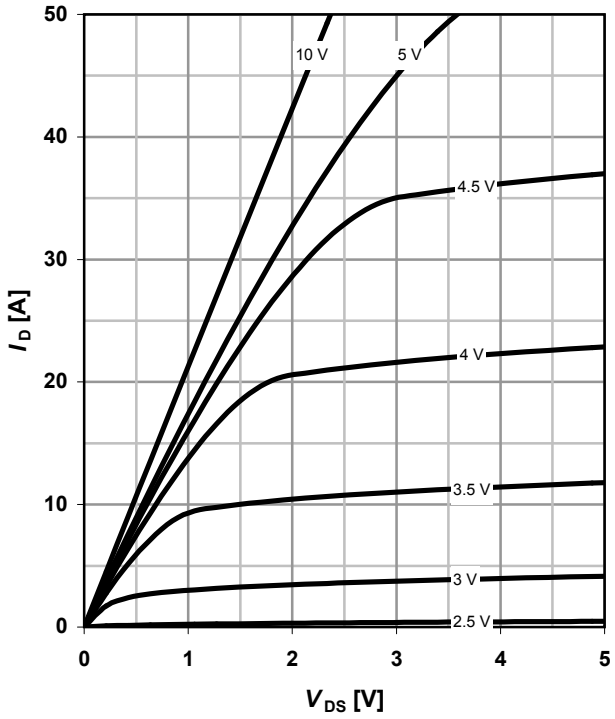
parameter: $D = t_p / T$



5 Typ. output characteristics

$I_D = f(V_{DS})$; $T_j = 25$ °C

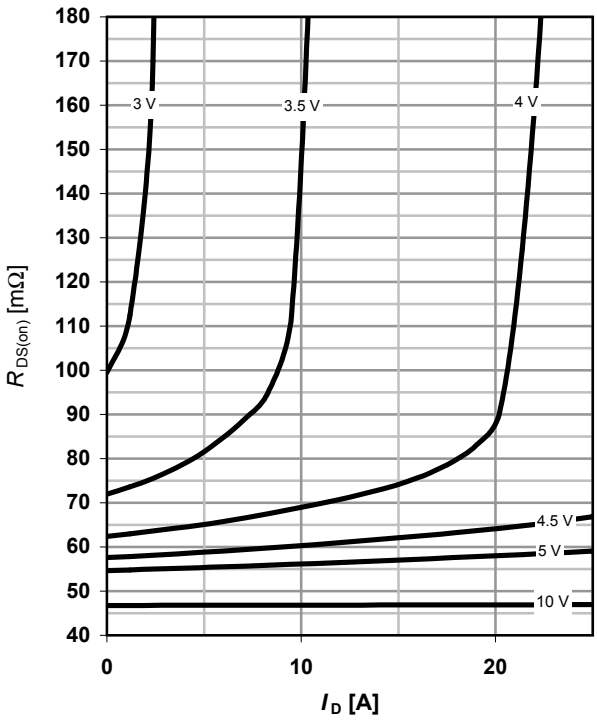
parameter: V_{GS}



6 Typ. drain-source on-state resistance

$R_{DS(on)} = (I_D)$; $T_j = 25$ °C

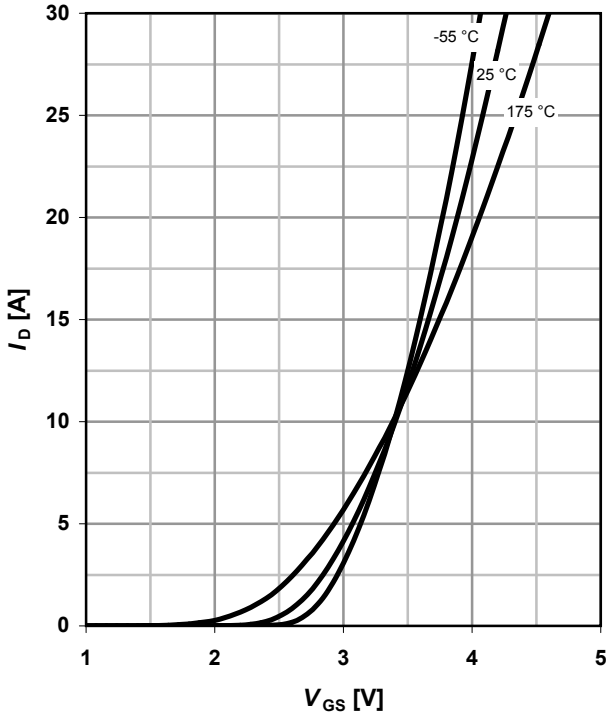
parameter: V_{GS}



7 Typ. transfer characteristics

$I_D = f(V_{GS})$; $V_{DS} = 5$V

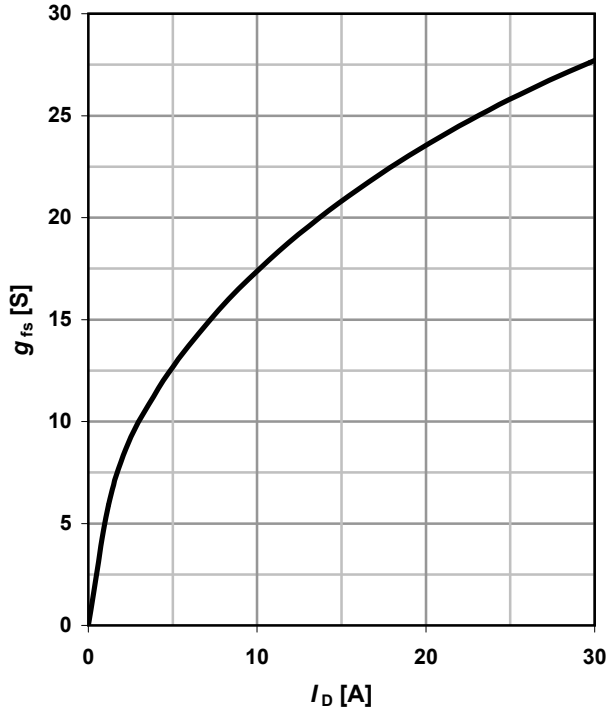
parameter: T_j



8 Typ. Forward transconductance

$g_{fs} = f(I_D)$; $T_j = 25$°C

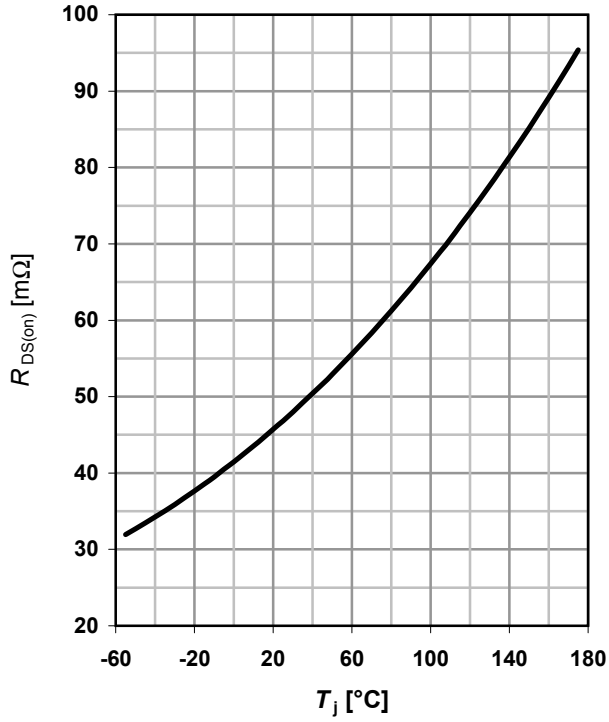
parameter: g_{fs}



9 Typ. Drain-source on-state resistance

$R_{DS(ON)} = f(T_j)$

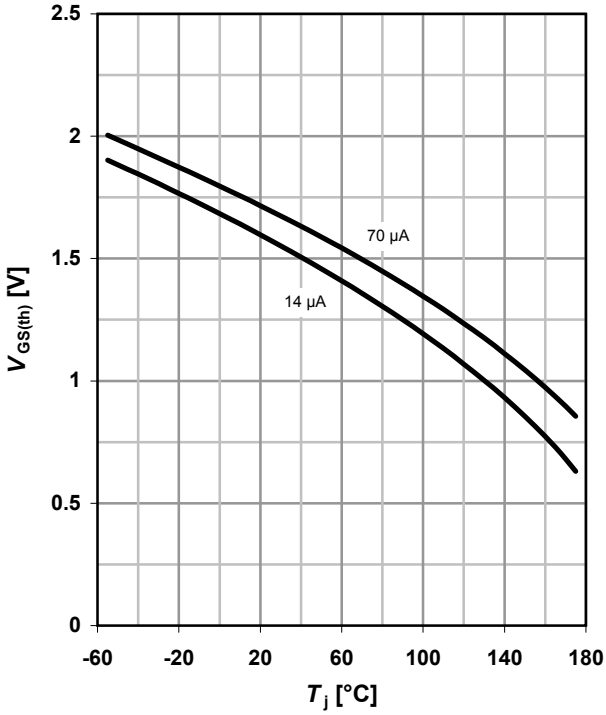
parameter: $I_D = 8$ A; $V_{GS} = 10$ V



10 Typ. gate threshold voltage

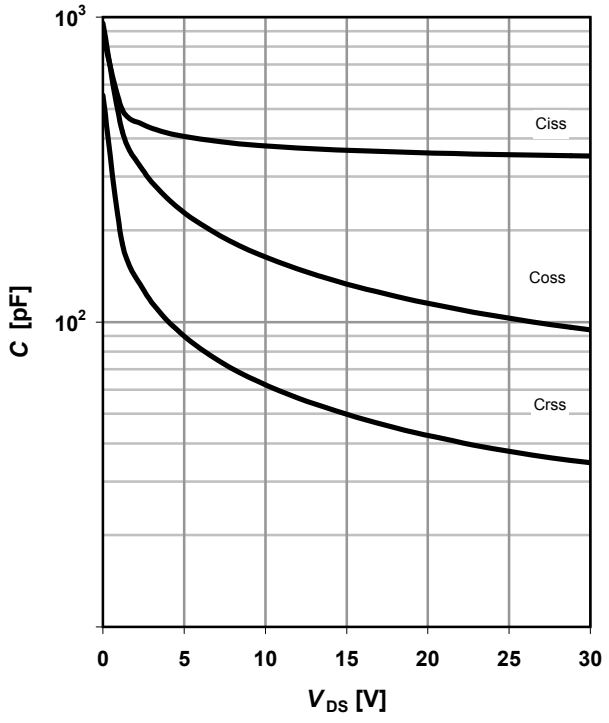
$V_{GS(th)} = f(T_j)$; $V_{GS} = V_{DS}$

parameter: I_D



11 Typ. capacitances

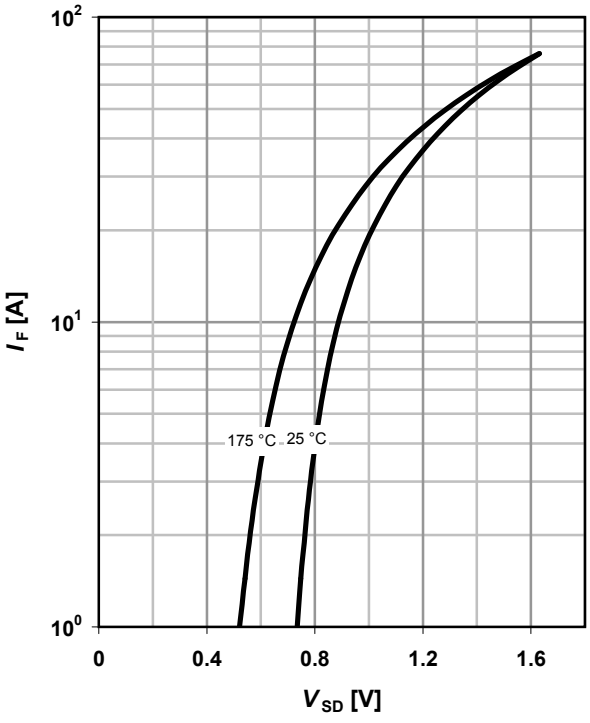
$C = f(V_{DS})$; $V_{GS} = 0$ V; $f = 1$ MHz



12 Typical forward diode characteristicis

IF = f(V_{SD})

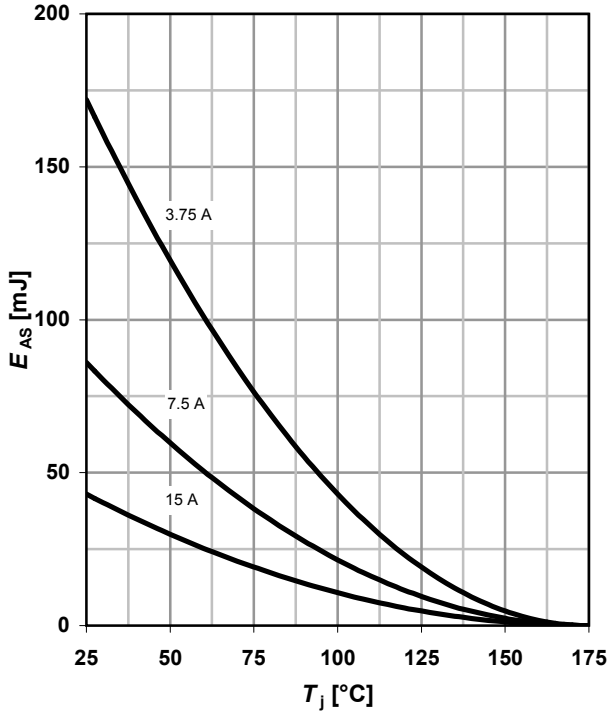
parameter: T_j



13 Typical avalanche energy

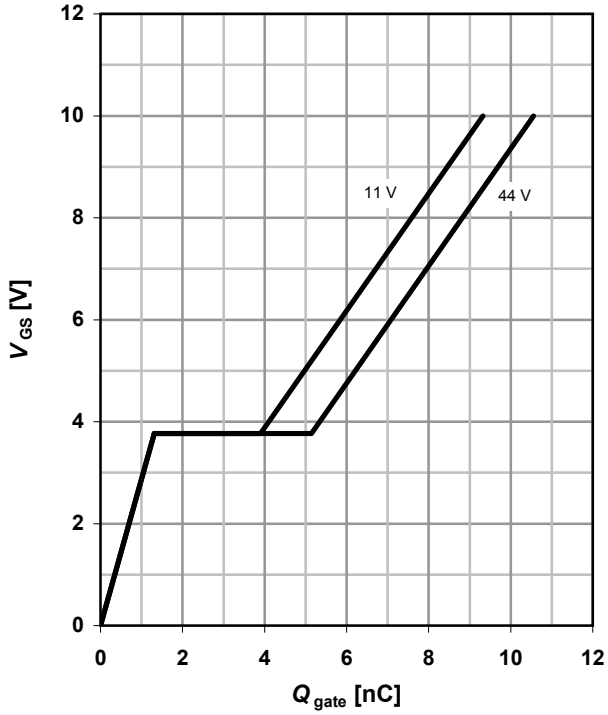
$E_{AS} = f(T_j)$

parameter: I_D



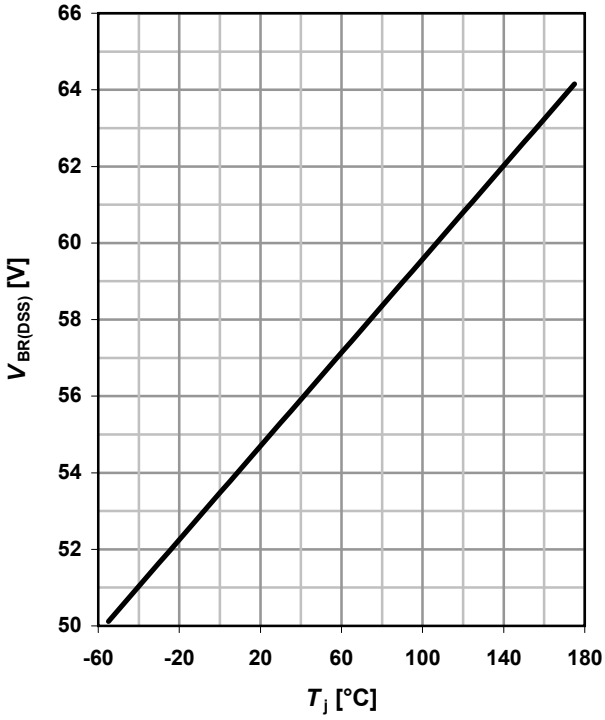
14 Typ. gate charge

$V_{GS} = f(Q_{gate})$; $I_D = 19$ A pulsed

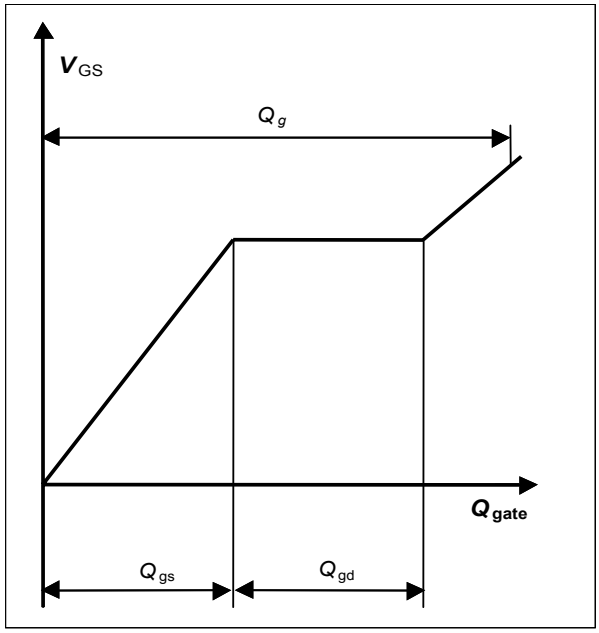


15 Typ. drain-source breakdown voltage

$V_{BR(DSS)} = f(T_j)$; $I_D = 1$ mA



16 Gate charge waveforms



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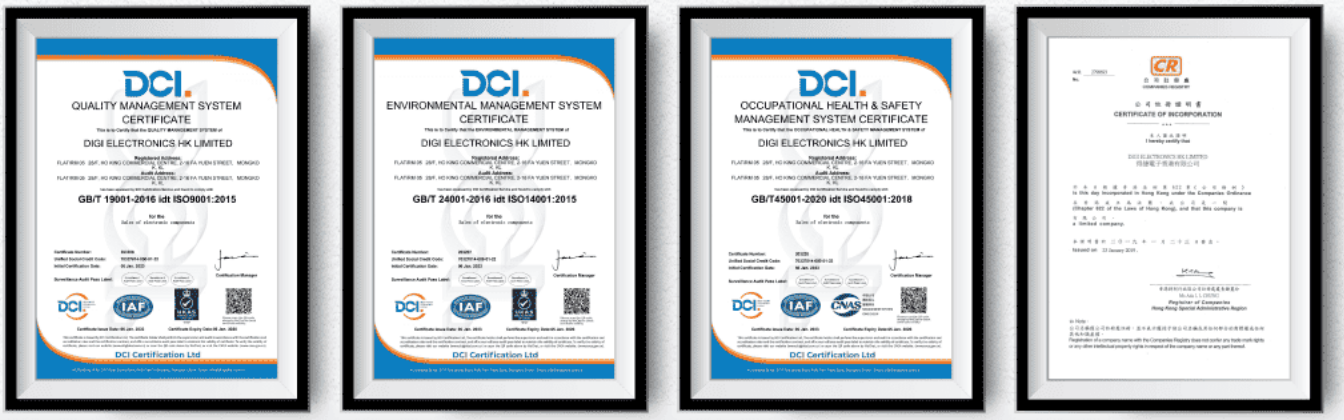

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