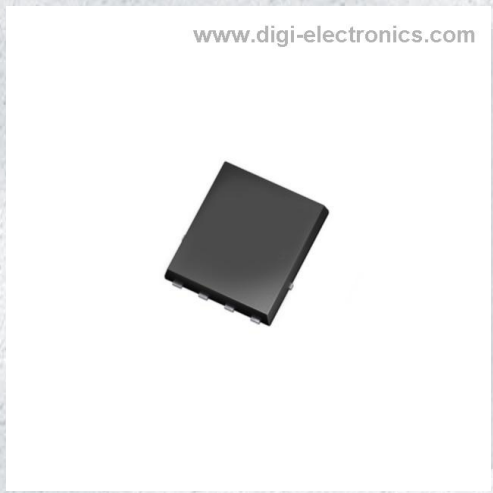
IPLK60R1K0PFD7ATMA1 Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number	IPLK60R1K0PFD7ATMA1-DG
Manufacturer	Infineon Technologies
Manufacturer Product Number	IPLK60R1K0PFD7ATMA1
Description	MOSFET N-CH 600V 5.2A THIN-PAK
Detailed Description	N-Channel 600 V 5.2A (Tc) 31.3W (Tc) Surface Mount PG-TDSON-8-52

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

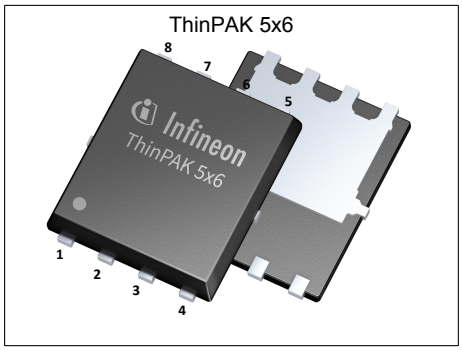
Manufacturer Product Number:	Manufacturer:
IPLK60R1K0PFD7ATMA1	Infineon Technologies
Series:	Product Status:
CoolMOS™ PFD7	Active
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
600 V	5.2A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
10V	1Ohm @ 1A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
4.5V @ 50µA	6 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	230 pF @ 400 V
FET Feature:	Power Dissipation (Max):
-	31.3W (Tc)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
PG-TDSON-8-52	8-PowerTDFN
Base Product Number:	
IPLK60	

Environmental & Export classification

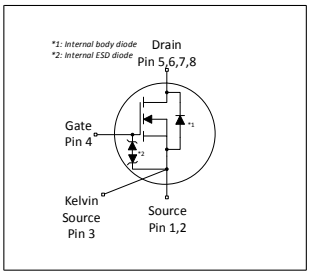
RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	

MOSFET

600V CoolMOS™ PFD7 SJ Power Device



CoolMOS™ is a revolutionary technology for high voltage power MOSFETs, designed according to the superjunction (SJ) principle and pioneered by Infineon Technologies.
The latest CoolMOS™ PFD7 is an optimized platform tailored to target cost sensitive applications in consumer markets such as charger, adapter, motor drive, lighting, etc.
The new series provides all the benefits of a fast switching Superjunction MOSFET, combined with an excellent price/performance ratio and state of the art ease-of-use level. The technology meets highest efficiency standards and supports high power density, enabling customers going towards very slim designs.



Features

- Extremely low losses due to very low FOM $R_{DS(on)}$*Q_g and $R_{DS(on)}$*E_{oss}
- Low switching losses E_{oss}, excellent thermal behavior
- Fast body diode
- Wide range portfolio of $R_{DS(on)}$ and package variations
- Integrated zener diode

Benefits

- Enables high power density designs and small form factors
- Enables efficiency gains at higher switching frequencies
- Excellent commutation ruggedness
- Easy to select right parts and optimize the design
- High ESD ruggedness

Potential applications

Recommended for ZVS topologies used in high density chargers, adapters, lighting and motor drives applications, etc.

Product validation

Fully qualified according to JEDEC for Industrial Applications

Please note: For MOSFET paralleling the use of ferrite beads on the gate or separate totem poles is generally recommended.

Table 1 Key Performance Parameters

Parameter	Value	Unit
V_{DS} @ $T_{j,max}$	650	V
$R_{DS(on),max}$	1000	mΩ
$Q_{g,typ}$	6.0	nC
$I_{D,pulse}$	8.8	A
E_{oss} @ 400V	0.7	µJ
Body diode di_F/dt	1300	A/µs
ESD Class (HBM)	2	-

Type / Ordering Code	Package	Marking	Related Links
IPLK60R1K0PFD7	ThinPAK 5x6 SMD	60R1K0D7	see Appendix A

600V CoolMOS™ PFD7 SJ Power Device
IPLK60R1K0PFD7

Table of Contents

600V CoolMOS™ PFD7 SJ Power Device
IPLK60R1K0PFD7

1 Maximum ratings
at T_j = 25°C, unless otherwise specified

Table 2 Maximum ratings

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Continuous drain current[1]	I_D	- -	- -	5.2 3.3	A	T_C=25°C T_C=100°C
Pulsed drain current[2]	$I_{D,pulse}$	-	-	8.8	A	T_C=25°C
Avalanche energy, single pulse	E_{AS}	-	-	10	mJ	I_D=0.9A; V_{DD}=50V; see table 10
Avalanche energy, repetitive	E_{AR}	-	-	0.05	mJ	I_D=0.9A; V_{DD}=50V; see table 10
Avalanche current, single pulse	I_{AS}	-	-	0.9	A	-
MOSFET dv/dt ruggedness	dv/dt	-	-	120	V/ns	V_{DS}=0...400V
Gate source voltage (static)	V_{GS}	-20	-	20	V	static;
Gate source voltage (dynamic)	V_{GS}	-30	-	30	V	AC (f>1 Hz)
Power dissipation	P_{tot}	-	-	31.3	W	T_C=25°C
Storage temperature	T_{stg}	-55	-	150	°C	-
Operating junction temperature	T_j	-55	-	150	°C	-
Mounting torque	-	-	-	-	Ncm	-
Continuous diode forward current[1]	I_S	-	-	5.2	A	T_C=25°C
Diode pulse current[2]	$I_{S,pulse}$	-	-	8.8	A	T_C=25°C
Reverse diode dv/dt[3]	dv/dt	-	-	70	V/ns	V_{DS}=0...400V, I_{SD}<=3.6A, T_j=25°C see table 8
Maximum diode commutation speed	di_F/dt	-	-	1300	A/µs	V_{DS}=0...400V, I_{SD}<=3.6A, T_j=25°C see table 8
Insulation withstand voltage	V_{ISO}	-	-	n.a.	V	V_{rms}, T_C=25°C, t=1min

[1] Limited by $T_{j,max}$. Maximum Duty Cycle D = 0.50
[2] Pulse width t_p limited by $T_{j,max}$
[3] Identical low side and high side switch with identical R_G

2 Thermal characteristics

Table 3 Thermal characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case	R_{thJC}	-	-	4.0	°C/W	-
Thermal resistance, junction - ambient	R_{thJA}	-	-	80	°C/W	device on PCB, minimal footprint
Thermal resistance, junction - ambient for SMD version	R_{thJA}	-	35	62	°C/W	Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thickness) copper area for drain connection and cooling. PCB is vertical without air stream cooling.
Soldering temperature, wave & reflow soldering allowed	T_{sold}	-	-	260	°C	reflow MSL1

3 Electrical characteristics

at T_j=25°C, unless otherwise specified

Table 4 Static characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Drain-source breakdown voltage	$V_{(BR)DSS}$	600	-	-	V	V_{GS}=0V, I_D=1mA
Gate threshold voltage	$V_{(GS)th}$	3.5	4	4.5	V	V_{DS}=V_{GS}, I_D=0.05mA
Zero gate voltage drain current[1]	I_{DSS}	- -	- 1	1 37	µA	V_{DS}=600V, V_{GS}=0V, T_j=25°C V_{DS}=600V, V_{GS}=0V, T_j=125°C
Gate-source leakage current	I_{GSS}	-	-	1000	nA	V_{GS}=20V, V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	- -	0.840 1.975	1.000 -	Ω	V_{GS}=10V, I_D=1.0A, T_j=25°C V_{GS}=10V, I_D=1.0A, T_j=150°C
Gate resistance	R_G	-	11.0	-	Ω	f=1MHz, open drain

Table 5 Dynamic characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Input capacitance	C_{iss}	-	230	-	pF	V_{GS}=0V, V_{DS}=400V, f=250kHz
Output capacitance	C_{oss}	-	6	-	pF	V_{GS}=0V, V_{DS}=400V, f=250kHz
Effective output capacitance, energy related[2]	$C_{o(er)}$	-	9	-	pF	V_{GS}=0V, V_{DS}=0...400V
Effective output capacitance, time related[3]	$C_{o(tr)}$	-	80	-	pF	I_D=constant, V_{GS}=0V, V_{DS}=0...400V
Turn-on delay time	$t_{d(on)}$	-	7.7	-	ns	V_{DD}=400V, V_{GS}=10V, I_D=1.0A, R_G=10.2Ω; see table 9
Rise time	t_r	-	9	-	ns	V_{DD}=400V, V_{GS}=10V, I_D=1.0A, R_G=10.2Ω; see table 9
Turn-off delay time	$t_{d(off)}$	-	42	-	ns	V_{DD}=400V, V_{GS}=10V, I_D=1.0A, R_G=10.2Ω; see table 9
Fall time	t_f	-	50	-	ns	V_{DD}=400V, V_{GS}=10V, I_D=1.0A, R_G=10.2Ω; see table 9

Table 6 Gate charge characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Gate to source charge	Q_{gs}	-	1.3	-	nC	V_{DD}=400V, I_D=1.0A, V_{GS}=0 to 10V
Gate to drain charge	Q_{gd}	-	2.2	-	nC	V_{DD}=400V, I_D=1.0A, V_{GS}=0 to 10V
Gate charge total	Q_g	-	6.0	-	nC	V_{DD}=400V, I_D=1.0A, V_{GS}=0 to 10V
Gate plateau voltage	$V_{plateau}$	-	5.6	-	V	V_{DD}=400V, I_D=1.0A, V_{GS}=0 to 10V

[1] Maximum specification is defined by calculated six sigma upper confidence bound
[2] $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V
[3] $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 400V

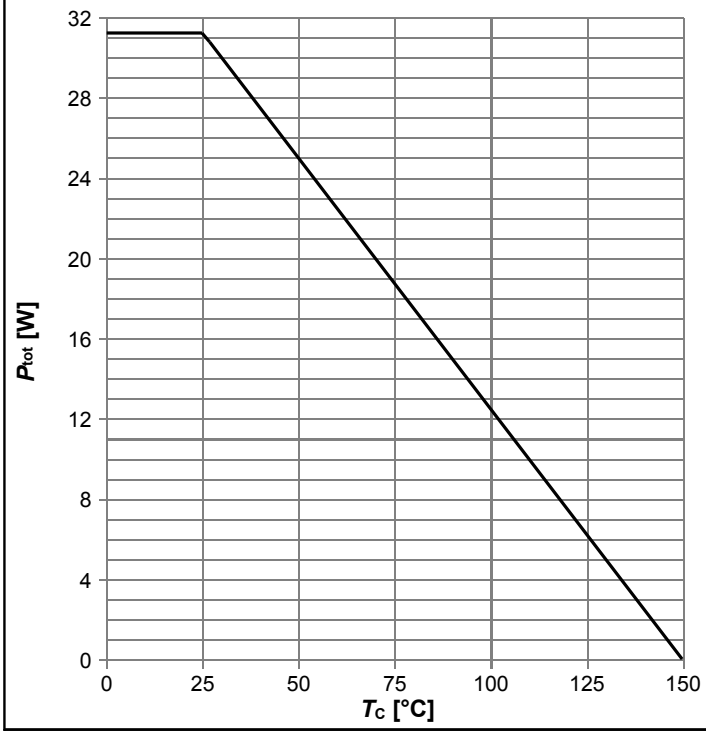
Table 7 Reverse diode characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Diode forward voltage	V_{SD}	-	1.0	-	V	V_{GS}=0V, I_F=1.0A, T_j=25°C
Reverse recovery time	t_{rr}	-	44	66	ns	V_R=400V, I_F=1.0A, di_F/dt=100A/µs; see table 8
Reverse recovery charge	Q_{rr}	-	0.08	0.16	µC	V_R=400V, I_F=1.0A, di_F/dt=100A/µs; see table 8
Peak reverse recovery current	I_{rrm}	-	3.2	-	A	V_R=400V, I_F=1.0A, di_F/dt=100A/µs; see table 8

600V CoolMOS™ PFD7 SJ Power Device
IPLK60R1K0PFD7

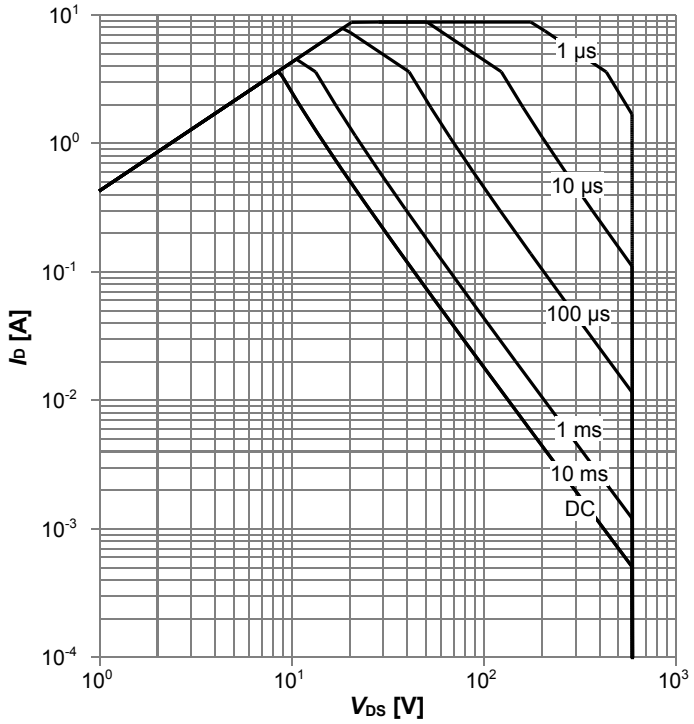
4 Electrical characteristics diagrams

Diagram 1: Power dissipation



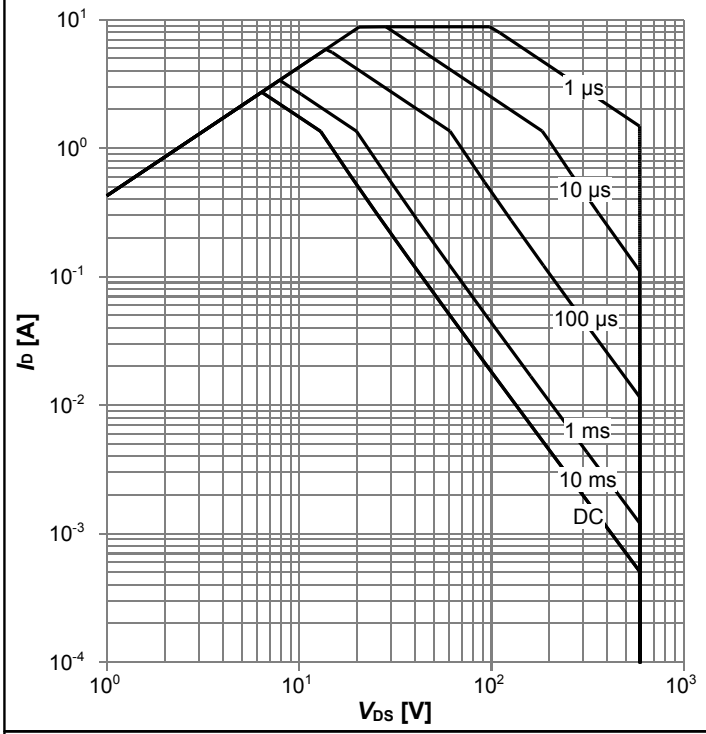
P_{tot}=f(T_C)

Diagram 2: Safe operating area



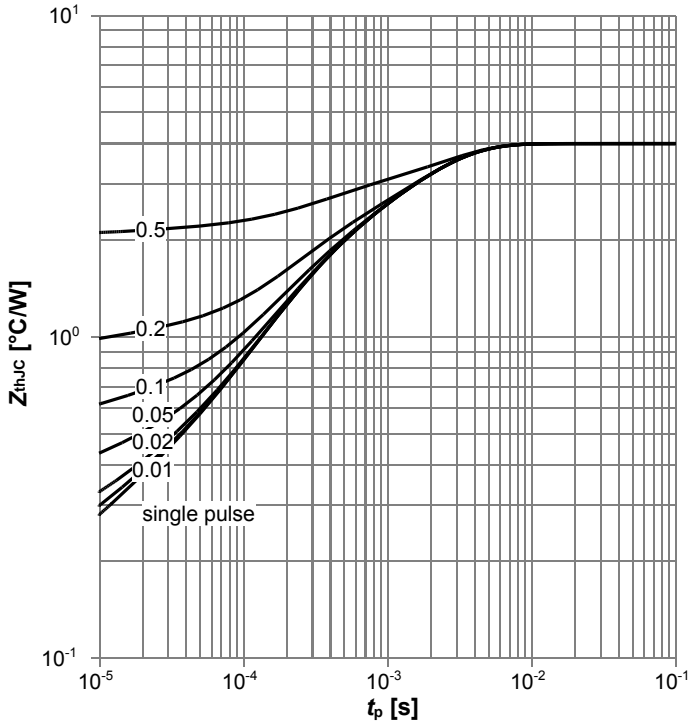
I_D=f(V_{DS}); T_C=25 °C; D=0; parameter: t_p

Diagram 3: Safe operating area



I_D=f(V_{DS}); T_C=80 °C; D=0; parameter: t_p

Diagram 4: Max. transient thermal impedance



Z_{thJC} =f(t_P); parameter: D=t_p/T

600V CoolMOS™ PFD7 SJ Power Device
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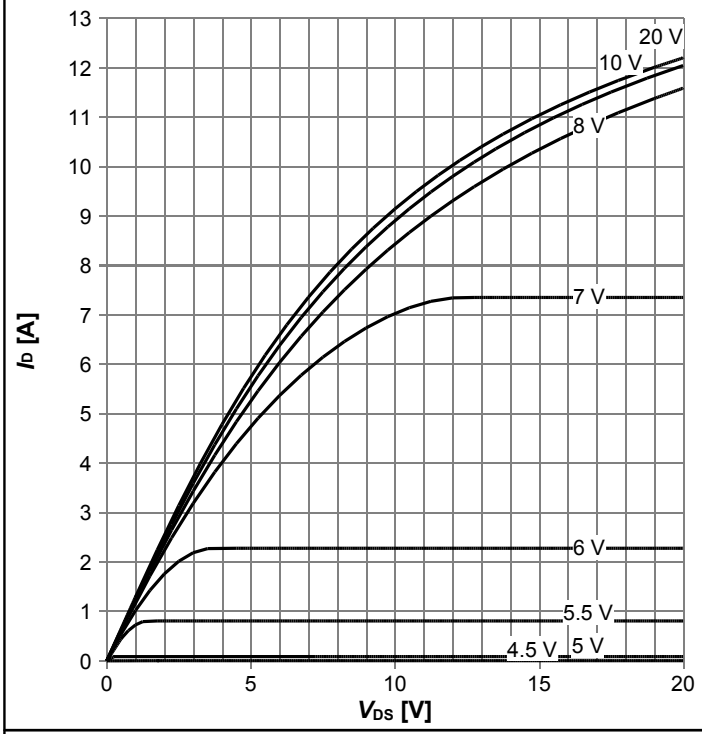
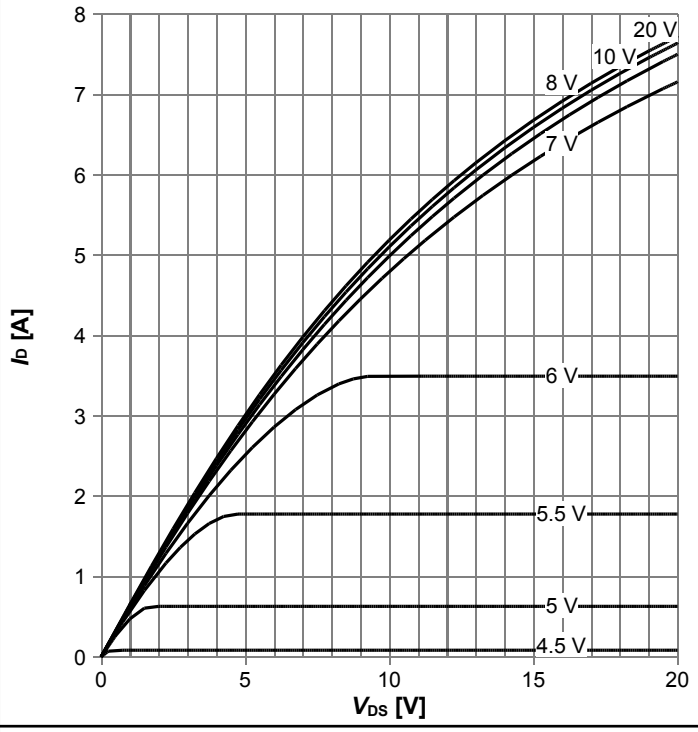
Diagram 5: Typ. output characteristics



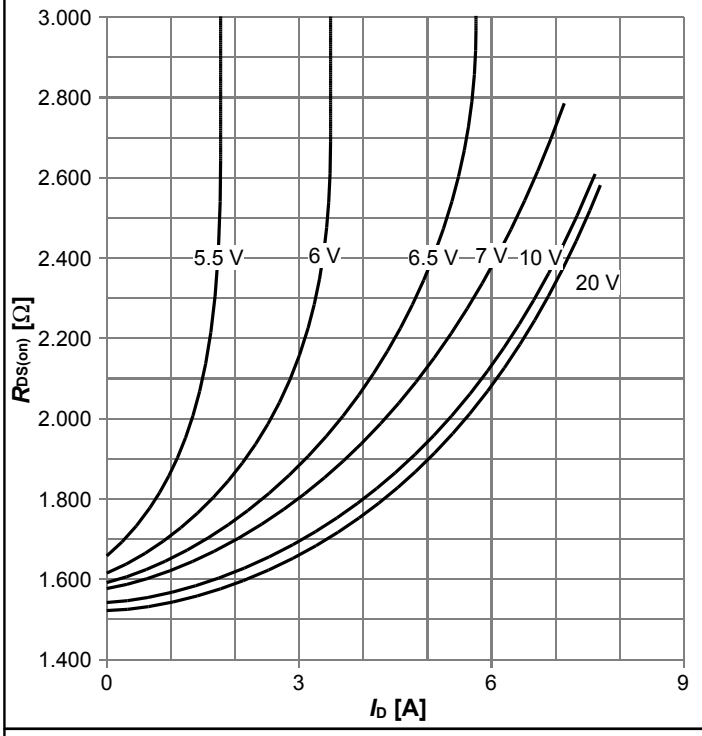
$I_D=f(V_{DS})$; $T_j=25$ °C; parameter: V_{GS}

Diagram 6: Typ. output characteristics



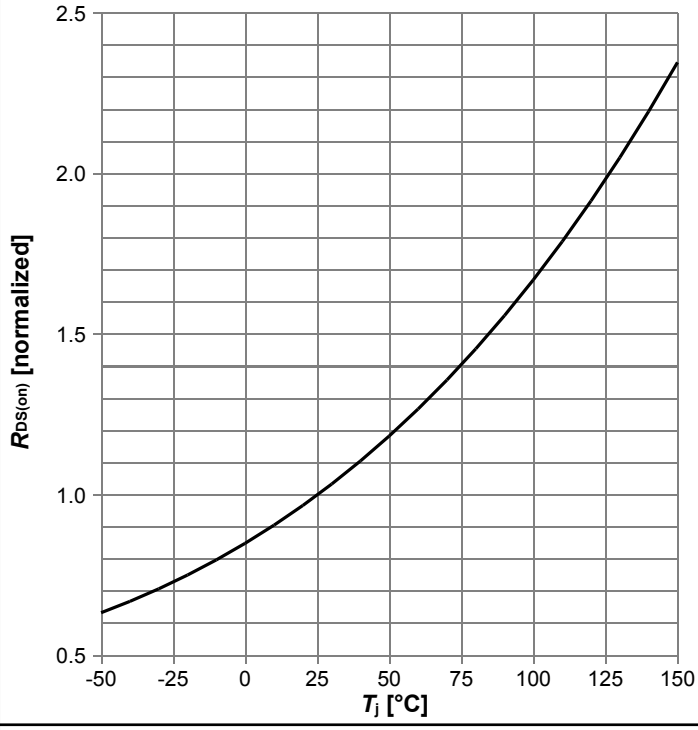
$I_D=f(V_{DS})$; $T_j=125$ °C; parameter: V_{GS}

Diagram 7: Typ. drain-source on-state resistance



$R_{DS(on)}=f(I_D)$; $T_j=125$ °C; parameter: V_{GS}

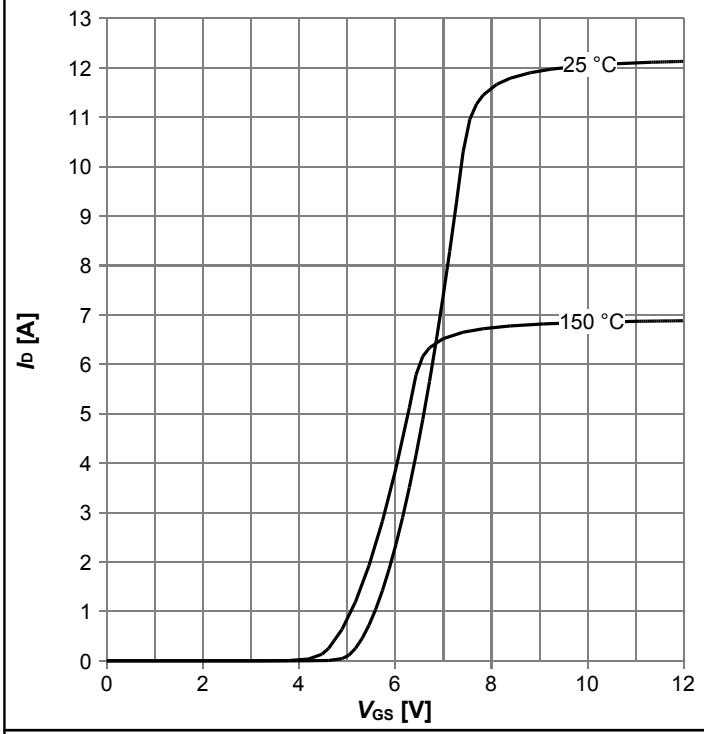
Diagram 8: Drain-source on-state resistance



$R_{DS(on)}=f(T_j)$; $I_D=1.0$ A; $V_{GS}=10$ V

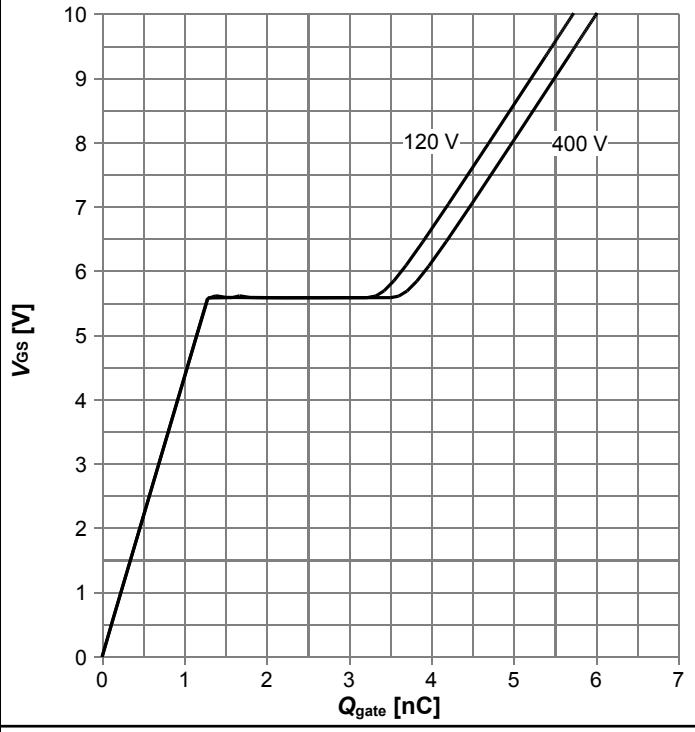
600V CoolMOS™ PFD7 SJ Power Device
IPLK60R1K0PFD7

Diagram 9: Typ. transfer characteristics



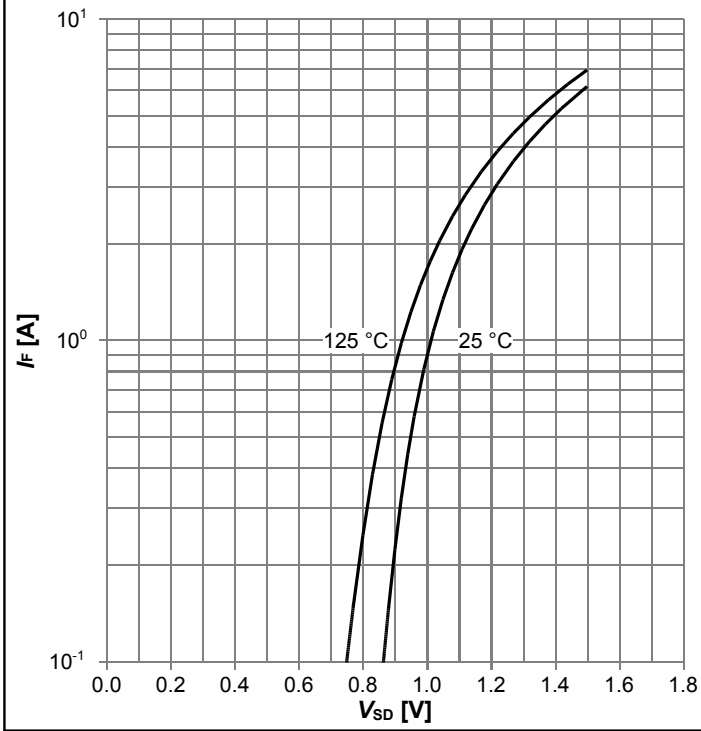
I_D=f(V_{GS}); V_{DS}=20V; parameter: T_j

Diagram 10: Typ. gate charge



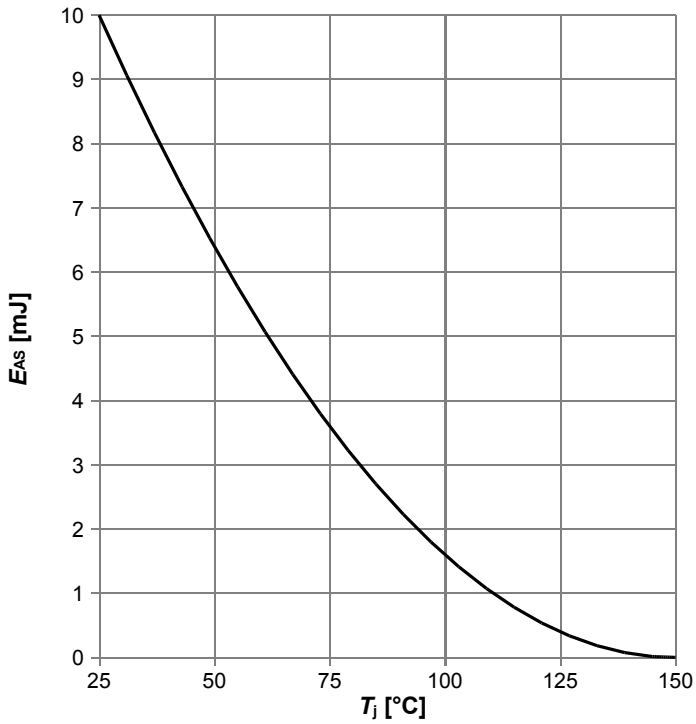
V_{GS}=f(Q_{gate}); I_D=1.0 A pulsed; parameter: V_{DD}

Diagram 11: Forward characteristics of reverse diode



I_F=f(V_{SD}); parameter: T_j

Diagram 12: Avalanche energy



E_{AS}=f(T_j); I_D=0.9 A; V_{DD}=50 V

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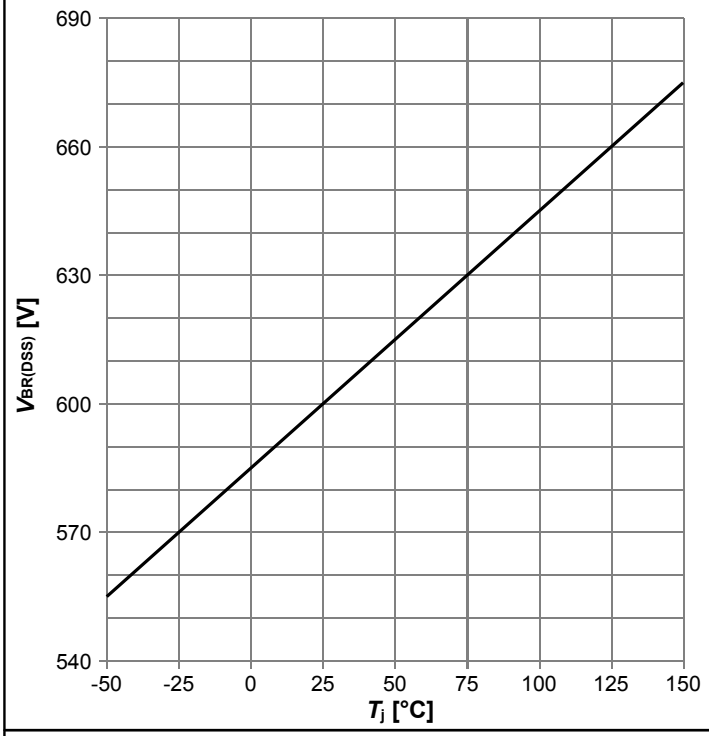
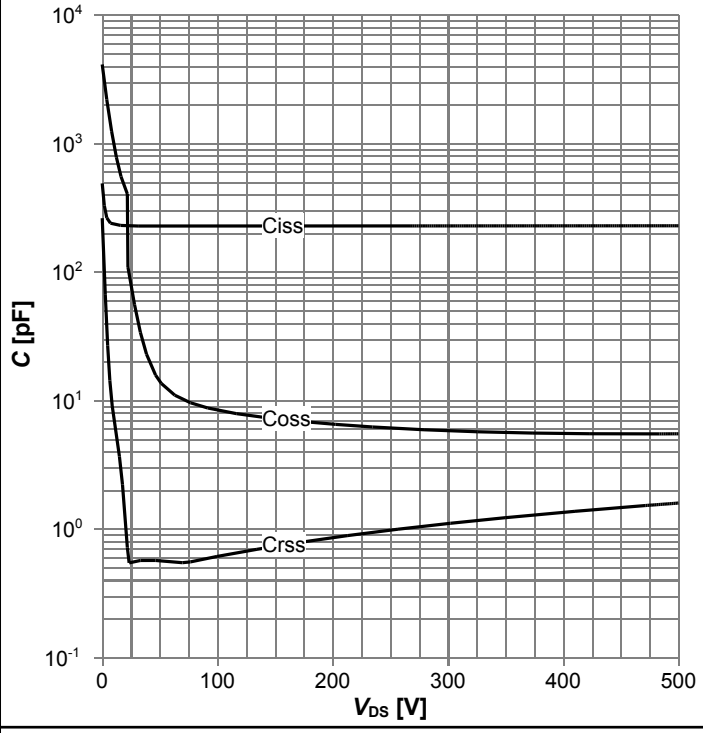
IPLK60R1K0PFD7

Diagram 13: Drain-source breakdown voltage



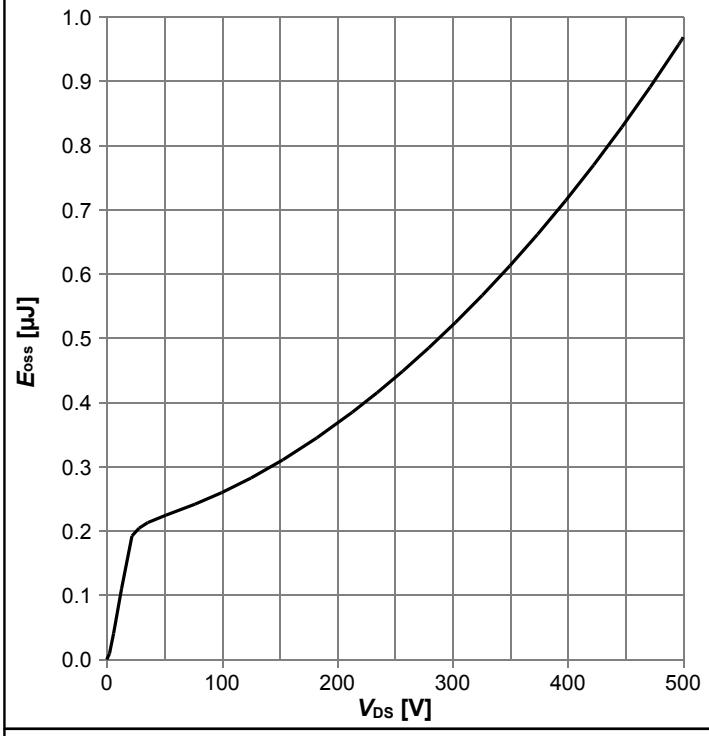
$V_{BR(DSS)}$=f(T_j); I_D=1 mA

Diagram 14: Typ. capacitances



C=f(V_{DS}); V_{GS}=0 V; f=250 kHz

Diagram 15: Typ. Coss stored energy



E_{oss}=f(V_{DS})

5 Test Circuits

Table 8 Diode characteristics

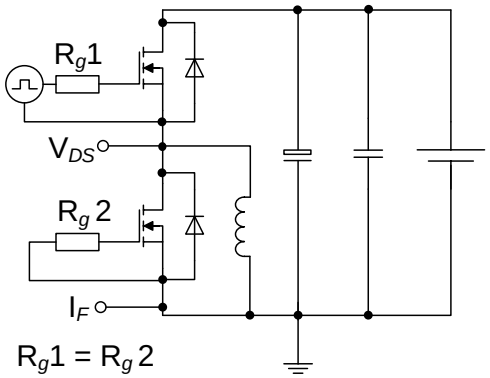
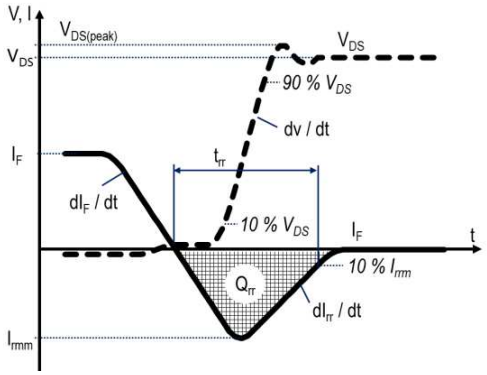
Test circuit for diode characteristics	Diode recovery waveform
	

Table 9 Switching times (ss)

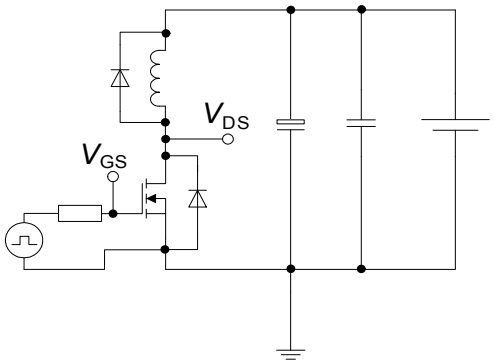
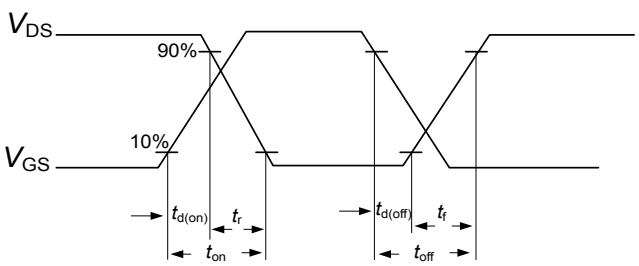
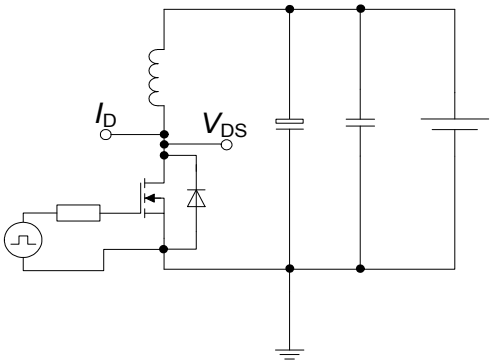
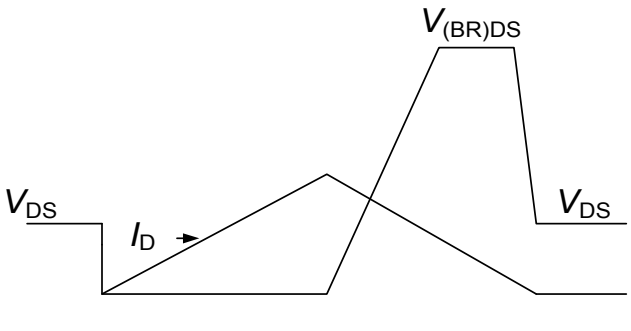
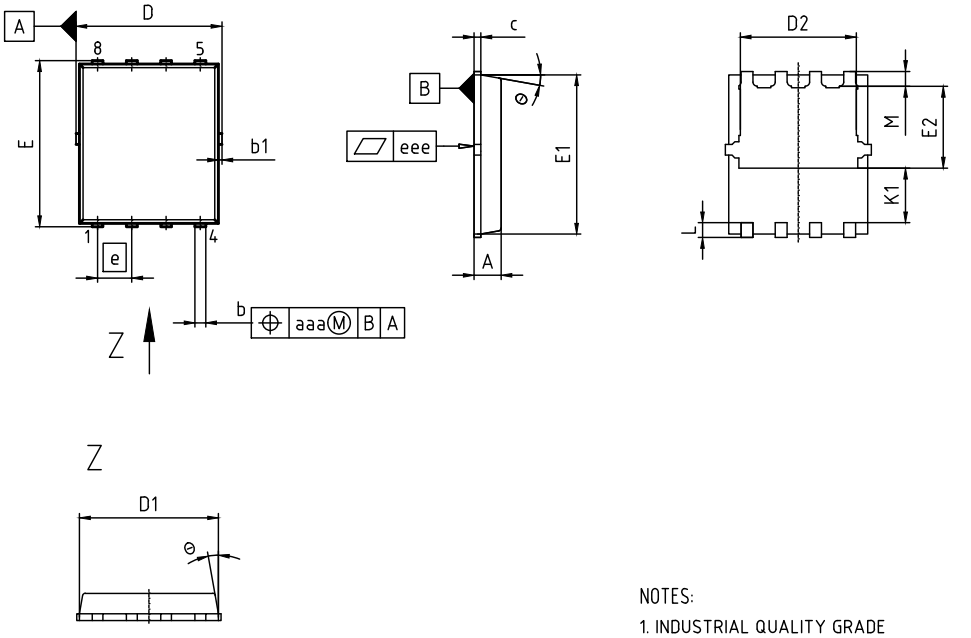
Switching times test circuit for inductive load	Switching times waveform
	

Table 10 Unclamped inductive load (ss)

Unclamped inductive load test circuit	Unclamped inductive waveform
	

6 Package Outlines



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
b	0.34	0.54	0.013	0.021
b1	0.03	0.23	0.001	0.009
c	0.15	0.35	0.006	0.014
D	5.15	5.51	0.203	0.217
D1	4.95	5.35	0.195	0.211
D2	4.20	4.40	0.165	0.173
E	5.95	6.35	0.234	0.250
E1	5.70	6.10	0.224	0.240
E2	2.93	3.13	0.115	0.123
e	1.27		0.050	
N	8		8	
K1	1.92	2.12	0.076	0.083
L	0.45	0.65	0.018	0.026
M	0.45	0.65	0.018	0.026
ϴ	8.5°	12°	8.5°	12°
aaa	0.25		0.010	
eee	0.08		0.003	

DOCUMENT NO.
Z8B00181453

SCALE
0 2.5 5mm

EUROPEAN PROJECTION

ISSUE DATE
13-05-2016

REVISION
01

Figure 1 Outline ThinPAK 5x6 SMD, dimensions in mm/inches

7 Appendix A

Table 11 Related Links

- **IFX CoolMOS PFD7 Webpage:** www.infineon.com
- **IFX CoolMOS PFD7 application note:** www.infineon.com
- **IFX CoolMOS PFD7 simulation model:** www.infineon.com
- **IFX Design tools:** www.infineon.com

Revision History

IPLK60R1K0PFD7

Revision: 2020-01-22, Rev. 2.0

Previous Revision		
Revision	Date	Subjects (major changes since last revision)
2.0	2020-01-22	Release of final version

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