

IRF7317PBF Datasheet



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DiGi Electronics Part Number	IRF7317PBF-DG
Manufacturer	Infineon Technologies
Manufacturer Product Number	IRF7317PBF
Description	MOSFET N/P-CH 20V 6.6A/5.3A 8SO
Detailed Description	Mosfet Array 20V 6.6A, 5.3A 2W Surface Mount 8-SO



Tel: +00 852-30501935

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Purchase and inquiry

Manufacturer Product Number:

IRF7317PBF

Series:

HEXFET®

Technology:

MOSFET (Metal Oxide)

FET Feature:

Logic Level Gate

Current - Continuous Drain (Id) @ 25°C:

6.6A, 5.3A

Vgs(th) (Max) @ Id:

700mV @ 250µA

Input Capacitance (Ciss) (Max) @ Vds:

900pF @ 15V

Operating Temperature:

-55°C ~ 150°C (Tj)

Package / Case:

8-SOIC (0.154", 3.90mm Width)

Base Product Number:

IRF731

Manufacturer:

Infineon Technologies

Product Status:

Discontinued at Digi-Key

Configuration:

N and P-Channel

Drain to Source Voltage (Vdss):

20V

Rds On (Max) @ Id, Vgs:

29mOhm @ 6A, 4.5V

Gate Charge (Qg) (Max) @ Vgs:

27nC @ 4.5V

Power - Max:

2W

Mounting Type:

Surface Mount

Supplier Device Package:

8-SO

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.29.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

IRF7317PbF

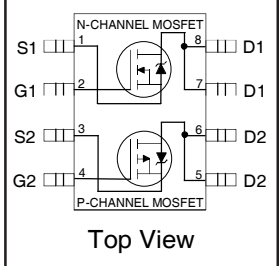
HEXFET® Power MOSFET

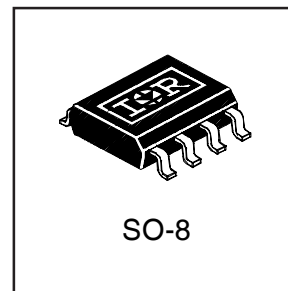
- Generation V Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- Fully Avalanche Rated
- Lead-Free

Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques.

 <p>Top View</p>		N-Ch	P-Ch
	V_{DS}	20V	-20V
$R_{DS(on)}$	0.029 Ω	0.058 Ω	



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

	Symbol	Maximum		Units
		N-Channel	P-Channel	
Drain-Source Voltage	V_{DS}	20	-20	
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current ^①	$T_A = 25^\circ\text{C}$ $T_A = 70^\circ\text{C}$	6.6	-5.3	A
		5.3	-4.3	
Pulsed Drain Current	I_{DM}	26	-21	
Continuous Source Current (Diode Conduction)	I_S	2.5	-2.5	
Maximum Power Dissipation ^②	$T_A = 25^\circ\text{C}$ $T_A = 70^\circ\text{C}$	2.0		W
		1.3		
Single Pulse Avalanche Energy	E_{AS}	100	150	mJ
Avalanche Current	I_{AR}	4.1	-2.9	A
Repetitive Avalanche Energy	E_{AR}	0.20		mJ
Peak Diode Recovery dv/dt ^③	dv/dt	5.0	-5.0	V/ ns
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to + 150 °C		

Thermal Resistance Ratings

Parameter	Symbol	Limit	Units
Maximum Junction-to-Ambient ^④	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

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International
IR RectifierElectrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Parameter	Parameter		Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	N-Ch	20	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
		P-Ch	-20	—	—		$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	N-Ch	—	0.027	—	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}, I_D = 1\text{mA}$
		P-Ch	—	0.031	—		Reference to $25^\circ\text{C}, I_D = -1\text{mA}$
$R_{DS(ON)}$	Static Drain-to-Source On-Resistance	N-Ch	—	0.023	0.029	Ω	$V_{GS} = 4.5V, I_D = 6.0A$ ④
			—	0.030	0.046		$V_{GS} = 2.7V, I_D = 5.2A$ ④
		P-Ch	—	0.049	0.058		$V_{GS} = -4.5V, I_D = -2.9A$ ④
			—	0.082	0.098		$V_{GS} = -2.7V, I_D = -1.5A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	N-Ch	0.7	—	—	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
		P-Ch	-0.7	—	—		$V_{DS} = V_{GS}, I_D = -250\mu A$
g_{fs}	Forward Transconductance	N-Ch	—	20	—	S	$V_{DS} = 10V, I_D = 6.0A$ ④
		P-Ch	—	5.9	—		$V_{DS} = -10V, I_D = -1.5A$ ④
I_{DSS}	Drain-to-Source Leakage Current	N-Ch	—	—	1.0	μA	$V_{DS} = 16V, V_{GS} = 0V$
		P-Ch	—	—	-1.0		$V_{DS} = -16V, V_{GS} = 0V$
		N-Ch	—	—	5.0		$V_{DS} = 16V, V_{GS} = 0V, T_J = 55^\circ\text{C}$
		P-Ch	—	—	-25		$V_{DS} = -16V, V_{GS} = 0V, T_J = 55^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	N-P	—	—	± 100	nA	$V_{GS} = \pm 12V$
Q_g	Total Gate Charge	N-Ch	—	18	27	nC	N-Channel $I_D = 6.0A, V_{DS} = 10V, V_{GS} = 4.5V$ ④
		P-Ch	—	19	29		
Q_{gs}	Gate-to-Source Charge	N-Ch	—	2.2	3.3	nC	P-Channel $I_D = -2.9A, V_{DS} = -16V, V_{GS} = -4.5V$
		P-Ch	—	4.0	6.1		
Q_{gd}	Gate-to-Drain ("Miller") Charge	N-Ch	—	6.2	9.3	nC	N-Channel $V_{DD} = 10V, I_D = 1.0A, R_G = 6.0\Omega, R_D = 10\Omega$ ④
		P-Ch	—	7.7	12		
$t_{d(on)}$	Turn-On Delay Time	N-Ch	—	8.1	12	ns	P-Channel $V_{DD} = -10V, I_D = -2.9A, R_G = 6.0\Omega, R_D = 3.4\Omega$ ④
		P-Ch	—	15	22		
t_r	Rise Time	N-Ch	—	17	25	ns	N-Channel $V_{DD} = 10V, I_D = 1.0A, R_G = 6.0\Omega, R_D = 10\Omega$ ④
		P-Ch	—	40	60		
$t_{d(off)}$	Turn-Off Delay Time	N-Ch	—	38	57	ns	P-Channel $V_{DD} = -10V, I_D = -2.9A, R_G = 6.0\Omega, R_D = 3.4\Omega$ ④
		P-Ch	—	42	63		
t_f	Fall Time	N-Ch	—	31	47	ns	N-Channel $V_{DD} = 10V, I_D = 1.0A, R_G = 6.0\Omega, R_D = 10\Omega$ ④
		P-Ch	—	49	73		
C_{iss}	Input Capacitance	N-Ch	—	900	—	pF	N-Channel $V_{GS} = 0V, V_{DS} = 15V, f = 1.0\text{MHz}$
		P-Ch	—	780	—		
C_{oss}	Output Capacitance	N-Ch	—	430	—	pF	P-Channel $V_{GS} = 0V, V_{DS} = -15V, f = 1.0\text{MHz}$
		P-Ch	—	470	—		
C_{rss}	Reverse Transfer Capacitance	N-Ch	—	200	—	pF	N-Channel $V_{GS} = 0V, V_{DS} = 15V, f = 1.0\text{MHz}$
		P-Ch	—	240	—		

Source-Drain Ratings and Characteristics

Parameter	Parameter		Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	N-Ch	—	—	2.5	A	
		P-Ch	—	—	-2.5		
I_{SM}	Pulsed Source Current (Body Diode) ①	N-Ch	—	—	26	A	
		P-Ch	—	—	-21		
V_{SD}	Diode Forward Voltage	N-Ch	—	0.72	1.0	V	$T_J = 25^\circ\text{C}, I_S = 1.7A, V_{GS} = 0V$ ③
		P-Ch	—	-0.78	-1.0		$T_J = 25^\circ\text{C}, I_S = -2.9A, V_{GS} = 0V$ ③
t_{rr}	Reverse Recovery Time	N-Ch	—	52	77	ns	N-Channel $T_J = 25^\circ\text{C}, I_F = 1.7A, di/dt = 100A/\mu s$ ④
		P-Ch	—	47	71		
Q_{rr}	Reverse Recovery Charge	N-Ch	—	58	86	nC	P-Channel $T_J = 25^\circ\text{C}, I_F = -2.9A, di/dt = 100A/\mu s$ ④
		P-Ch	—	49	73		

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 22)
- ② N-Channel $I_{SD} \leq 4.1A, di/dt \leq 92A/\mu s, V_{DD} \leq V_{(BR)DSS}, T_J \leq 150^\circ\text{C}$
P-Channel $I_{SD} \leq -2.9A, di/dt \leq -77A/\mu s, V_{DD} \leq V_{(BR)DSS}, T_J \leq 150^\circ\text{C}$
- ③ N-Channel Starting $T_J = 25^\circ\text{C}, L = 12\text{mH}, R_G = 25\Omega, I_{AS} = 4.1A$. (See Figure 12)
P-Channel Starting $T_J = 25^\circ\text{C}, L = 35\text{mH}, R_G = 25\Omega, I_{AS} = -2.9A$.
- ④ Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.
- ⑤ Surface mounted on FR-4 board, $t \leq 10\text{sec}$.

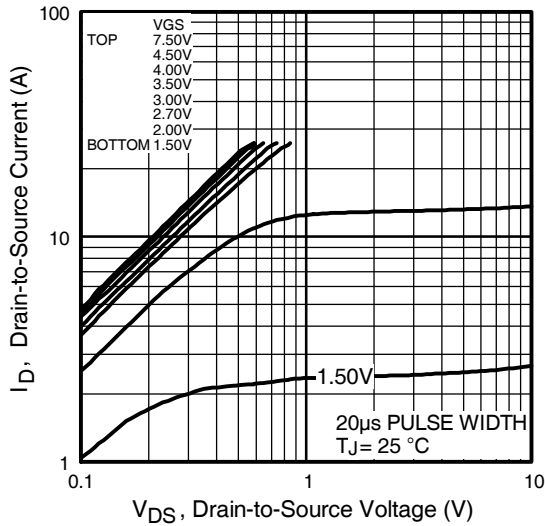


Fig 1. Typical Output Characteristics

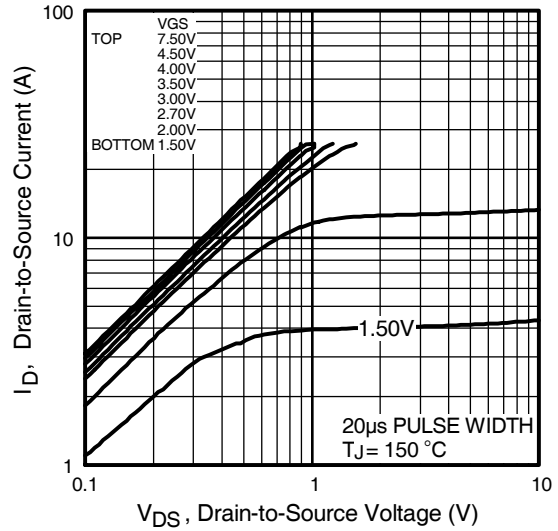


Fig 2. Typical Output Characteristics

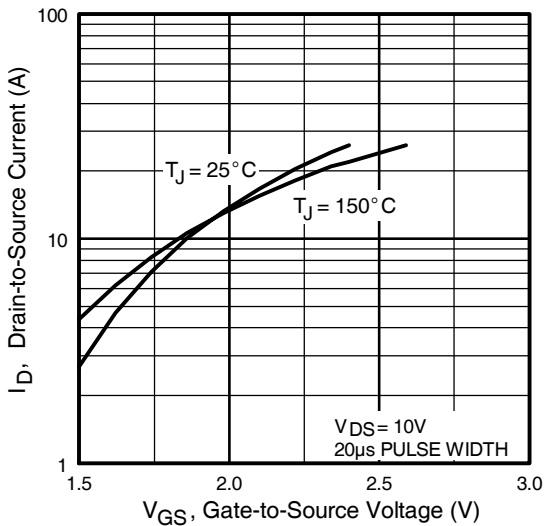


Fig 3. Typical Transfer Characteristics

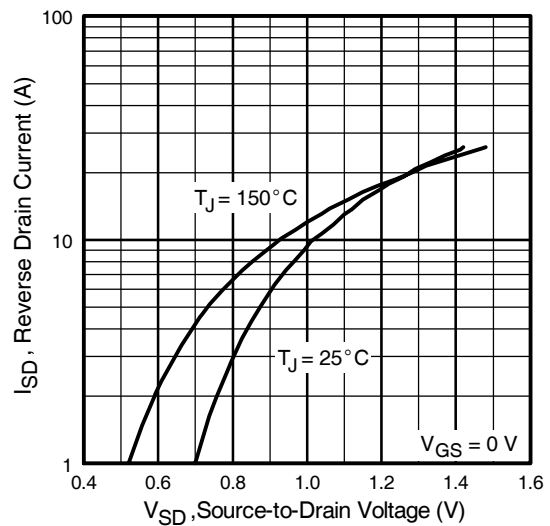


Fig 4. Typical Source-Drain Diode Forward Voltage

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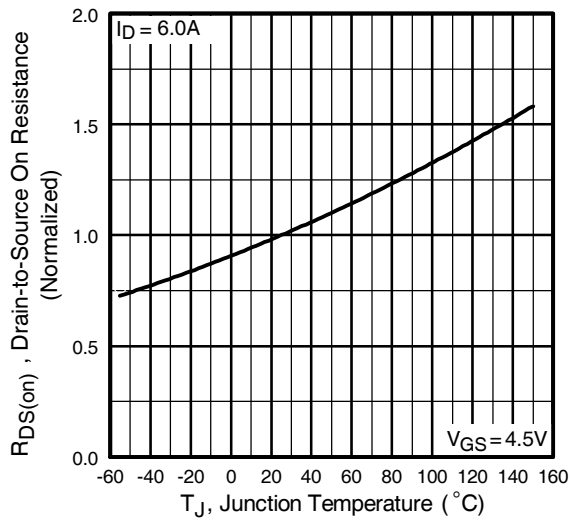


Fig 5. Normalized On-Resistance Vs. Temperature

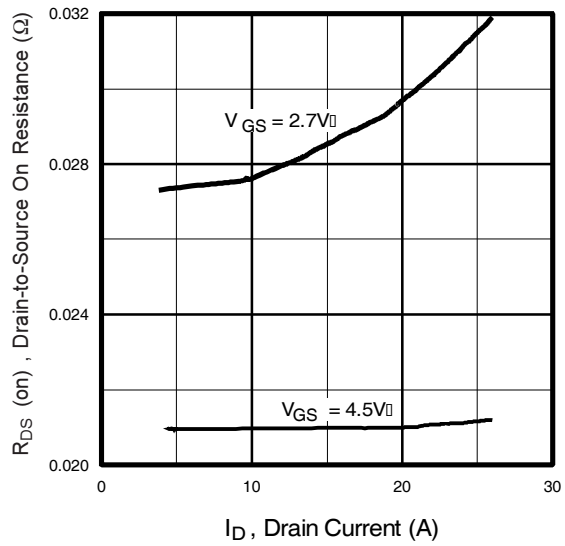


Fig 6. Typical On-Resistance Vs. Drain Current

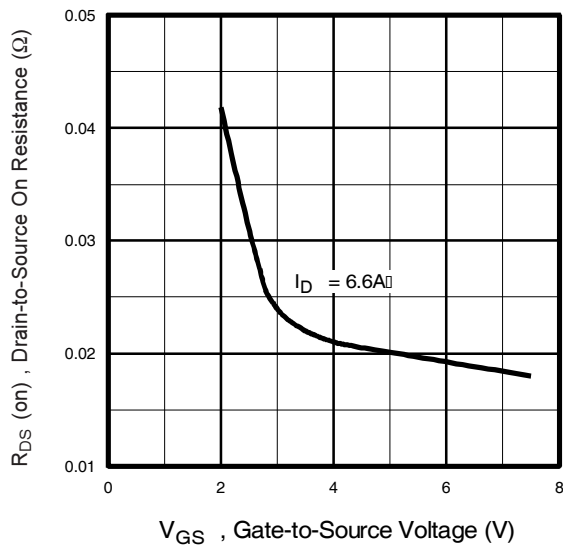


Fig 7. Typical On-Resistance Vs. Gate Voltage

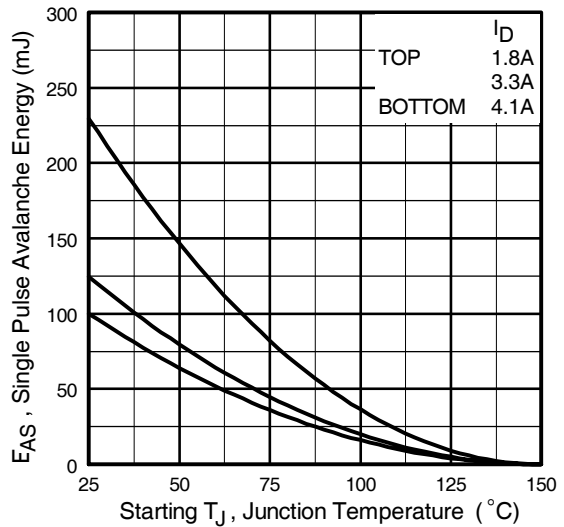


Fig 8. Maximum Avalanche Energy Vs. Drain Current

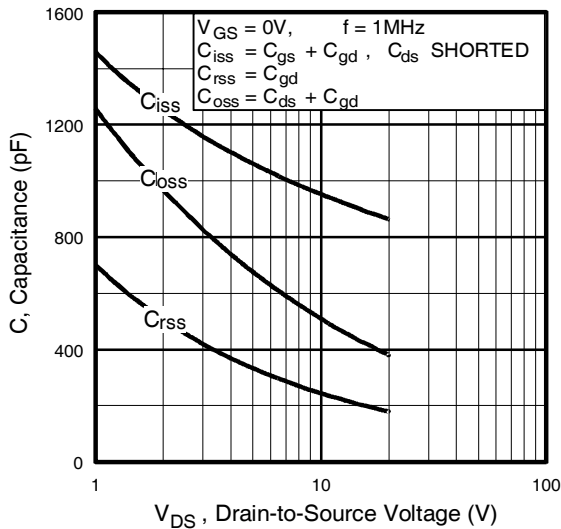


Fig 9. Typical Capacitance Vs. Drain-to-Source Voltage

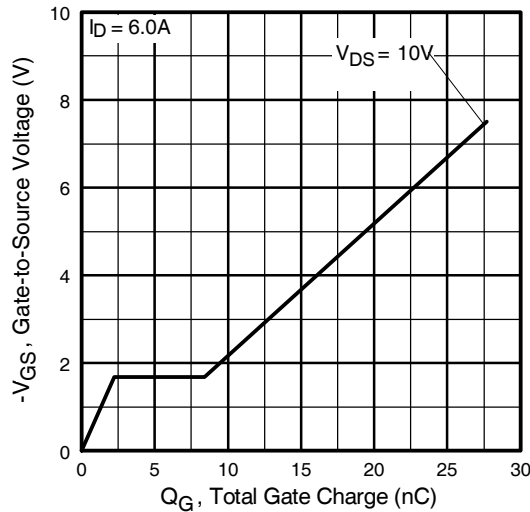


Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

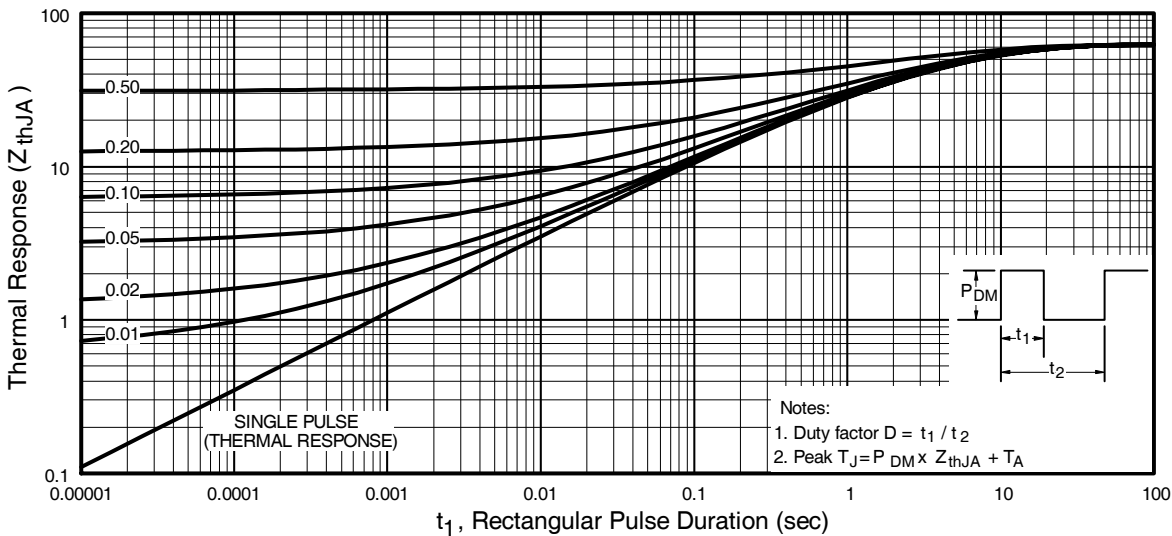


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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P-Channel

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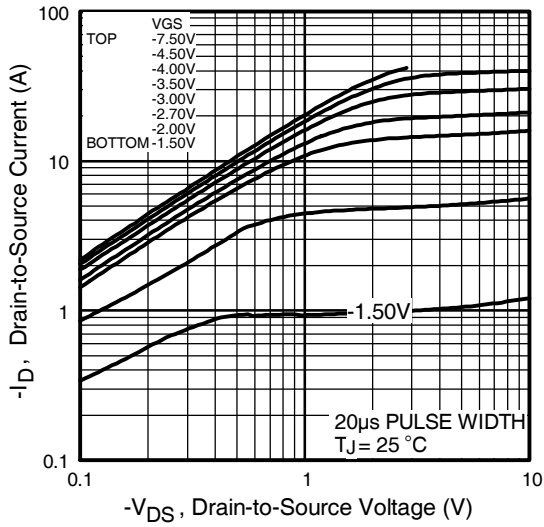


Fig 12. Typical Output Characteristics

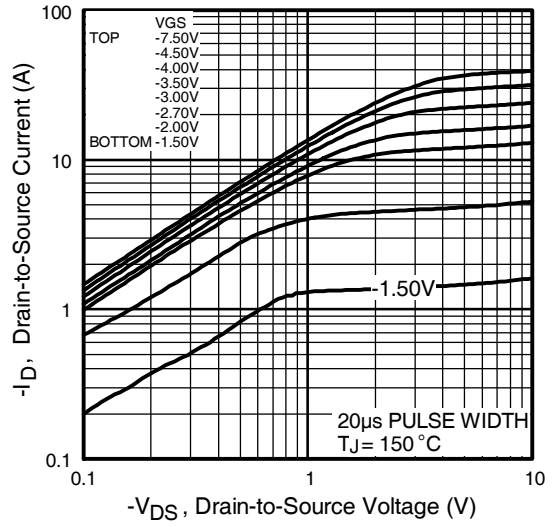


Fig 13. Typical Output Characteristics

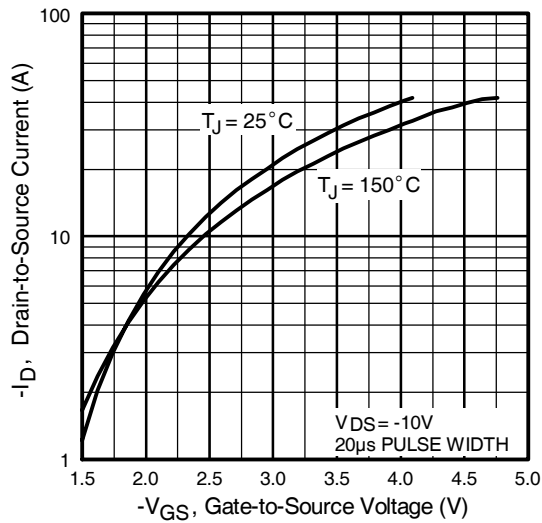


Fig 14. Typical Transfer Characteristics

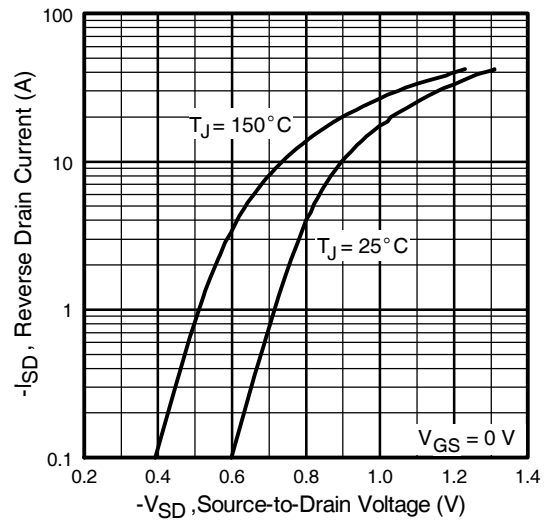


Fig 15. Typical Source-Drain Diode Forward Voltage

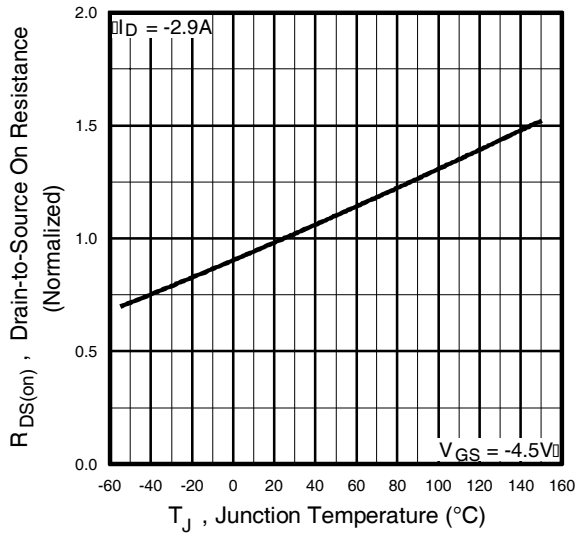


Fig 16. Normalized On-Resistance Vs. Temperature

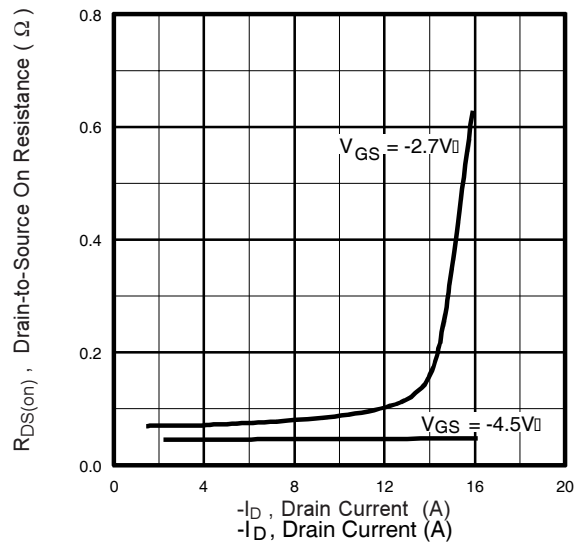


Fig 17. Typical On-Resistance Vs. Drain Current

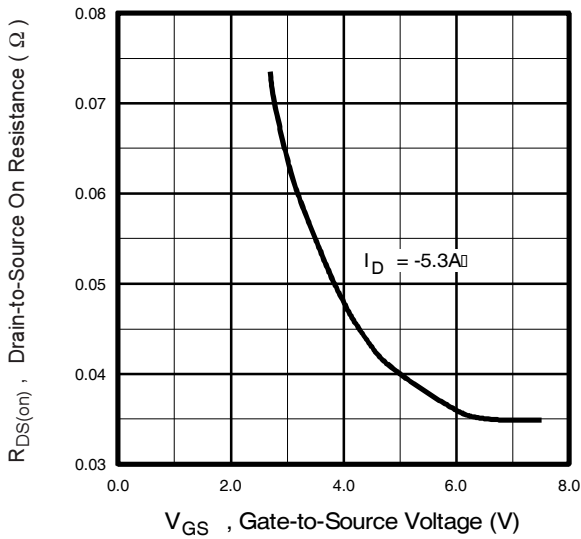


Fig 18. Typical On-Resistance Vs. Gate Voltage

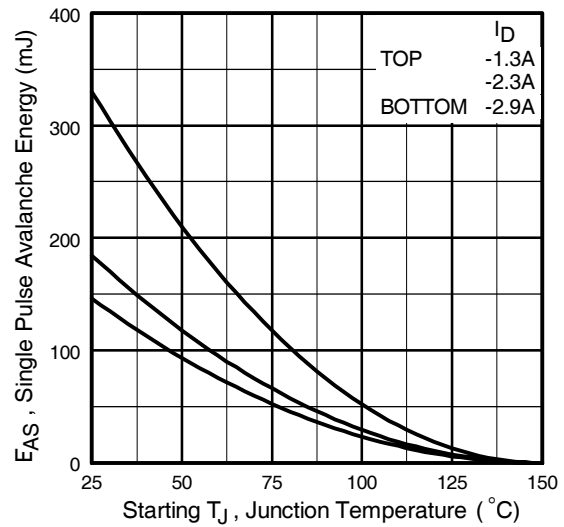


Fig 19. Maximum Avalanche Energy Vs. Drain Current

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P-Channel

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IR Rectifier

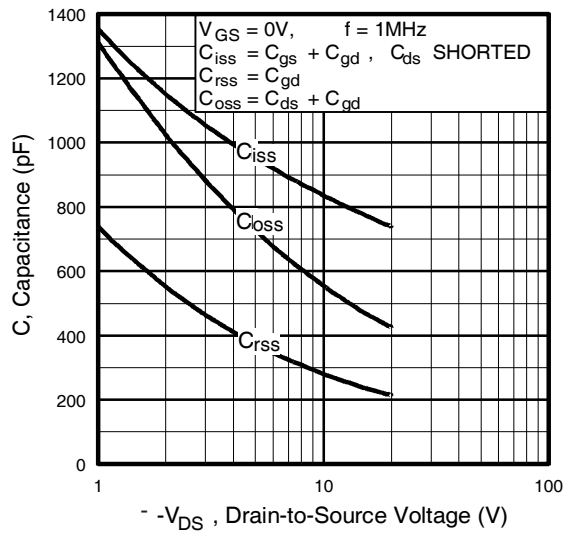


Fig 20. Typical Capacitance Vs. Drain-to-Source Voltage

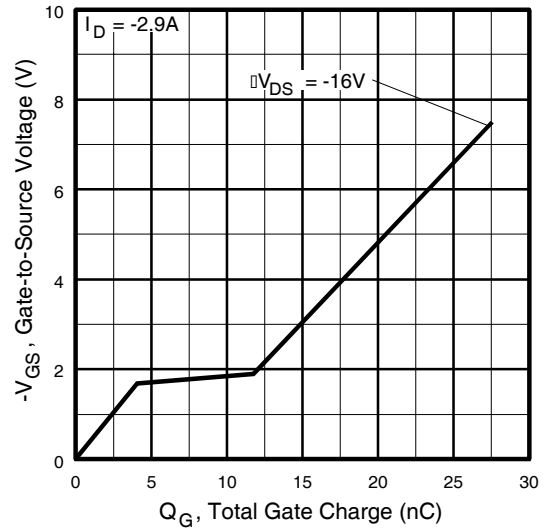


Fig 21. Typical Gate Charge Vs. Gate-to-Source Voltage

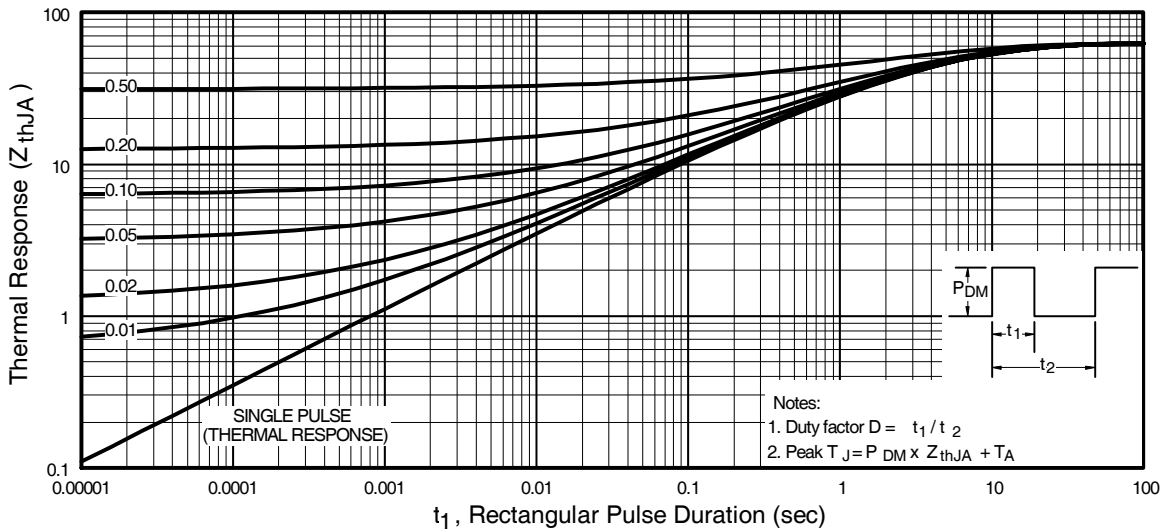


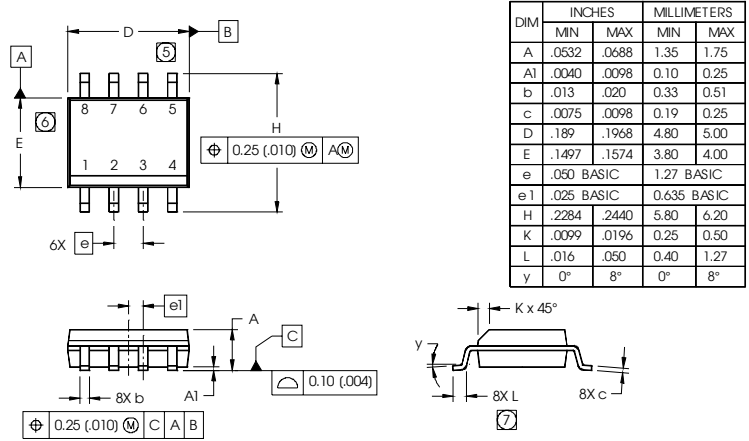
Fig 22. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



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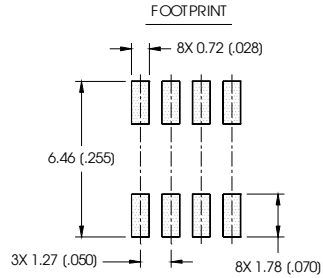
SO-8 Package Outline

Dimensions are shown in millimeters (inches)



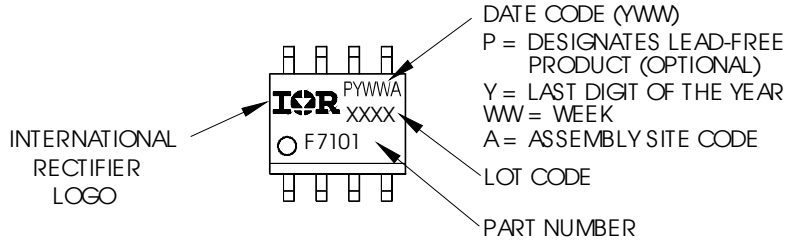
NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- 5 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
- 6 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
- 7 DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.



SO-8 Part Marking Information (Lead-Free)

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

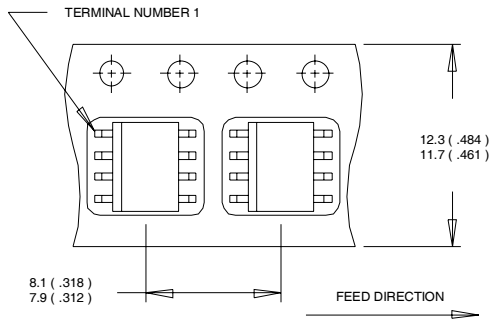


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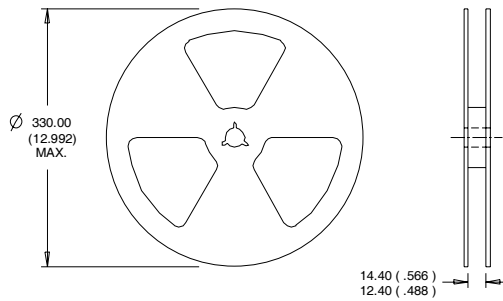
SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.
 This product has been designed and qualified for the Consumer market.
 Qualification Standards can be found on IR's Web site.



IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
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