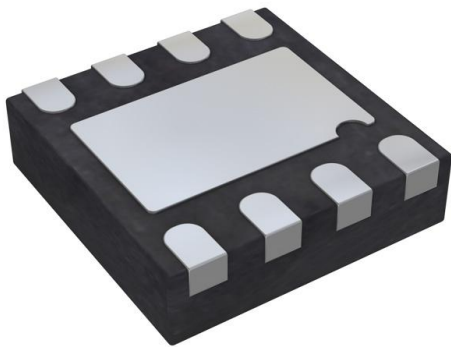


AD5111BCPZ10-RL7 Datasheet

www.digi-electronics.com



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	AD5111BCPZ10-RL7-DG
Manufacturer	Analog Devices Inc.
Manufacturer Product Number	AD5111BCPZ10-RL7
Description	IC DGTL POT 10KOHM 128TAP 8LFCSP
Detailed Description	Digital Potentiometer 10k Ohm 1 Circuit 128 Taps Up/Down (U/D, CS) Interface 8-LFCSP-UD (2x2)

This model AD5111BCPZ10-RL7 is available at DiGi Electronics.

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Purchase and inquiry

Manufacturer Product Number:

AD5111BCPZ10-RL7

Series:

-

DiGi-Electronics Programmable:

Not Verified

Configuration:

Potentiometer

Number of Taps:

128

Interface:

Up/Down (U/D, CS)

Voltage - Supply:

2.3V ~ 5.5V

Tolerance:

±8%

Mounting Type:

Surface Mount

Package / Case:

8-UFDFN Exposed Pad, CSP

Resistance - Wiper (Ohms) (Typ):

70

Manufacturer:

Analog Devices Inc.

Product Status:

Active

Taper:

Linear

Number of Circuits:

1

Resistance (Ohms):

10k

Memory Type:

Non-Volatile

Features:

-

Temperature Coefficient (Typ):

35ppm/°C

Supplier Device Package:

8-LFCSP-UD (2x2)

Operating Temperature:

-40°C ~ 125°C

Base Product Number:

AD5111

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8542.39.0001

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



Quick Start Guide

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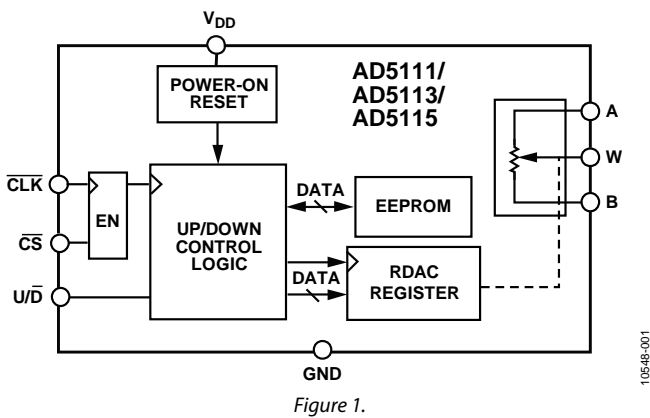
One Technology Way • P.O. Box 9106 • Norwood, MA 02062-9106, U.S.A. • Tel: 781.329.4700 • Fax: 781.461.3113 • www.analog.com

Overview of AD5111/AD5113/AD5115 Connections and Functionality

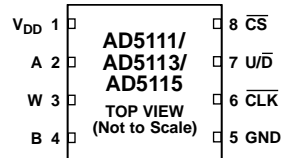
GENERAL DESCRIPTION

This quick start guide provides a general overview of AD5111/AD5113/AD5115 connections and functionality and should be reviewed in conjunction with the AD5111/AD5113/AD5115 data sheet.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



NOTES
1. THE EXPOSED PAD IS INTERNALLY FLOATING.

Figure 2.

PIN FUNCTION DESCRIPTIONS

Table 1.

Pin No.	Mnemonic	Description
1	V _{DD}	Positive power supply.
2	A	Terminal A of RDAC.
3	W	Wiper terminal of RDAC.
4	B	Terminal B of RDAC.
5	GND	Ground pin.
6	CLK	Clock input.
7	U/D	Up/down selection.
8	CS	Chip select.
	EPAD	Internally floating exposed pad.

TYPICAL EXTERNAL CONNECTIONS

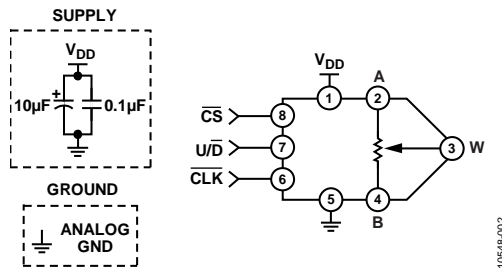


Figure 3.

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Quick Start Guide

OPERATIONAL CONDITIONS

Table 2. Specifications

Parameter	Specification
V_{DD} to GND	2.3 V to 5.5 V
V_A , V_W , V_B to GND	GND to V_{DD}
Maximum Continuous I_A , I_W , I_B	
$R_{AW} = 5\text{ k}\Omega$ and $10\text{ k}\Omega$	$\pm 6\text{ mA}$
$R_{AW} = 80\text{ k}\Omega$	$\pm 1.5\text{ mA}$
Maximum Clock (CLK)	50 MHz

COMMANDS

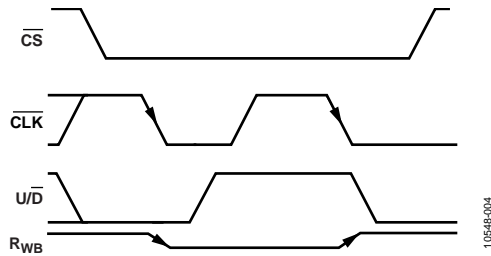


Figure 4. Increment and Decrement

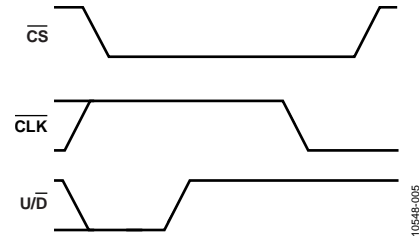


Figure 5. Shutdown

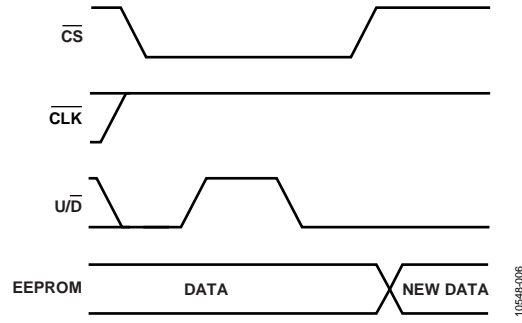


Figure 6. Store

REVISION HISTORY

3/12—Revision 0: Initial Version

**ESD Caution**

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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