

MAX709TESA+ Datasheet



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DiGi Electronics Part Number MAX709TESA+-DG

Manufacturer Analog Devices Inc./Maxim Integrated

Manufacturer Product Number MAX709TESA+

Description IC SUPERVISOR 1 CHANNEL 8SOIC

Detailed Description Supervisor Push-Pull, Totem Pole 1 Channel 8-SOIC



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
MAX709TESA+	Analog Devices Inc./Maxim Integrated
Series:	Product Status:
	Active
DiGi-Electronics Programmable:	Type:
Not Verified	Simple Reset/Power-On Reset
Number of Voltages Monitored:	Voltage - Threshold:
1	3.08V
Output:	Reset:
Output: Push-Pull, Totem Pole	Reset: Active Low
Push-Pull, Totem Pole	Active Low
Push-Pull, Totem Pole Reset Timeout:	Active Low Operating Temperature:
Push-Pull, Totem Pole Reset Timeout: 140ms Minimum	Active Low Operating Temperature: -40°C ~ 85°C (TA)
Push-Pull, Totem Pole Reset Timeout: 140ms Minimum Mounting Type:	Active Low Operating Temperature: -40°C ~ 85°C (TA) Package / Case:

Environmental & Export classification

8542.39.0001

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

Features



Power-Supply Monitor with Reset

General Description

The MAX709 provides a system reset during power-up, power-down, and brownout conditions. When V_{CC} falls below the reset threshold, RESET goes low and holds the μP in reset for 140ms min after V_{CC} rises above the threshold.

The RESET output is guaranteed to be in the correct state with $\rm V_{CC}$ down to 1V. The MAX709 provides excellent circuit reliability and low cost by eliminating external components and adjustments when used with +5V, +3.3V, or +3V powered systems. The MAX709 is available 8-pin DIP, µMAX, and SO packages.

Applications

Minimum Component Count, Low-Cost Processor Systems

♦ +5V, +3.3V, and +3V Versions

- ♦ No External Components
- **♦ Low Cost**
- ♦ Precise Power-Down Reset Threshold
- ♦ 140ms Min Power-On Reset Delay
- ♦ Immune to Short Negative V_{CC} Transients
- ♦ 8-Pin DIP, µMAX, and SO Packages
- ♦ Low Supply Current: 35µA MAX709R/S/T 65µA - MAX709L/M

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX709_CPA	0°C to +70°C	8 Plastic DIP
MAX709_CUA	0°C to +70°C	8 µMAX
MAX709_CSA	0°C to +70°C	8 SO
MAX709_C/D	0°C to +70°C	Dice
MAX709_EPA	-40°C to +85°C	8 Plastic DIP
MAX709_EUA	-40°C to +85°C	8 µMAX
MAX709_ESA	-40°C to +85°C	8 SO

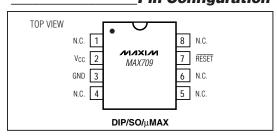
Devices are available in both leaded and lead-free packaging. Specify lead free by adding the + symbol at the end of the part number when ordering.

*Dice are specified at $T_A = +25$ °C, DC parameters only.

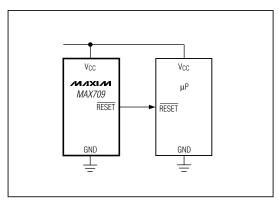
Note: This part offers a choice of five different reset threshold voltages. Select the letter corresponding to the desired nominal reset threshold voltage, and insert it into the blank to complete the part number.

RESET THRESHOLD					
SUFFIX	VOLTAGE (V)				
L	4.65				
М	4.40				
Т	3.08				
S	2.93				
R	2.63				

Pin Configuration



Typical Operating Circuit



NINXINI

Maxim Integrated Products 1

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ABSOLUTE MAXIMUM RATINGS

Terminal Voltage (with respect to GND)	
V _{CC}).3V to 6.0V
RESET0.3V to ($V_{CC} + 0.3V$
Input Current, V _{CC}	20mA
Output Current, RESET	20mA
Rate-of-Rise, V _{CC}	100V/µs
Continuous Power Dissipation (T _A = +70°C)	
Plastic DIP (derate 9.09mW/°C above +70°C)	727mW
μMAX (derate 4.10mW/°C above +70°C)	330mW
SO (derate 5.88mW/°C above +70°C)	471mW

Operating Temperature Ranges	
MAX709_C	0°C to +70°C
MAX709_E	40°C to +85°C
Storage Temperature Range	65°C to +160°C
Lead Temperature (soldering, 10sec)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

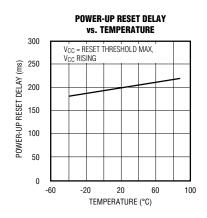
 $(V_{CC} = full range, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)$

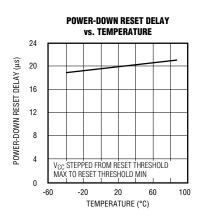
PARAMETER	CON	MIN	TYP	MAX	UNITS		
V _{CC} Range			1.0		5.5	\rfloor $_{\vee}$ \mid	
• CC Hange	MAX709_E	1.2		5.5	, ,		
	MAY700P/C/T only	MAX709_C, V _{CC} < 3.6V		35	85		
Supply Current (Note 1)	MAX709R/S/T only	MAX709_E, V _{CC} < 3.6V		35	110		
Supply Current (Note 1)	All versions	MAX709_C, V _{CC} < 5.5V		65	150	- μΑ	
	All versions	MAX709_E, V _{CC} < 5.5V		65	200		
	MAX709L		4.50	4.65	4.75		
	MAX709M		4.25	4.40	4.50]	
RESET Threshold, V _{TH}	MAX709T	3.00	3.08	3.15	V		
	MAX709S	2.85	2.93	3.00			
	MAX709R	2.55	2.63	2.70			
V _{CC} to RESET Delay	V _{CC} = reset threshold ma	V _{CC} = reset threshold max to reset threshold min				μs	
Reset Active Timeout Period	V _{CC} = reset threshold max, V _{CC} rising			280	560	ms	
	I _{SINK} = 1.2mA, V _{CC} = reset threshold min, MAX709R/S/T only				0.3		
	I _{SINK} = 3.2mA, V _{CC} = rese MAX709L/M only			0.4			
RESET Output Voltage	$I_{SINK} = 50\mu A, V_{CC} \ge 1.0V,$			0.3			
RESET Output voitage	I _{SINK} = 100μA, V _{CC} ≥ 1.2V			0.4]		
	I _{SOURCE} = 500μA, V _{CC} ≥ roman MAX709R/S/T only	0.8 x V _{C0}	0				
	I _{SOURCE} = 800µA, V _{CC} ≥ reset threshold max, MAX709L/M only			5			

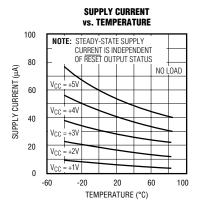
Note 1: Supply current is measured with V_{CC} = 3.6V for MAX709R/S/T, and V_{CC} = 5.5V for all versions.

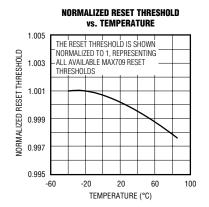


Typical Operating Characteristics









Pin Description

PIN	NAME	FUNCTION
1, 4, 5, 6, 8	N.C.	No Connect. There is no internal connection to this pin.
2	V _{CC}	+5V, +3.3V, or +3V Supply Voltage
3	GND	Ground
7	RESET	Reset Output remains low while V _{CC} is below the reset threshold, and for 280ms after V _{CC} rises above the reset threshold.

Applications Information Negative-Going V_{CC} Transients

In addition to issuing a reset to the microprocessor (µP) during power-up, power-down, and brownout conditions, the MAX709 is relatively immune to short duration negative-going V_{CC} transients (glitches).

Figure 1 shows typical transient duration vs. reset comparator overdrive, for which the MAX709 does not generate a reset pulse. The graph was generated using a negative-going pulse applied to V_{CC} , starting 1.5V above the actual reset threshold and ending below it by the magnitude indicated (reset comparator overdrive). The graph indicates the typical maximum pulse width that a negative-going V_{CC} transient may have without causing a reset pulse to be issued. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, for the MAX709L/MAX709M, a V_{CC} transient that goes 100mV below the reset threshold and lasts 40µs or less will not cause a reset pulse to be issued.

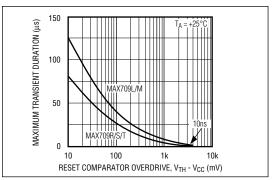


Figure 1. Maximum Transient Duration without Causing a Reset Pulse vs. Reset Comparator Overdrive

A 0.1µF bypass capacitor mounted as close as possible to pin 2 (V_{CC}) provides additional transient immunity.

Ensuring a Valid RESET Output Down to $V_{CC} = 0V$

When V_{CC} falls below 1V, the MAX709 RESET output no longer sinks current—it becomes an open circuit. Therefore, high-impedance CMOS logic inputs connected to the RESET output can drift to undermined voltages. This presents no problem in most applications, since most µP and other circuitry is inoperative with V_{CC} below 1V. However, in applications where the RESET output must be valid down to 0V, adding a pulldown resistor to the RESET pin will cause any stray leakage currents to flow to ground, holding RESET low (see Figure 2). The resistance value of R1 is not critical. It should be about $100k\Omega$, which is large enough not to load RESET and small enough to pull RESET to ground.

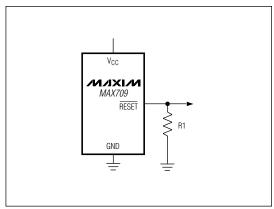


Figure 2. RESET Valid to V_{CC} = Ground Circuit

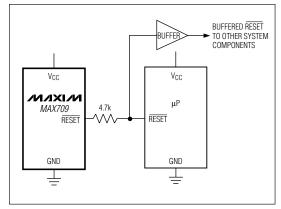


Figure 3. Interfacing to µPs with Bidirectional Reset I/O

Interfacing to µPs with Bidirectional Reset Pins

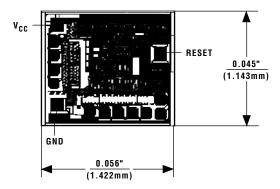
Microprocessors with bidirectional reset pins (such as the Motorola 68HC11 series) can contend with the MAX709 reset output. If, for example the MAX709 RESET output is asserted high and the μP wants to pull it low, indeterminate logic levels may result. To correct this, connect a 4.7k Ω resistor between the MAX709 RESET output and the μP reset I/O (see Figure 3). Buffer the MAX709 RESET output to other system components.

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_____μP Supervisory Circuits

Part Number	Nominal Reset Threshold (V)	Minimum Reset Pulse Width (ms)	Nominal Watchdog Timeout Period (sec)	Backup- Battery Switch	CE - Write Protect		Manual- Reset Input	Watch- dog Output	Low- Line Output	Active- High Reset	Battery- On Output
MAX690A/692A	4.65/4.40	140	1.6	<i>'</i>		<i>'</i>					
MAX691A/693A	4.65/4.40	140/adj.	1.6/adj.	~	✓ /10ns	~		~	~	~	~
MAX696	Adj.	35/adj.	1.6/adj.	'		/		/	/	/	'
MAX697	Adj.	35/adj.	1.6/adj.		~	•		~	'	'	
MAX700	4.65/adj.	200	-				/			/	
MAX703/704	4.65/4.40	140	-	'		'	/				
MAX705/706	4.65/4.40	140	1.6			•	~	~			
MAX706P	2.63	140	1.6			~	~	~		~	
MAX706R/S/T	2.63/2.93/ 3.08	140	1.6			•	~	~			
MAX707/708	4.65/4.40	140	-			~	~			~	
MAX708R/S/T	2.63/2.93/ 3.08	140	-			•	~			~	
MAX709L/M/ R/S/T	4.65/4.40/ 2.63/2.93/3.08	140	-								
MAX791	4.65	140	1	~	✓ /10ns	~	~	~	~	~	~
MAX792L/M/ R/S/T	4.65/4.40/ 2.63/2.93/3.08	140	1		✓ /10ns	•	~	~	•	~	
MAX800L/M	4.60/4.40	140	1.6/adj.	~	✓ /10ns	✔/±2%		~	/	~	~
MAX802L/M	4.60/4.40	140	1.6	~		✔/±2%					
MAX805L	4.65	140	1.6	v		v				~	
MAX813L	4.65	140	1.6			~	~	v		~	
MAX820L/M/ R/S/T	4.65/4.40/ 2.63/2.93/3.08	140	1		✓ /10ns	✔/±2%	~	~	~	~	
MAX1232	4.37/4.62	250	0.15/0.60/1.2				~				
MAX1259	-	-	-	~		V					

_____Chip Topography

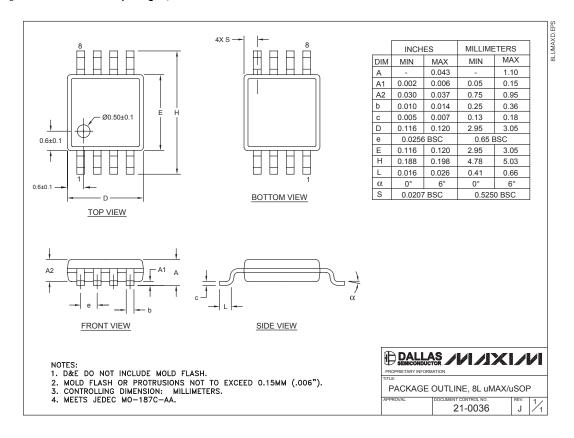


TRANSISTOR COUNT: 380 SUBSTRATE CONNECTED TO VCC

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Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



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