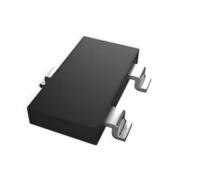


2N7002HV-TP Datasheet





https://www.DiGi-Electronics.com

DiGi Electronics Part Number 2N7002HV-TP-DG

Manufacturer Micro Commercial Co

Manufacturer Product Number 2N7002HV-TP

Description N-CHANNEL MOSFET SOT-23

Detailed Description N-Channel 60 V 115mA 200mW Surface Mount SOT

-23



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
2N7002HV-TP	Micro Commercial Co
Series:	Product Status:
	Not For New Designs
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
60 V	115mA
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
5V, 10V	7.50hm @ 500mA, 10V
Vgs(th) (Max) @ ld:	Vgs (Max):
1.8V @ 250µA	±20V
Input Capacitance (Ciss) (Max) @ Vds:	FET Feature:
50 pF @ 25 V	
Power Dissipation (Max):	Operating Temperature:
200mW	-55°C ~ 150°C (TJ)
Mounting Type:	Supplier Device Package:
Surface Mount	SOT-23
Package / Case:	Base Product Number:
TO-236-3, SC-59, SOT-23-3	2N7002

Environmental & Export classification

8541.21.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



Features

- · Trench MV MOSFET Technology
- · Low Threshold Voltage
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

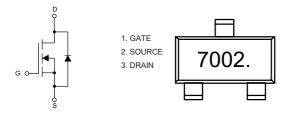
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 320°C/W Junction to Ambient^(Note2)

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage	V _{DS}	60	V		
Gate-Source Volltage	V _{GS}	±20	V		
Continuous Drain Current	T _A =25°C		115		
	T _A =100°C	l _D	73	mA	
Pulsed Drain Current ^(Note3)		I _{DM}	460	mA	
Total Power Dissipation ^(Note4)		P _D	390	mW	

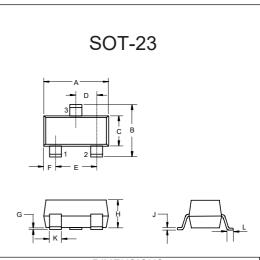
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta,JA}$ is measured with the device mounted on the minimum recommended pad size, in a still air environment with T_A =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_{D} is based on max. junction temperature, using junction to ambient thermal resistance.

Internal Structure and Marking Code

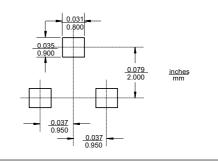


N-Channel MOSFET



	DIMENSIONS				
DIM	DIM INCHES		MM		NOTE
DIIVI	MIN	MAX	MIN	MAX	NOIL
Α	0.110	0.120	2.80	3.04	
В	0.083	0.104	2.10	2.64	
С	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
Е	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
Н	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout





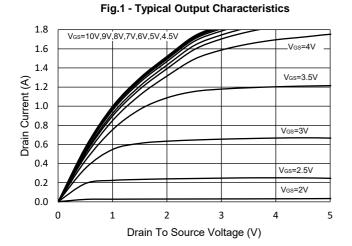
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1	ı		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =10μA	60			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zoro Coto Voltago Drain Current		V _{DS} =60V, V _{GS} =0V			80	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V, T _J =125°C			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.6	1.7	1.8	V	
	_	V _{GS} =10V, I _D =500mA		0.9 3			
Drain-Source On-Resistance	$R_{DS(on)}$	V _{GS} =5V, I _D =50mA		1	4	Ω	
Forward Transconductance	g _{fs}	V _{DS} =10V, I _D =200mA		300		mS	
Gate Resistance	R_g	F=1 MHz, Open drain		6		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				115	mA	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =115mA			1.5	V	
Reverse Recovery Time	t _{rr}	1 0 0 4 4 11 / 14 4 4 0 0 4 /		7		ns	
Reverse Recovery Charge	Q _{rr}	I _F =0.34A, dI _F /dt=100A/μs		8		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			34			
Output Capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V,f=1MHz		5		pF	
Reverse Transfer Capacitance	C_{rss}			4			
Total Gate Charge	Q _g			1.6			
Gate-Source Charge	Q _{gs}	V _{DS} =25V,V _{GS} =10V,I _D =0.35A		0.47		nC	
Gate-Drain Charge	Q_{gd}			0.25			
Turn-On Delay Time	t _{d(on)}			0.8			
Turn-On Rise Time	t _r	V _{DD} =30V, V _{GS} =10V,		15			
Turn-Off Delay Time	t _{d(off)}	R_{GEN} =25 Ω , I_{DS} =340mA		4		ns	
Turn-Off Fall Time	t _f			33			

0



Curve Characteristics



1.8
1.6
1.4

1.2
1.0
0.8
1.0
0.6
0.4
0.2
0.0

2

Fig.2 - Transfer Characteristic

Fig.3 - $R_{\rm DS(ON)}$ - $V_{\rm GS}$

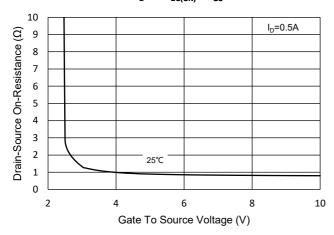


Fig.4 - R_{DS(ON)} - I_D

Gate To Source Voltage (V)

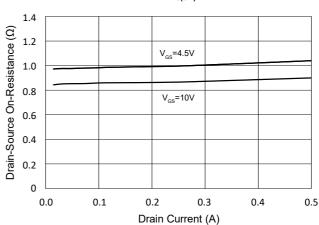


Fig.5 - Capacitance Characteristics

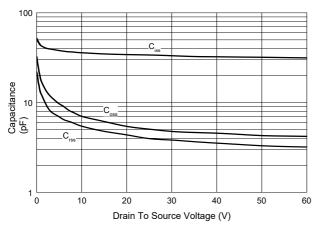
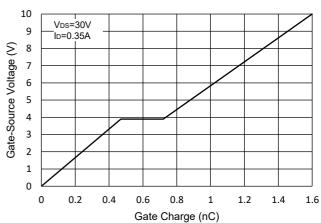
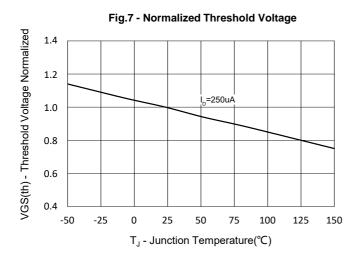


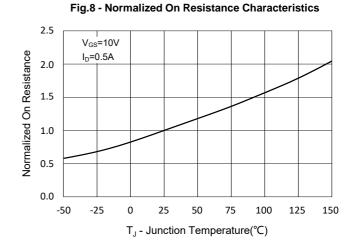
Fig.6 - Gate Charge

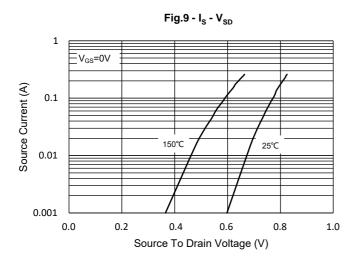


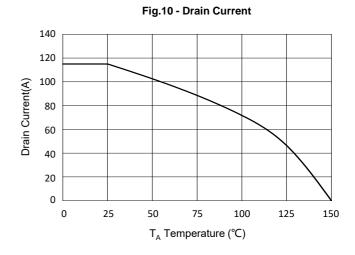


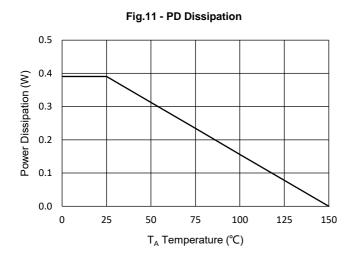
Curve Characteristics













Curve Characteristics

Fig.12 - Safe Operation Area

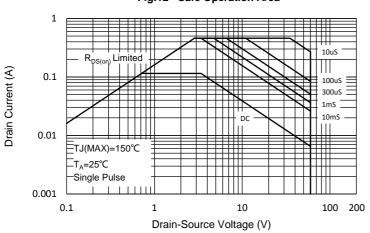
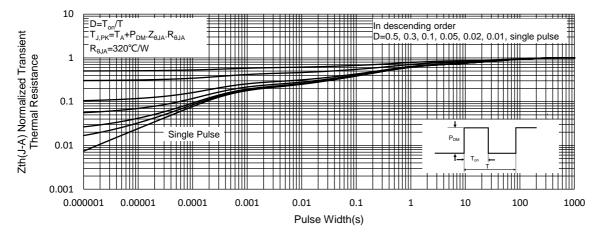


Fig.13 - Normalized Transient Thermal Impedance





Ordering Information

Device	Packing		
Part Number-TP	Tape&Reel:3Kpcs/Reel		

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