

# MCQ4828A-TP Datasheet

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DiGi Electronics Part Number MCQ4828A-TP-DG

Manufacturer Micro Commercial Co

Manufacturer Product Number MCQ4828A-TP

Description MOSFET 2N-CH 60V 4.5A 8SOP

Detailed Description Mosfet Array 60V 4.5A (Ta) 1.25W (Ta) Surface Mou

nt 8-50



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## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
MCQ4828A-TP	Micro Commercial Co
Series:	Product Status:
-	Active
Technology:	Configuration:
MOSFET (Metal Oxide)	2 N-Channel (Dual)
FET Feature:	Drain to Source Voltage (Vdss):
	60V
Current - Continuous Drain (Id) @ 25°C:	Rds On (Max) @ Id, Vgs:
4.5A (Ta)	56mOhm @ 4.5A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
3V @ 250μA	10.5nC @ 10V
Input Capacitance (Ciss) (Max) @ Vds:	Power - Max:
540pF @ 30V	1.25W (Ta)
Operating Temperature:	Mounting Type:
150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
8-SOIC (0.154", 3.90mm Width)	8-SOP
Base Product Number:	
MCQ4828	

## **Environmental & Export classification**

8541.29.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



#### **Features**

- Trench MOSFET Technology
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note1)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## **Maximum Ratings**

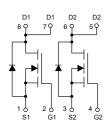
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 78°C/W Junction to Ambient<sup>(Note2)</sup>

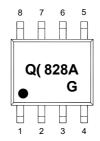
Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V <sub>DS</sub>	60	V
Gate-Source Volltage		V <sub>GS</sub>	±20	V
Continuous Drain Current	T <sub>A</sub> =25°C		4.5	^
	T <sub>A</sub> =100°C	- I <sub>D</sub>	2.8	A
Pulsed Drain Current (Note3)		I <sub>DM</sub>	18	Α
Total Power Dissipation (Note4)		P <sub>D</sub>	1.6	W
Avalanche Energy (Note5)		E <sub>AS</sub>	18	mJ

#### Note:

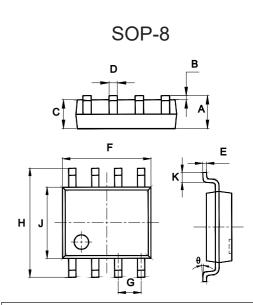
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The Value of  $R_{\theta JA}$  is Measured with the Device Mounted on 1in2 FR-4 Board with 2oz. Copper, in a Still Air Environment with  $T_A$ =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.  $P_{\text{D}}$  is based on max. junction temperature, using junction-ambient thermal resistance.
- 5.  $T_J$ =25°C,  $V_{DS}$ =30V,  $V_{GS}$ =10V, L=0.5mH.

## **Internal Structure and Marking Code**



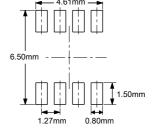


# Dual N-Channel Power MOSFET



DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.053	0.069	1.35	1.75	
В	0.004	0.010	0.10	0.25	
С	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
Е	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
Н	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	

## Suggested Solder Pad Layout 4.61mm →





## Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μΑ	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	1	2.1	3	V	
	В	V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A		32	56	m-0	
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	46 77		77	mΩ	
Gate Resistance	$R_G$	f=1MHz, Open drain		2		Ω	
Diode Characteristics			•				
Continuous Body Diode Current	Is				4.5	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =5.6A			1	V	
Reverse Recovery Time	t <sub>rr</sub>	L 0.05A II / II 400A/		25		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =2.25A, dI <sub>F</sub> /dt=100A/μs		18		nC	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>			824			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V,f=1MHz		44		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			39			
Total Gate Charge	$Q_g$			17.5			
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =30V,V <sub>GS</sub> =10V,I <sub>D</sub> =4.5A		3		nC	
Gate-Drain Charge	$Q_{gd}$			5			
Turn-On Delay Time	t <sub>d(on)</sub>			4.3			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V,		17		<b>.</b>	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{G}$ =2.2 $\Omega$ , $I_{D}$ =2.25A		20		ns	
Turn-Off Fall Time	t <sub>f</sub>			2.5			



## **Curve Characteristics**

Fig. 1 - Typical Output Characteristics

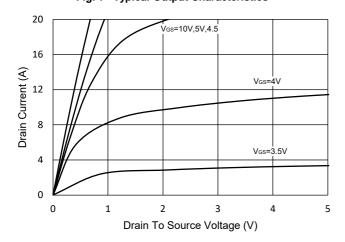


Fig.2 - Transfer Characteristic

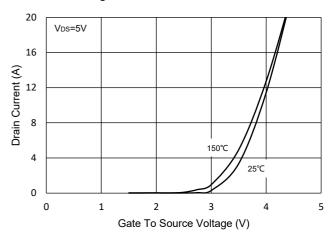


Fig.3 -  $R_{\rm DS(ON)}$  -  $V_{\rm GS}$ 

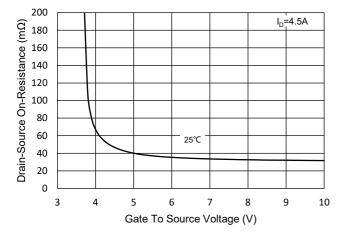


Fig.4 - R<sub>DS(ON)</sub> - I<sub>D</sub>

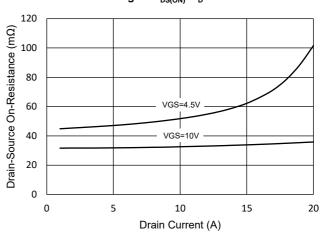


Fig.5 - Capacitance Characteristics

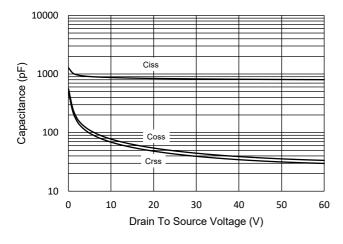
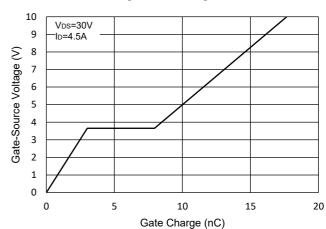


Fig.6 - Gate Charge





## **Curve Characteristics**

Fig.7 - Normalized Threshold Voltage

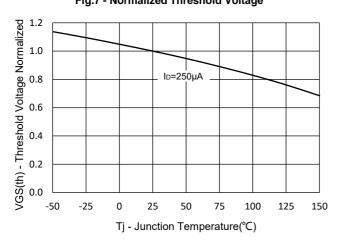


Fig.8 - Normalized On Resistance Characteristics

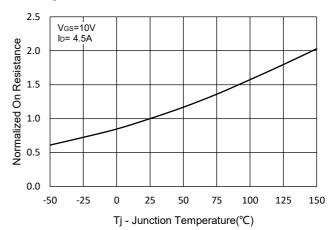


Fig.9 - I<sub>S</sub> - V<sub>SD</sub>

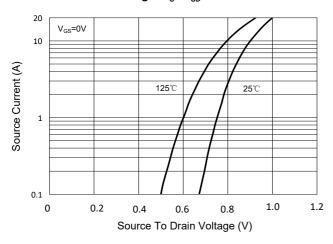


Fig.10 - Drain Current

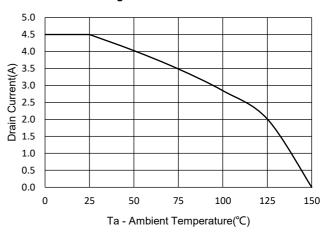
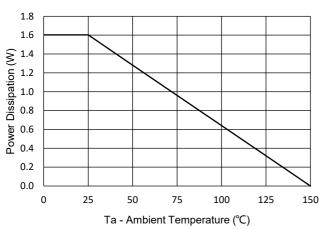


Fig.11 - PD Dissipation





## **Curve Characteristics**

Fig.12 - Safe Operation Area

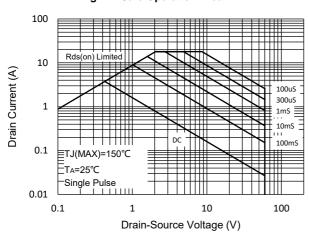
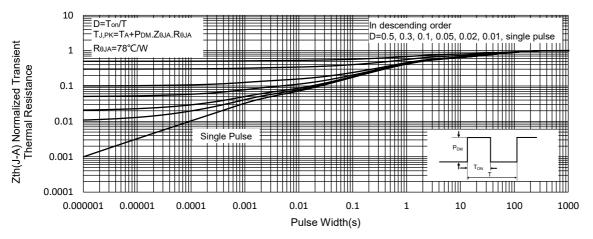


Fig.13 - Normalized Transient Thermal Impedance





## **Ordering Information**

Device	Packing		
Part Number-TP	Tape&Reel: 4Kpcs/Reel		

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