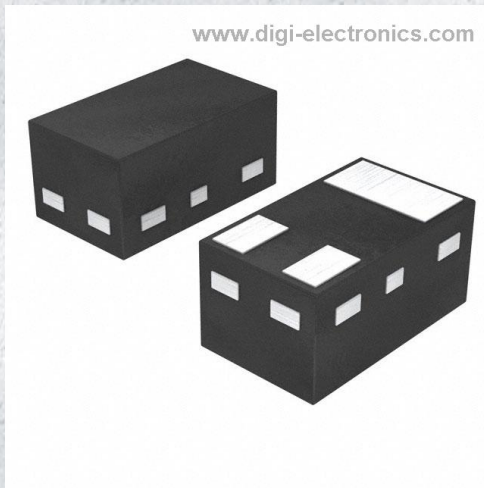


BC846BM,315 Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	BC846BM,315-DG
Manufacturer	Nexperia USA Inc.
Manufacturer Product Number	BC846BM,315
Description	TRANS NPN 65V 0.1A DFN1006B-3
Detailed Description	Bipolar (BJT) Transistor NPN 65 V 100 mA 100MHz 2 50 mW Surface Mount DFN1006B-3



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

BC846BM,315

Series:

-

Transistor Type:

NPN

Voltage - Collector Emitter Breakdown (Max):

65 V

Current - Collector Cutoff (Max):

15nA (ICBO)

Power - Max:

250 mW

Operating Temperature:

150°C (TJ)

Qualification:

AEC-Q101

Package / Case:

3-XDFN

Base Product Number:

BC846

Manufacturer:

Nexperia USA Inc.

Product Status:

Active

Current - Collector (Ic) (Max):

100 mA

Vce Saturation (Max) @ Ib, Ic:

200mV @ 500µA, 10mA

DC Current Gain (hFE) (Min) @ Ic, Vce:

200 @ 2mA, 5V

Frequency - Transition:

100MHz

Grade:

Automotive

Mounting Type:

Surface Mount

Supplier Device Package:

DFN1006B-3

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.21.0075

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



BC846BM

65 V, 100 mA NPN general-purpose transistor

20 August 2015

Product data sheet

1. General description

NPN general-purpose transistor in a leadless ultra small DFN1006-3 (SOT883) Surface-Mounted Device (SMD) plastic package.

PNP complement: BC856BM.

2. Features and benefits

- Leadless ultra small SMD plastic package
- Power dissipation comparable to SOT23
- AEC-Q101 qualified

3. Applications

- General-purpose switching and amplification
- Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CE0}	collector-emitter voltage	open base	-	-	65	V
I_C	collector current		-	-	100	mA
h_{FE}	DC current gain	$V_{CE} = 5\text{ V}$; $I_C = 2\text{ mA}$; $T_{amb} = 25\text{ °C}$	200	-	450	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	<p>Transparent top view DFN1006-3 (SOT883)</p>	<p>sym021</p>
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC846BM	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883

7. Marking

Table 4. Marking codes

Type number	Marking code
BC846BM	ZB

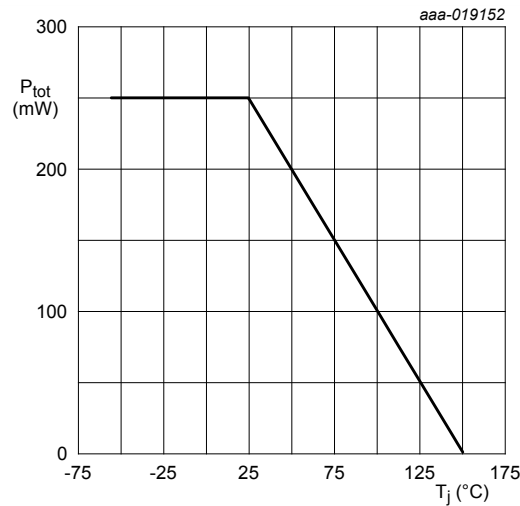
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	80	V
V_{CEO}	collector-emitter voltage	open base		-	65	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I_C	collector current			-	100	mA
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	200	mA
I_{BM}	peak base current			-	200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	250	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



FR4 PCB, standard footprint

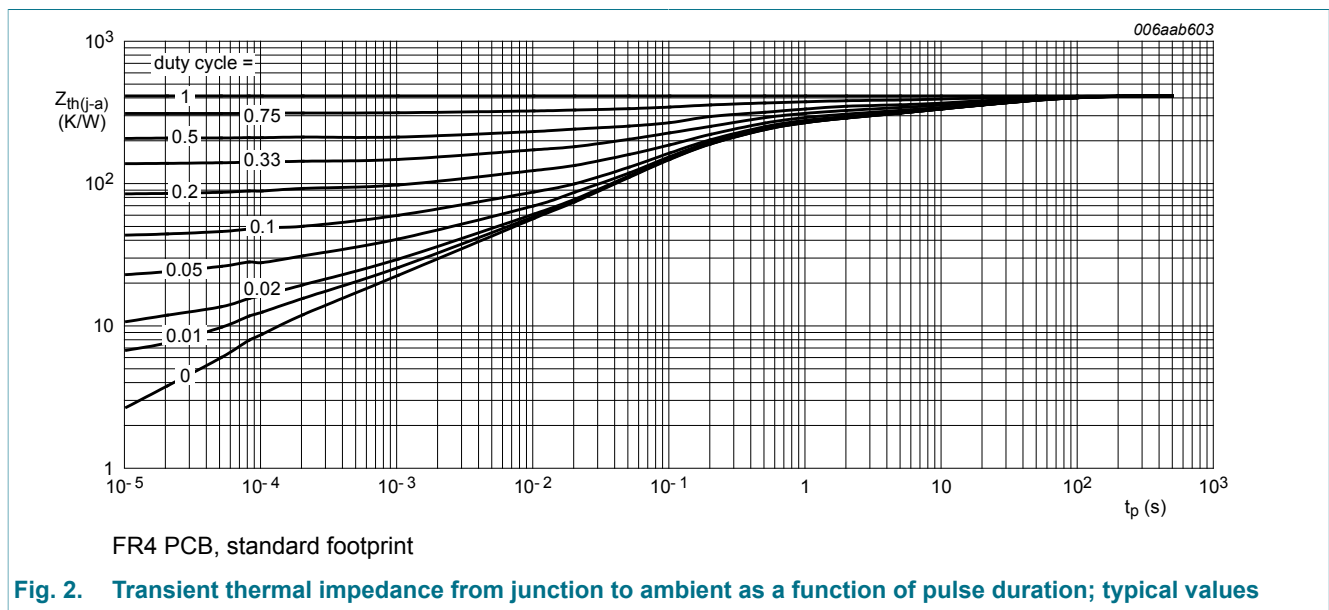
Fig. 1. Power derating curve DFN1006-3 (SOT883)

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

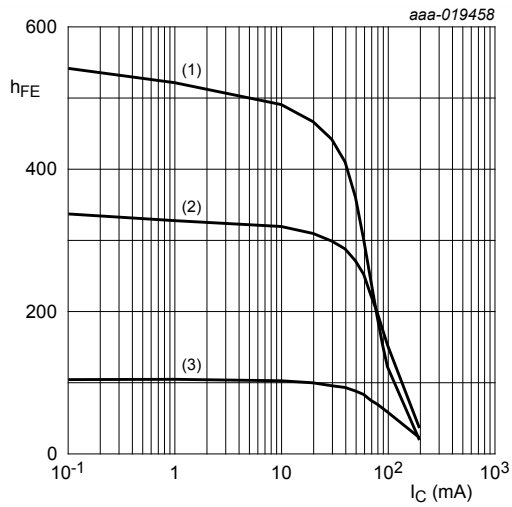
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



10. Characteristics

Table 7. Characteristics

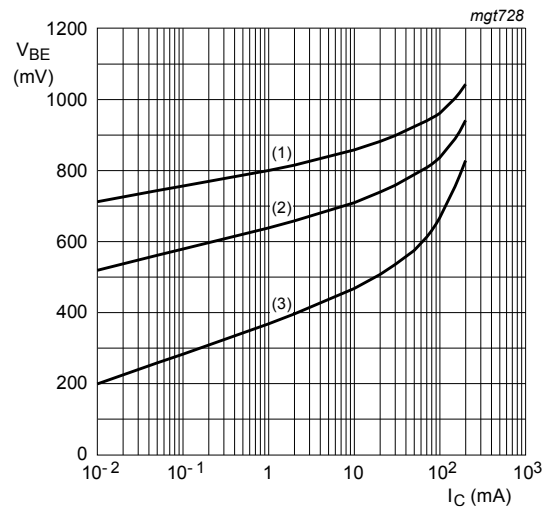
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	15	nA
		V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	200	-	450	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-	200	mV
		I _C = 100 mA; I _B = 5 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	200	400	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	760	-	mV
		I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C	-	900	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	580	660	700	mV
		V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C	-	-	770	mV
C _C	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	2	3	pF
C _E	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	11	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 200 μA; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz; T _{amb} = 25 °C	-	2	10	dB



$$V_{CE} = 5 \text{ V}$$

- (1) $T_{amb} = 150 \text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 25 \text{ }^{\circ}\text{C}$
- (3) $T_{amb} = -55 \text{ }^{\circ}\text{C}$

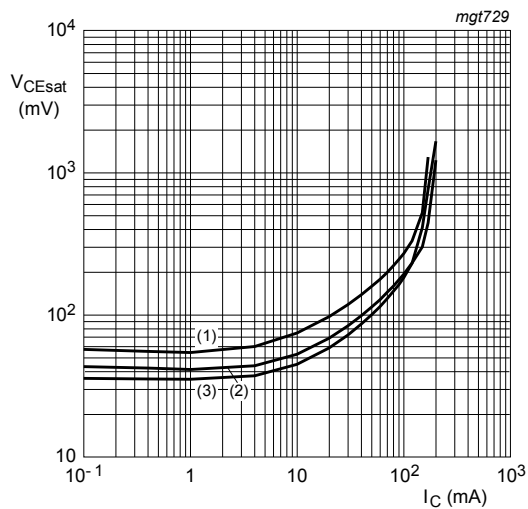
Fig. 3. DC current gain as a function of collector current; typical values



$$V_{CE} = 5 \text{ V}$$

- (1) $T_{amb} = -55 \text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 25 \text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 150 \text{ }^{\circ}\text{C}$

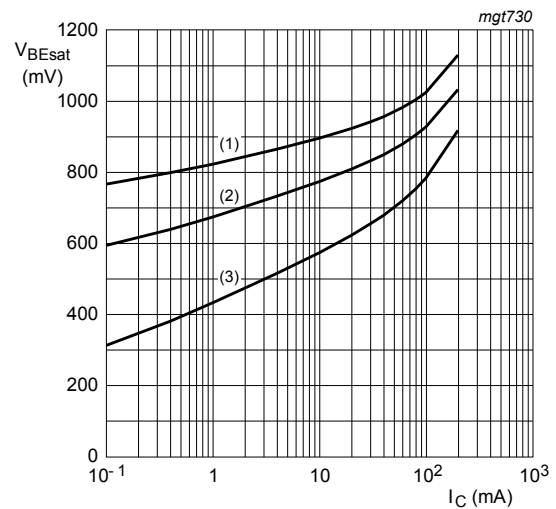
Fig. 4. Base-emitter voltage as a function of collector current; typical values



$$I_C/I_B = 20$$

- (1) $T_{amb} = 150 \text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 25 \text{ }^{\circ}\text{C}$
- (3) $T_{amb} = -55 \text{ }^{\circ}\text{C}$

Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values



$$I_C/I_B = 10$$

- (1) $T_{amb} = -55 \text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 25 \text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 150 \text{ }^{\circ}\text{C}$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

11. Test information

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline

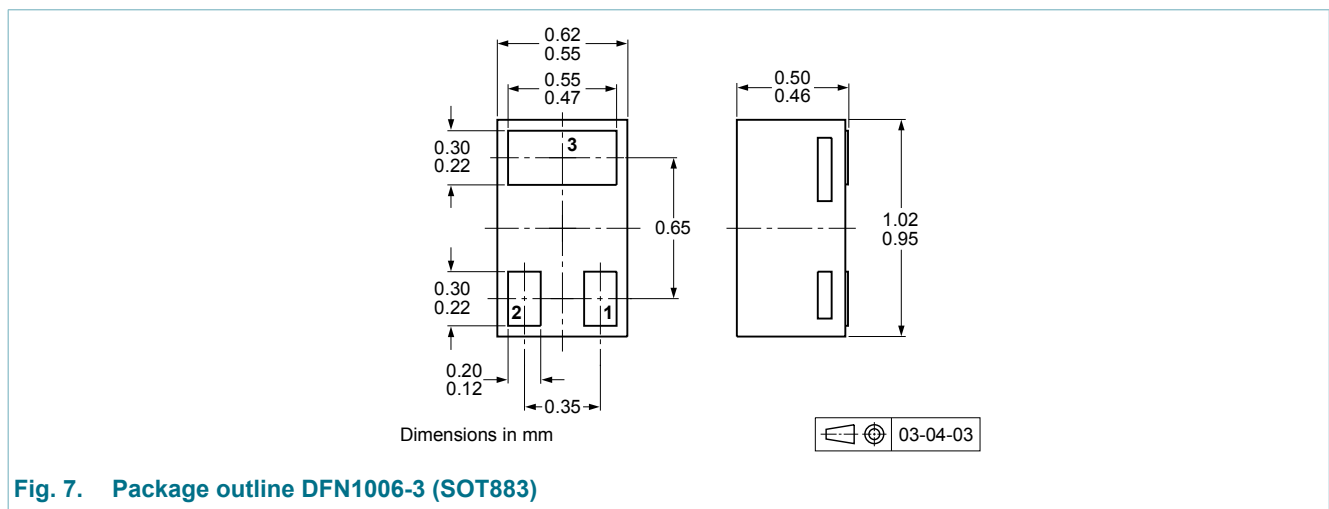


Fig. 7. Package outline DFN1006-3 (SOT883)

13. Soldering

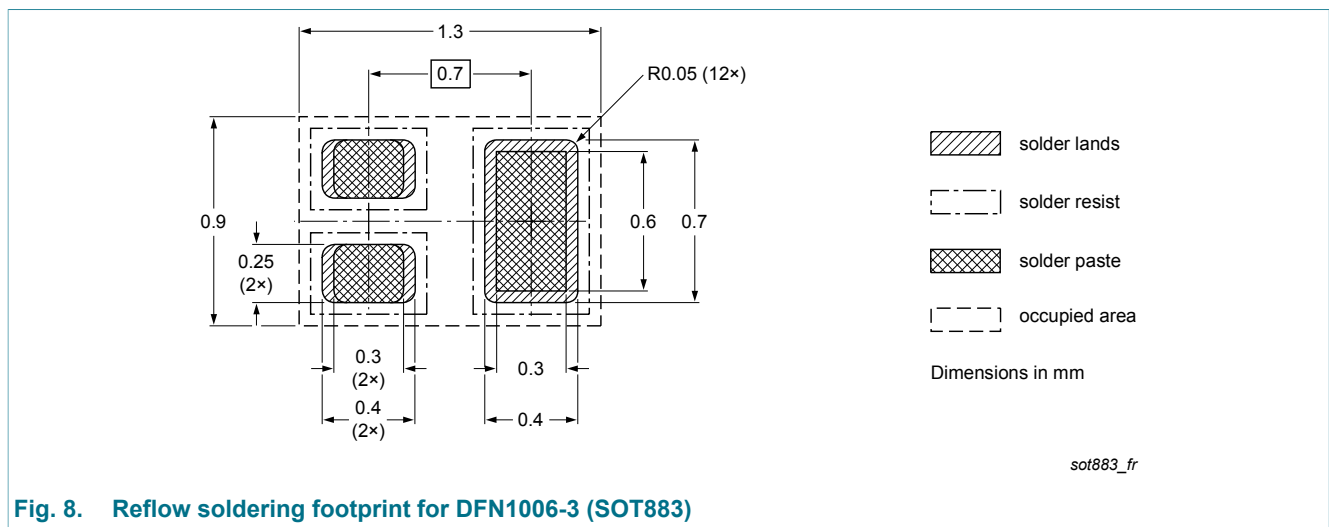


Fig. 8. Reflow soldering footprint for DFN1006-3 (SOT883)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC846BM v.1	20150820	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 20 August 2015

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