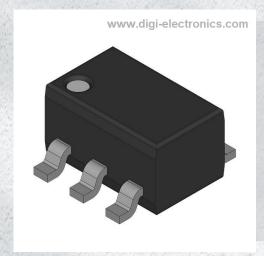


# BC846S/ZL115 Datasheet



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DiGi Electronics Part Number BC846S/ZL115-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number BC846S/ZL115

Description SMALL SIGNAL BIPOLAR TRANSISTOR

**Detailed Description** Bipolar (BJT) Transistor Array 2 NPN 65V 100mA 100

MHz 300mW Surface Mount 6-TSSOP



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# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
BC846S/ZL115	Nexperia USA Inc.
Series:	Product Status:
-	Active
Transistor Type:	Current - Collector (Ic) (Max):
2 NPN	100mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, Ic:
65V	300mV @ 5mA, 100mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
15nA (ICBO)	110 @ 2mA, 5V
Power - Max:	Frequency - Transition:
300mW	100MHz
Operating Temperature:	Mounting Type:
150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
6-TSSOP, SC-88, SOT-363	6-TSSOP

# **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
Not applicable	1 (Unlimited)
REACH Status:	ECCN:
Vendor Undefined	EAR99
HTSUS:	



# **BC846S**

# NPN general purpose double transistor

**Product data sheet** 

## 1. General description

NPN double transistor in a very small SOT363 (SC-88) plastic six lead package.

#### 2. Features and benefits

- Two transistors in one package
- · Reduces number of components and board space
- No mutual interference between the transistors

# 3. Applications

· General purpose switching and small signal amplification.

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	65	V
Ic	collector current			-	-	100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C		110	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1	6 5 4	
3	C2	collector TR2		(TR1)
4	E2	emitter TR2		
5	B2	base TR2	∐1 ∐2 ∐3	I I I E1 B1 C2
6	C1	collector TR1	TSSOP6 (SOT363)	sym140



#### NPN general purpose double transistor

# 6. Ordering information

#### **Table 3. Ordering information**

T	ype number	Package	ackage							
		Name	Description	Version						
E	SC846S		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>						

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
BC846S	4F%

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

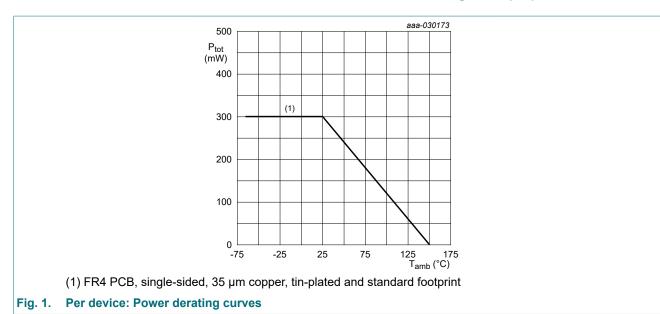
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transisto	or		-		'	
$V_{CBO}$	collector-base voltage	open emitter		-	80	V
$V_{CEO}$	collector-emitter voltage	open base		-	65	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
Ic	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	220	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Per device		1	'	'		
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.

#### NPN general purpose double transistor



#### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W
Per transist	tor		•				
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	568	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	230	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35  $\mu m$  copper, tin-plated and standard footprint.

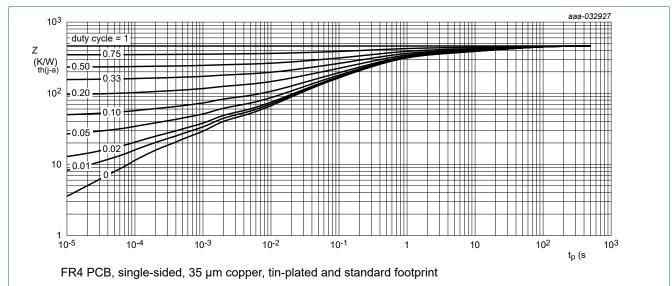


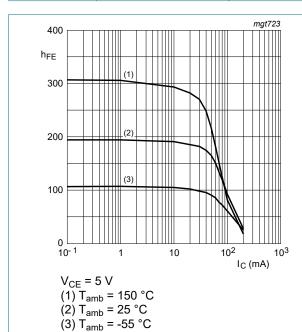
Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

#### NPN general purpose double transistor

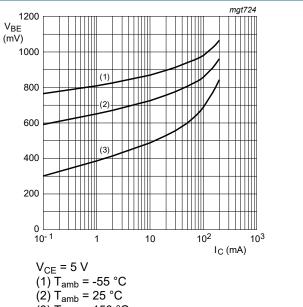
### 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	or					
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_C = 100 \ \mu\text{A}; \ I_E = 0 \ \text{A}; \ T_{amb} = 25 \ ^{\circ}\text{C}$	80	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	65	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_C = 0 \text{ A}; I_E = 100 \mu\text{A}; T_{amb} = 25 \text{ °C}$	6	-	-	V
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	15	nA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	110	-	-	
- OLSai	collector-emitter	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C	-	-	100	mV
	saturation voltage	$I_C$ = 100 mA; $I_B$ = 5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	300	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	770	-	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	580	-	700	mV
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25  ^{\circ}\text{C}$	-	-	1.5	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	-	-	MHz



DC current gain as a function of collector Fig. 3. current; typical values



(3)  $T_{amb} = 150 \, ^{\circ}C$ 

Fig. 4. Base-emitter voltage as a function of collector current; typical values

### NPN general purpose double transistor

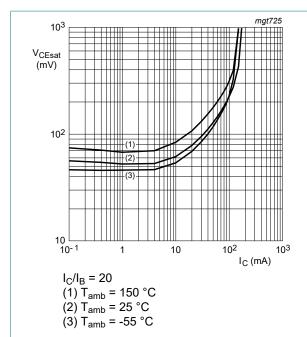


Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values

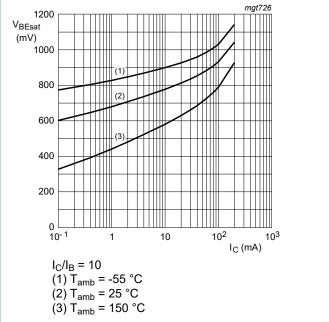
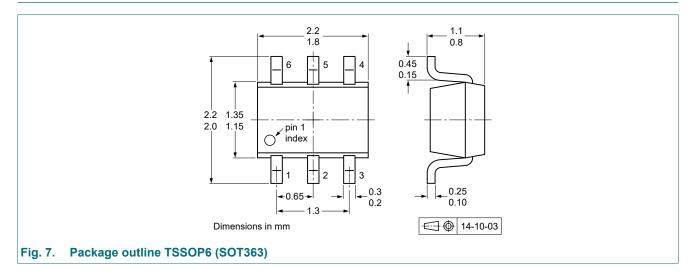


Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

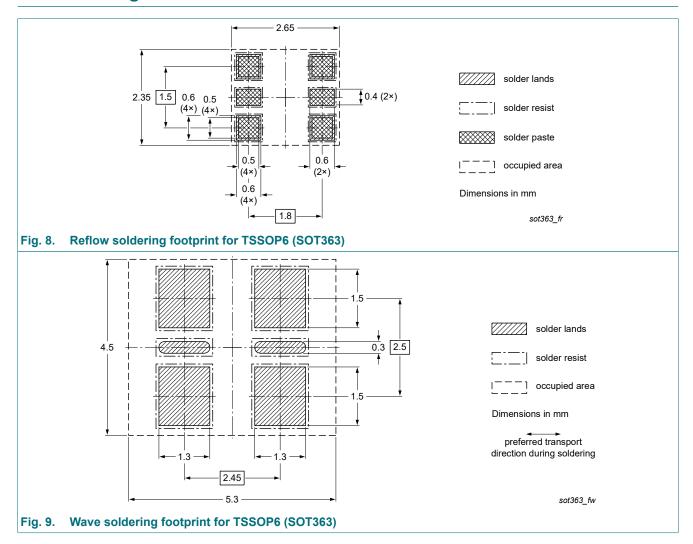
# 11. Package outline



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#### NPN general purpose double transistor

# 12. Soldering



### NPN general purpose double transistor

# 13. Revision history

#### **Table 8. Revision history**

Supersedes
BC846S v.3
BC846S v.2
BC846S v.1
-

# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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