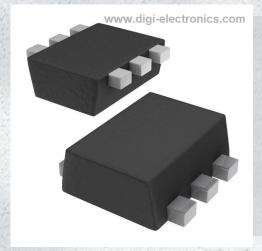


BC847BV,315 Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number BC847BV,315-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number BC847BV,315

Description TRANS 2NPN 45V 0.1A SOT666

Detailed Description Bipolar (BJT) Transistor Array 2 NPN (Dual) 45V 100

mA 100MHz 300mW Surface Mount SOT-666



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RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
BC847BV,315	Nexperia USA Inc.
Series:	Product Status:
	Not For New Designs
Transistor Type:	Current - Collector (Ic) (Max):
2 NPN (Dual)	100mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
45V	300mV @ 5mA, 100mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
15nA (ICBO)	200 @ 2mA, 5V
Power - Max:	Frequency - Transition:
300mW	100MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
SOT-563, SOT-666	SOT-666
Base Product Number:	
BC847	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0075	



BC847BV

NPN general purpose double transistor

27 December 2022

Product data sheet

1. General description

NPN double transistor in an ultra small SOT666 flat lead plastic package.

PNP complement: BC857BV

2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm x 0.55 mm ultra thin package
- Excellent coplanarity due to straight leads
- Low collector capacitance
- Improved thermal behaviour due to flat leads
- Reduces number of components as replacement of two SC-75/SC-89 packaged BISS transistors
- Reduces required board space
- · Reduces pick and place costs

3. Applications

· General purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
V _{CEO}	collector-emitter voltage	open base	-	-	45	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	200	-	450	

5. Pinning information

Table 2. Pinning information

	abic 2.1 mining information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	E1	emitter TR1	6 5 4	C1 B2 E2				
2	B1	base TR1						
3	C2	collector TR2		(TR1 TR2)				
4	E2	emitter TR2						
5	B2	base TR2	1 2 3					
6	C1	collector TR1	SOT666	sym020				



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6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC847BV	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>

7. Marking

Table 4. Marking codes

Type number	Marking code
BC847BV	1F

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or			'	'	
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device	-					
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1] [2]	-	-	625	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1] [2]	-	-	416	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[2] Reflow soldering is the only recommended soldering method.

NPN general purpose double transistor

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	N	/lin	Тур	Max	Unit
Per transisto	or						
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-		-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-		-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-		-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	2	200	-	450	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-		-	100	mV
satu	saturation voltage	I_{C} = 100 mA; I_{B} = 5 mA; pulsed; $t_{p} \le$ 300 µs; $\delta \le$ 0.02; T_{amb} = 25 °C	-		-	300	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-		755	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	5	80	655	700	mV
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	-		-	1.5	pF
C _e	emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-		11	-	pF
f _T	transition frequency	$V_{CE} = 5 \text{ V; } I_{C} = 10 \text{ mA; } f = 100 \text{ MHz;}$ $T_{amb} = 25 \text{ °C}$	1	100	-	-	MHz

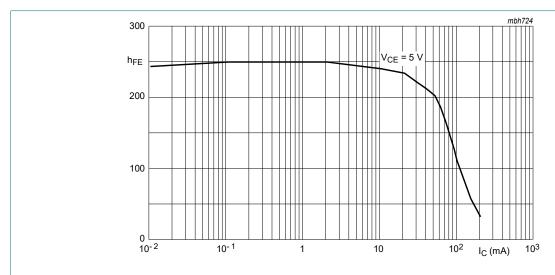
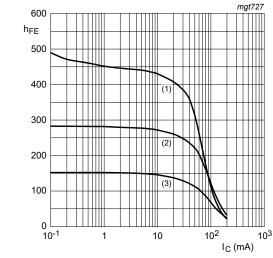


Fig. 1. DC current gain as a function of collector current; typical values

NPN general purpose double transistor



$$V_{CE} = 5 V$$

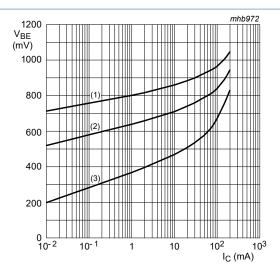
$$(1) T_{amb} = 150 ° ($$

$$V_{CE} = 5 V$$

(1) $T_{amb} = 150 \,^{\circ}C$
(2) $T_{amb} = 25 \,^{\circ}C$
(3) $T_{amb} = -55 \,^{\circ}C$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$



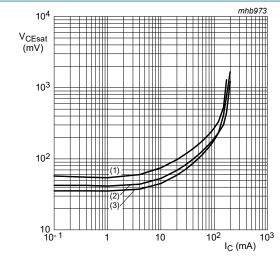


$$V_{CF} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

Base-emitter voltage as a function of collector current; typical values



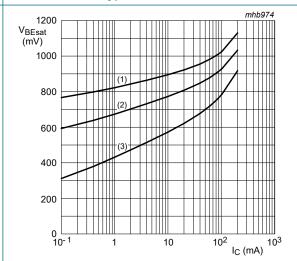
$$I_{\rm C}/I_{\rm B}=20$$

$$(1) T_{amb} = 150 °C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Collector-emitter saturation voltage as a Fig. 4. function of collector current; typical values



$$I_{\rm C}/I_{\rm B}=20$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

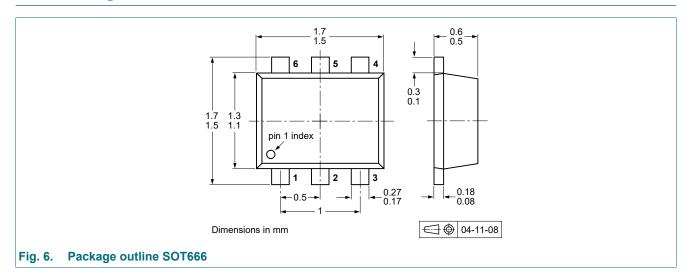
(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

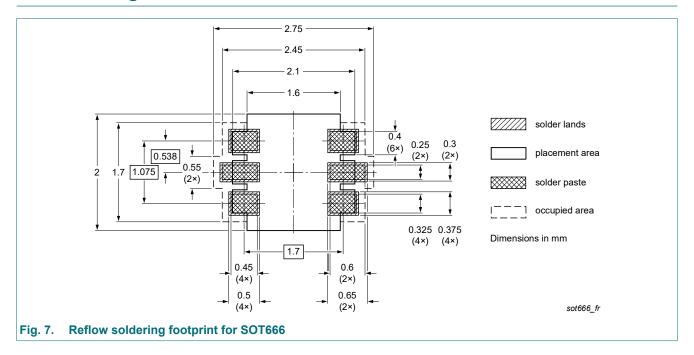
Base-emitter saturation voltage as a function of Fig. 5. collector current; typical values

NPN general purpose double transistor

11. Package outline



12. Soldering



NPN general purpose double transistor

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC847BV v.2	20221227	Product data sheet	-	BC847BV v.1
Modifications:	Nexperia. • Legal texts have	this data sheet has been rede ve been adapted to the new c anged to non-automotive quali	ompany name where a	
BC847BV v.1	20010910	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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NPN general purpose double transistor

BC847BV

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