

BCP53-16T-QX Datasheet



DiGi Electronics Part Number BCP53-16T-QX-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number BCP53-16T-QX

Description BCP53-16T-Q/SOT223/SC-73

Detailed Description Bipolar (BJT) Transistor PNP 80 V 1 A 140MHz 600 m

W Surface Mount SOT-223

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
BCP53-16T-QX	Nexperia USA Inc.
Series:	Product Status:
BCP53T-Q	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	1 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
80 V	500mV @ 50mA, 500mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA (ICBO)	100 @ 150mA, 2V
Power - Max:	Frequency - Transition:
600 mW	140MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-261-4, TO-261AA	SOT-223

Environmental & Export classification

8541.29.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



Product data sheet

1. General description

PNP medium power transistors in a medium power SOT223 (SC73) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package	NPN comlement	
	Nexperia	JEDEC	
BCP53T-Q	SOT223	SC-73	BCP56T-Q
BCP53-10T-Q			BCP56-10T-Q
BCP53-16T-Q			BCP56-16T-Q

2. Features and benefits

- High collector current capability I_C and I_{CM}
- Three current gain selections
- · High power dissipation capability
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Linear voltage regulators
- MOSFET drivers
- · High-side switches
- Power management
- Amplifiers

4. Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-80	V
I _C	collector current			-	-	-1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-2	А
h _{FE}	DC current gain		,				
	BCP53T-Q	$V_{CE} = -2 \text{ V}; I_{C} = -150 \text{ mA}$	[1]	63	-	250	
	BCP53-10T-Q		[1]	63	-	160	
	BCP53-16T-Q		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	- O
2	С	collector		B—
3	Е	emitter		
4	С	collector	☐1 ☐2 ☐3	Ë sym132

6. Ordering information

Table 4. Ordering information

Type number	Package	Package				
	Name	Description	Version			
BCP53T-Q	SC-73	plastic, surface-mounted package with increased heatsink;	SOT223			
BCP53-10T-Q	-	4 leads				
BCP53-16T-Q	1					

7. Marking

Table 5. Marking

Type number	Marking code
BCP53T-Q	BCP53T
BCP53-10T-Q	P5310T
BCP53-16T-Q	P5316T

8. Limiting values

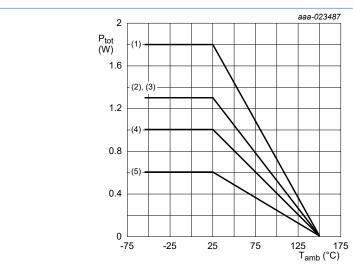
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		-100	V
V _{CEO}	collector-emitter voltage	open base		-	-80	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-2	Α
I _B	base current			-	-0.2	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-0.3	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.6	W
			[2]	-	1	W
			[3]	-	1.3	W
			[4]	-	1.3	W
			[5]	-	1.8	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².
- [4] Device mounted on an FR4 Printed-Circuit-Board (PCB), 4-layer copper; tin-plated and standard footprint.
- [5] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm.²



- (1) FR4 PCB; 4-layer copper; 1 cm²
- (2) FR4 PCB; single-sided copper; 6 cm²
- (3) FR4 PCB; 4-layer copper; standard footprint
- (4) FR4 PCB; single-sided copper; 1 cm²
- (5) FR4 PCB; single-sided copper; standard footprint

Fig. 1. Power derating curves

BCP53T-Q series

80 V, 1 A PNP medium power transistors

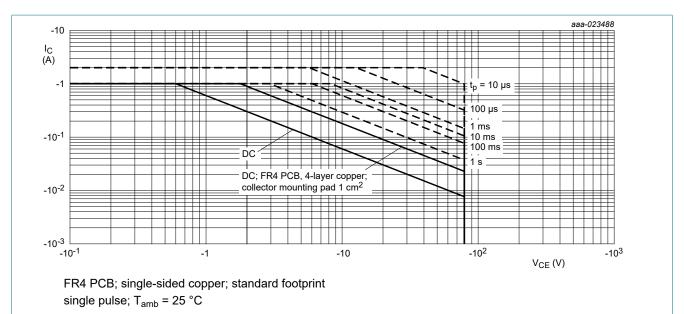


Fig. 2. Safe operating area; junction to ambient; continous and peak collector currents as a funtion of collecoremitter voltage

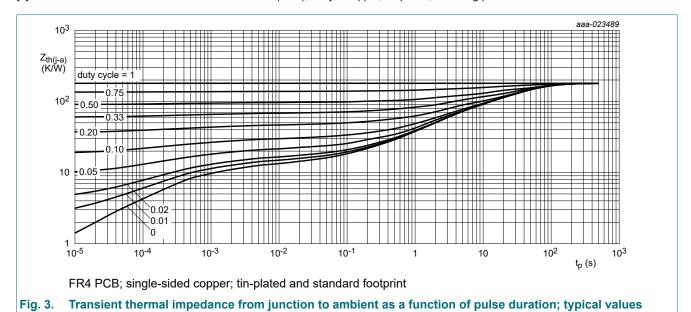
9. Thermal characteristics

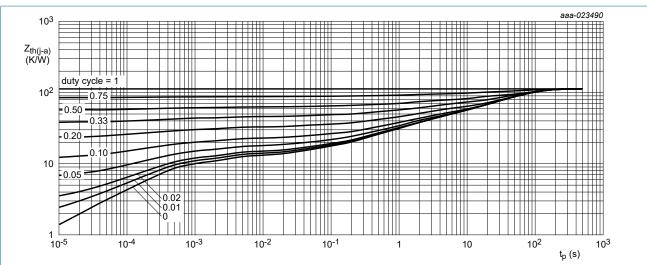
Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	209	K/W
			[2]			125	K/W
			[3]			97	K/W
			[4]	-	-	97	K/W
			[5]	-	-	70	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	18	K/W

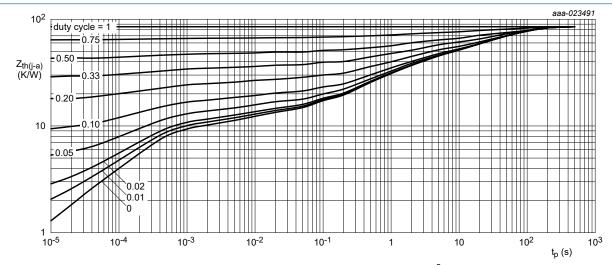
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm². [2] [3]
- Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm².





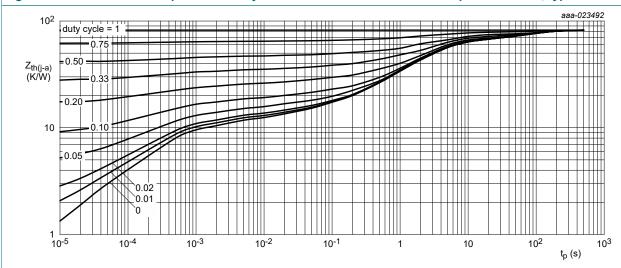
FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm²

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



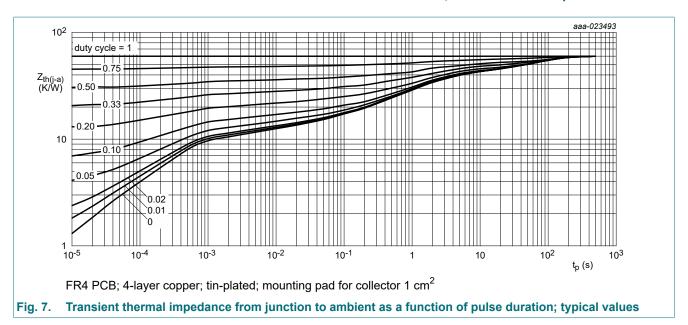
FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm²

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; 4-layer copper; tin-plated and standard footprint

Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



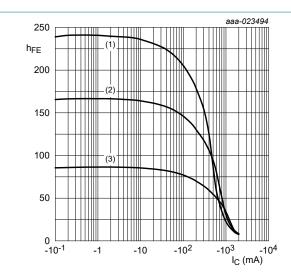
10. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-100	-		V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = -2 \text{ mA}; I_E = 0 \text{ A}$		-80	-		V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -100 μA; I _C = 0 A	I _E = -100 μA; I _C = 0 A		-		V
I _{CBO}	collector-base	V _{CB} = -30 V; I _E = 0 A		-	-	-100	nA
	cut-off current	V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C		-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A		-	-	-100	nA
h _{FE}	DC current gain					1	
	BCP53T-Q, -10T-Q,	V _{CE} = -2 V; I _C = -5 mA		63	-	-	
	-16T-Q	V _{CE} = -2 V; I _C = -500 mA	[1]	40	-	-	
	BCP53T-Q	V _{CE} = -2 V; I _C = -150 mA	[1]	63	-	250	
	BCP53-10T-Q	V _{CE} = -2 V; I _C = -150 mA	[1]	63	-	160	
	BCP53-16T-Q	V _{CE} = -2 V; I _C = -150 mA	[1]	100	-	250	
V _{CEsat}	collector-emitter saturation voltage	I _C = -500 mA; I _B = -50 mA		-	-	-500	mV
V _{BE}	base-emitter voltage	V _{CE} = -2 V; I _C = -500 mA		-	-	-1	V
f _T	transition frequency	V _{CE} = -5 V; I _C = -50 mA; f = 100 MHz		100	140	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	7	-	pF

[1] pulsed; $t_p \le 300 \,\mu s$; $\delta \le 0.02$



$$V_{CE} = -2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 8. DC current gain as a function of collector current; typical values

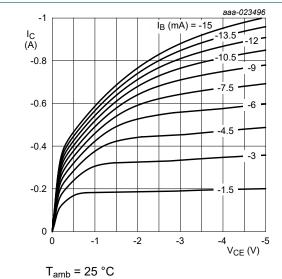
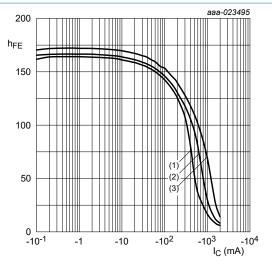


Fig. 10. Collector current as a function of collectoremitter voltage; typical values

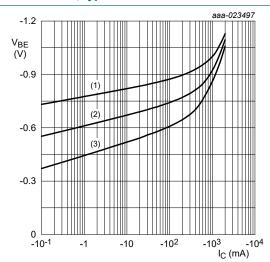


(1)
$$V_{CE} = -1 V$$

(2)
$$V_{CE} = -2 V$$

(3)
$$V_{CE} = -5 V$$

Fig. 9. DC current gain as a function of collector current; typical values

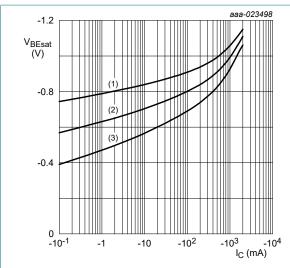


$$V_{CE} = -2 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

Fig. 11. Base-emitter voltage as a function of collector current; typical values



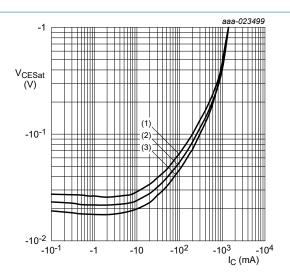
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 100 °C





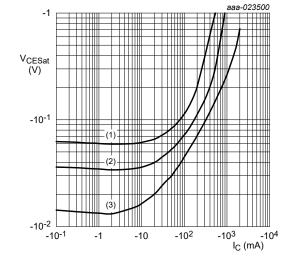
$$I_C/I_B = 10$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

function of collector current; typical values



 T_{amb} = 25 °C

(1) $I_C/I_B = 50$

(2) $I_C/I_B = 20$

(3) $I_C/I_B = 5$

Fig. 14. Collector-emitter saturation voltage as a function of collector current; typical values

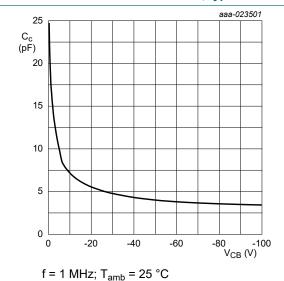


Fig. 15. Collector capacitance as a function of collectorbase voltage; typical values

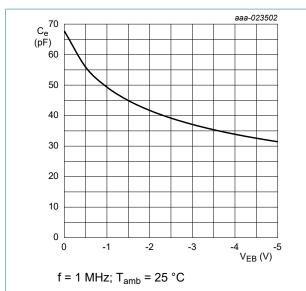


Fig. 16. Emitter capacitance as a function of emitterbase voltage; typical values

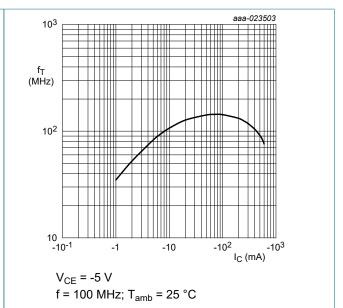


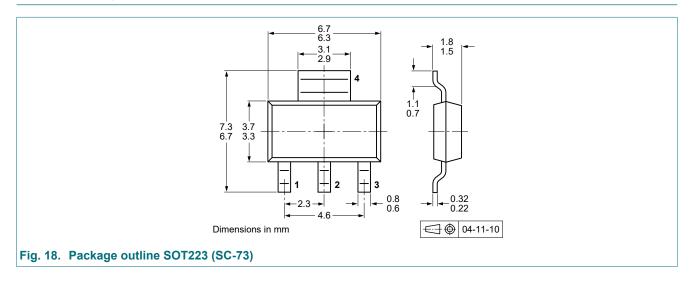
Fig. 17. Transition frequency as a function of collector current; typical values

11. Test information

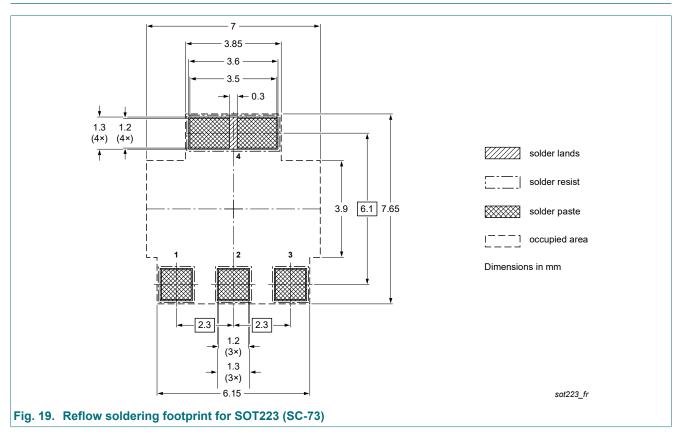
11.1. Quality information

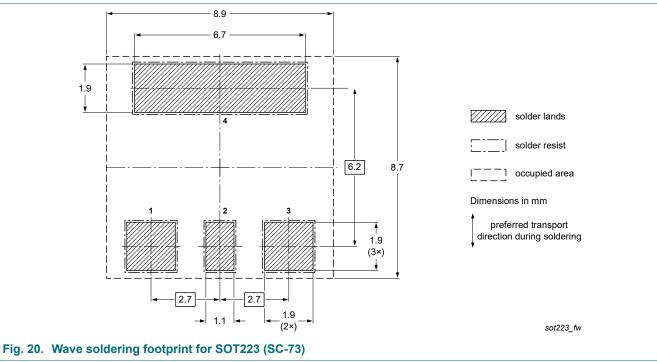
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





BCP53T-Q series

80 V, 1 A PNP medium power transistors

14. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP53T-Q_SER v.1	20230614	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BCP53T-Q series

80 V, 1 A PNP medium power transistors

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