

# **BCP68/ZLX Datasheet**

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DiGi Electronics Part Number BCP68/ZLX-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number BCP68/ZLX

Description TRANS NPN 20V 2A SOT223

**Detailed Description** Bipolar (BJT) Transistor NPN 20 V 2 A 170MHz 1.35 W

Surface Mount SOT-223



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# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
BCP68/ZLX	Nexperia USA Inc.
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
NPN	2 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
20 V	600mV @ 200mA, 2A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA (ICBO)	160 @ 500mA, 1V
Power - Max:	Frequency - Transition:
1.35 W	170MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-261-4, TO-261AA	SOT-223

# **Environmental & Export classification**

8541.29.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

**Product data sheet** 

### 1. General description

NPN medium power transistors in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- Two current gain selections
- · High power dissipation capability

# 3. Applications

- Linear voltage regulators
- MOSFET drivers
- Low-side switches
- Power management
- Amplifiers
- Battery-driven devices

### 4. Quick reference data

#### Table 1. Quick reference data

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base		-	-	20	V
Ic	collector current			-	-	2	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	3	Α
h <sub>FE</sub>	DC current gain					•	
	BCP68	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	85	-	375	
	BCP68-25		[1]	160	-	375	

[1] pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 



20 V, 2 A NPN medium power transistors

# 5. Pinning information

#### Table 2. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	C; C
2	С	collector		В
3	E	emitter		
4	С	collector	∃1 ∃2 ∃3	Ė
				sym016

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
BCP68	SC-73	plastic, surface-mounted package with increased heatsink; 4	SOT223
BCP68-25		leads	

# 7. Marking

#### Table 4. Marking

Type number	Marking code
BCP68	BCP68
BCP68-25	BCP68/25

#### 20 V, 2 A NPN medium power transistors

# 8. Limiting values

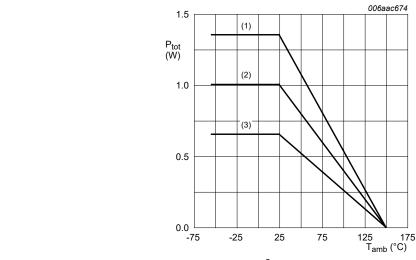
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	32	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	20	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	2	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	3	Α
I <sub>B</sub>	base current				0.4	Α
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	0.4	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>
- (2) FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves

#### 20 V, 2 A NPN medium power transistors

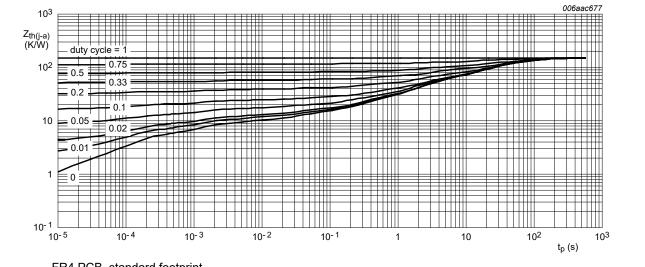
#### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

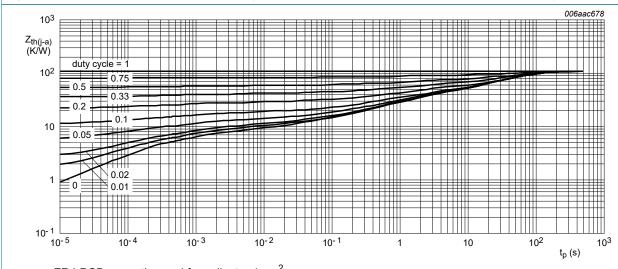
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]			125	K/W
			[3]			93	K/W
R <sub>(j-sp)</sub>	thermal resistance from junction to solder point			-	-	16	K/W

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



FR4 PCB, standard footprint

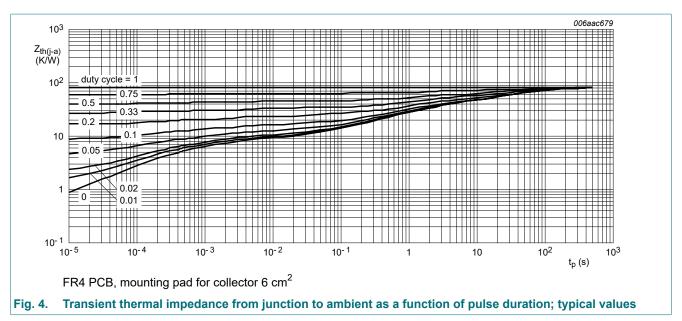
Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 2.



FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>

Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 20 V, 2 A NPN medium power transistors



#### 20 V, 2 A NPN medium power transistors

# 10. Characteristics

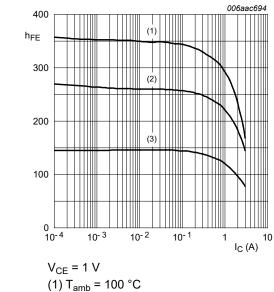
#### **Table 7. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		32	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 30 mA; I <sub>B</sub> = 0 A		20	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage			5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	10	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V; } I_{C} = 0 \text{ A}$		-	-	100	nA
h <sub>FE</sub>	DC current gain				·	'	'
	BCP68	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 5 mA	[1]	50	-	-	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	85	-	375	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 1 A	[1]	60	-	-	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 2 A	[1]	40	-	-	
	BCP68-25	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 5 mA	[1]	50	-	-	
		V <sub>CE</sub> =1 V; I <sub>C</sub> = 500 mA	[1]	160	-	375	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 1 A	[1]	60	-	-	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 2 A	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA	[1]	-	-	0.5	V
	saturation voltage	I <sub>C</sub> = 2 A; I <sub>B</sub> = 200 mA	[1]	-	-	0.6	V
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 5 mA	[1]	-	-	0.7	V
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 1 A	[1]	-	-	1	V
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	22	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz		40	170	-	MHz

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

#### 20 V, 2 A NPN medium power transistors

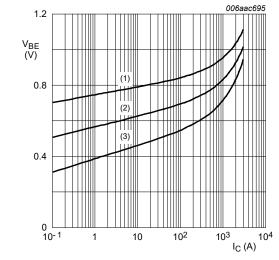


$$(1) T_{amb} = 100 °C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

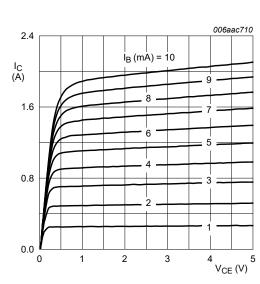
DC current gain as a function of collector Fig. 5. current; typical values



$$V_{CE} = 1 V$$

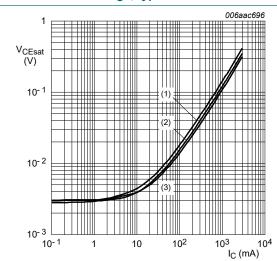
(1) 
$$T_{amb} = -55$$
 °C

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

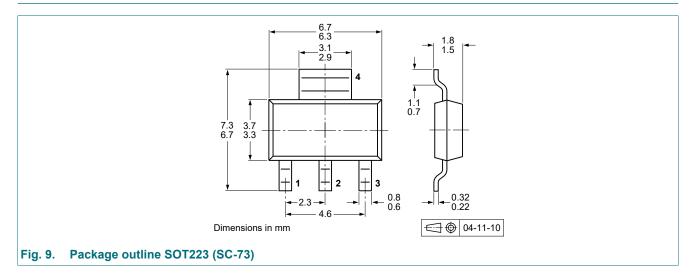
(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

20 V, 2 A NPN medium power transistors

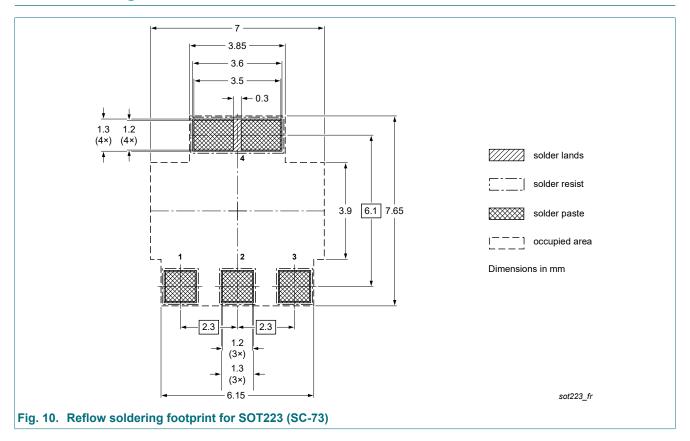
# 11. Package outline

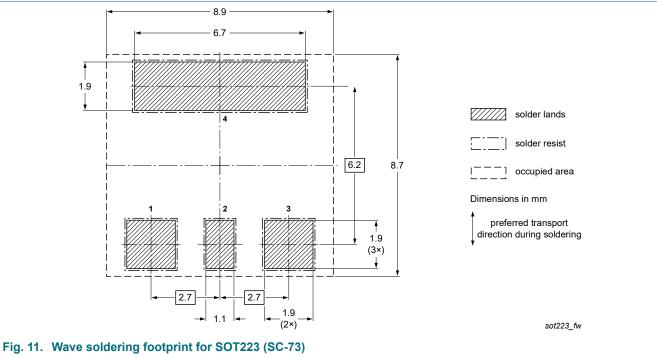


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#### 20 V, 2 A NPN medium power transistors

# 12. Soldering





### 20 V, 2 A NPN medium power transistors

# 13. Revision history

#### **Table 8. Revision history**

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Document ID	Release date	Data sheet status	Change notice	Supersedes					
BCP68_SER v.9	20230701	Product data sheet	_	BCP68_BC868_BC68PA v.8					
Modifications:	<ul><li>Section "Packing</li><li>Product(s) change</li></ul>	splitted to 3 data sheets. information" removed. ed to non-automotive qua roduct alternative(s).		ase refer to nexperia.com for					
BCP68_BC868_BC68PA v.8	20111018	Product data sheet		BC868 v.7					
BC868 v.7	20041108	Product specification	-	BC868 v.6					
BC868 v.6	20031202	Product specification	-	BC868 v.5					
BC868 v.5	19990408	Product specification	-	BC868 v.4					
BC868 v.4	19980716	Product specification	-	BC868_CNV v.3					
BC868_CNV v.3	19970319	Product specification	-	BC868_CNV v.2					
BC868_CNV v.2	19970307	Product specification	-						
BCP68 v.4	20031125	Product specification	-	BCP68 v.3					
BCP68 v.3	19990408	Product specification	-	BCP68_CNV v.2					
BCP68_CNV v.2	19970409	Product specification	-	-					

# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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# Nexperia

# **BCP68** series

#### 20 V, 2 A NPN medium power transistors

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