

PBSS2515M,315 Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number PBSS2515M,315-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PBSS2515M,315

Description TRANS NPN 15V 0.5A SOT883

Detailed Description Bipolar (BJT) Transistor NPN 15 V 500 mA 420MHz 4

30 mW Surface Mount SOT-883



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

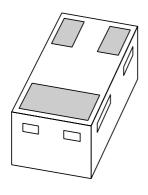
Manufacturer Product Number:	Manufacturer:
PBSS2515M,315	Nexperia USA Inc.
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN	500 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
15 V	250mV @ 50mA, 500mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA (ICBO)	150 @ 100mA, 2V
Power - Max:	Frequency - Transition:
430 mW	420MHz
Operating Temperature:	Mounting Type:
150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
SC-101, SOT-883	SOT-883
Base Product Number:	
PBSS2515	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0075	

DISCRETE SEMICONDUCTORS

DATA SHEET



PBSS2515M 15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

Product data sheet Supersedes data of 2003 Jun 17 2003 Sep 15



15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

PBSS2515M

FEATURES

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High efficiency leading to reduced heat generation
- Reduced printed-circuit board requirements.

APPLICATIONS

- Power management:
 - DC-DC converter
 - Supply line switching
 - Battery charger
 - LCD backlighting.
- · Peripheral driver:
 - Driver in low supply voltage applications (e.g. lamps and LEDs)
 - Inductive load drivers (e.g. relays, buzzers and motors).

DESCRIPTION

Low V_{CEsat} NPN transistor in a SOT883 leadless ultra small plastic package.

PNP complement: PBSS3515M.

MARKING

TYPE NUMBER	MARKING CODE
PBSS2515M	S2

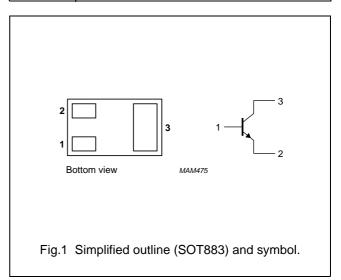
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage	15	٧
I _C	collector current (DC)	500	mA
I _{CM}	peak collector current	1	Α
R _{CEsat}	equivalent on-resistance <500 mg		mΩ

PINNING

2

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



2003 Sep 15

15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

PBSS2515M

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	15	V
V_{CEO}	collector-emitter voltage	open base	-	15	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
Ic	collector current (DC)	notes 1 and 2	_	500	mA
I _{CM}	peak collector current		_	1	Α
I _{BM}	peak base current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; notes 1 and 2	_	250	mW
		T _{amb} ≤ 25 °C; note 1 and 3	_	430	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Notes

- 1. Refer to SOT883 standard mounting conditions.
- 2. Device mounted on an FR4 printed-circuit board, single-sided copper, tinplated, standard footprint, with 60 μ m copper strip line.
- 3. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm².

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to	in free air; notes 1 and 2	500	K/W
	ambient	in free air; notes 1, 3 and 4	290	K/W

Notes

- 1. Refer to SOT883 standard mounting conditions.
- 2. Device mounted on an FR4 printed-circuit board, single-sided copper, tinplated, standard footprint, with 60 μm copper strip line.

3

- 3. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm².
- 4. Operated under pulsed conditions: duty cycle $\delta \le 20\%$, pulse width $t_0 \le 30$ ms.

Soldering

Reflow soldering is the only recommended soldering method.

2003 Sep 15

15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

PBSS2515M

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

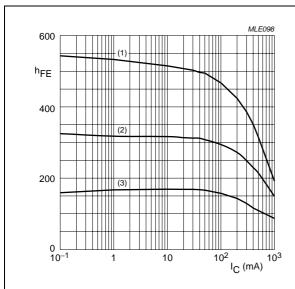
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 15 V; I _E = 0	_	_	100	nA
		V _{CB} = 15 V; I _E = 0; T _j = 150 °C	_	_	50	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0	-	_	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 10 mA	200	_	-	
		V _{CE} = 2 V; I _C = 100 mA; note 1	150	_	-	
		$V_{CE} = 2 \text{ V}; I_{C} = 500 \text{ mA}; \text{ note 1}$	90	_	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	-	_	25	mV
		$I_C = 200 \text{ mA}; I_B = 10 \text{ mA}; \text{ note 1}$	-	_	150	mV
		$I_C = 500 \text{ mA}$; $I_B = 50 \text{ mA}$; note 1	-	_	250	mV
R _{CEsat}	equivalent on-resistance	$I_C = 500 \text{ mA}$; $I_B = 50 \text{ mA}$; note 1	-	360	<500	mΩ
V _{BEsat}	base-emitter saturation voltage	$I_C = 500 \text{ mA}$; $I_B = 50 \text{ mA}$; note 1	-	_	1.1	V
V _{BEon}	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}; I_{C} = 100 \text{ mA}; \text{ note 1}$	-	_	0.9	V
f _T	transition frequency	I _C = 100 mA; V _{CE} = 5 V; f = 100 MHz	250	420	_	MHz
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = I_e = 0; f = 1 \text{ MHz}$	=	4.4	6	pF

Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

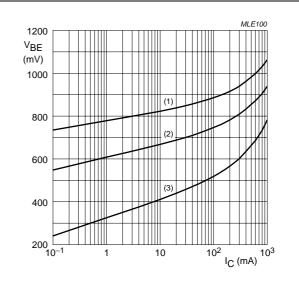
PBSS2515M



V_{CE} = 2 V.

- (1) $T_{amb} = 150 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

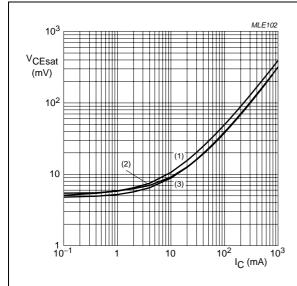
Fig.2 DC current gain as a function of collector current; typical values.



 $V_{CE} = 2 V$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

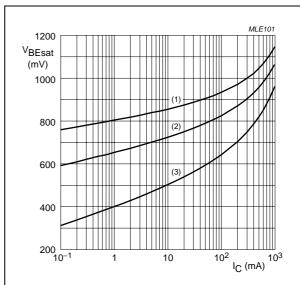
Fig.3 Base-emitter voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B} = 20.$

- (1) $T_{amb} = 150 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B} = 20.$

- (1) $T_{amb} = 150 \, ^{\circ}C.$
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.5 Base-emitter saturation voltage as a function of collector current; typical values.

15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

PBSS2515M

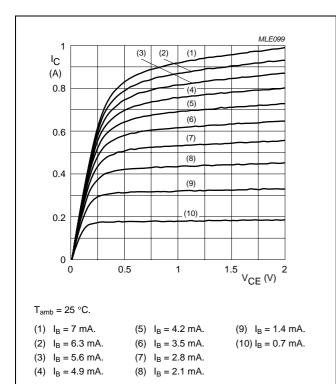
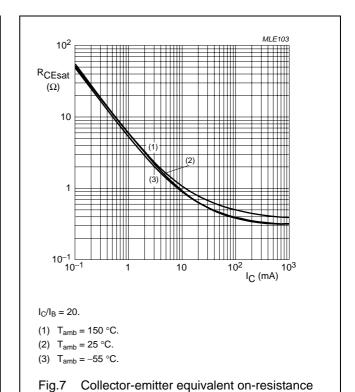


Fig.6 Collector current as a function of collector-emitter voltage; typical values.



as a function of collector current; typical

2003 Sep 15 6

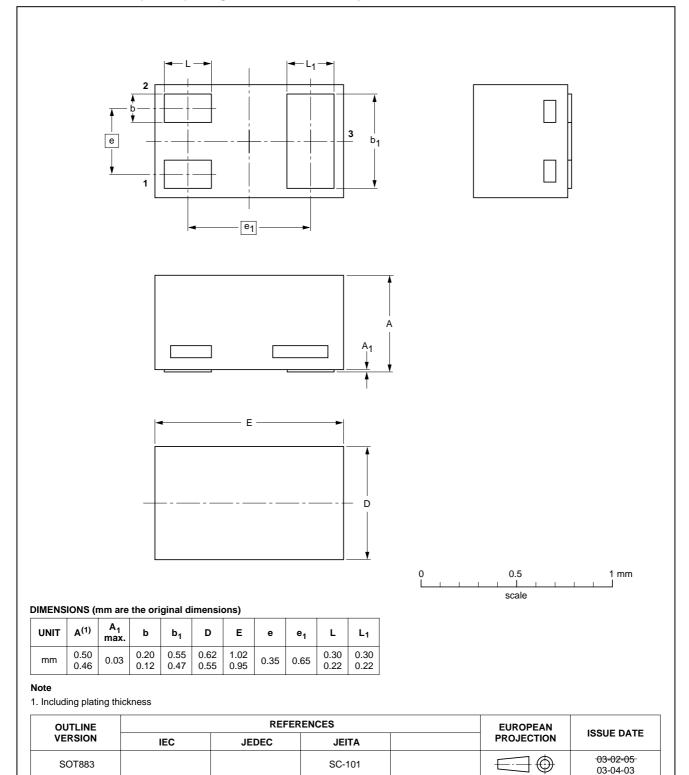
15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

PBSS2515M

PACKAGE OUTLINE

Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.5 mm

SOT883



15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

PBSS2515M

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

DISCLAIMERS

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

Contact information

For additional information please visit: http://www.nxp.com

For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands R75/03/pp9 Date of release: 2003 Sep 15 Document order number: 9397 750 11782





OUR CERTIFICATE

DiGi provide top-quality products and perfect service for customer worldwide through standardization, technological innovation and continuous improvement. DiGi through third-party certification, we striciy control the quality of products and services. Welcome your RFQ to Email: Info@DiGi-Electronics.com

















Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com