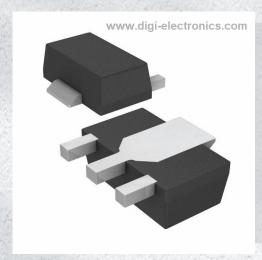


PBSS4160XF Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number PBSS4160XF-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PBSS4160XF

Description TRANS NPN 60V 1A SOT89

Detailed Description Bipolar (BJT) Transistor NPN 60 V 1 A 180MHz 1.35 W

Surface Mount SOT-89



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PBSS4160XF	Nexperia USA Inc.
Series:	Product Status:
-	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN	1 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
60 V	200mV @ 50mA, 500mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA (ICBO)	170 @ 500mA, 10V
Power - Max:	Frequency - Transition:
1.35 W	180MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-243AA	SOT-89
Base Product Number:	
PBSS4160	

Environmental & Export classification

8541.29.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



PBSS4160X

60 V, 1 A NPN low VCEsat BISS transistor

23 May 2017

Product data sheet

1. General description

NPN low V_{CEsat} Breakthrough in Smal Signal (BISS) transitor in a medium power SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- · High energy efficiency due to less heat generation
- AEC-Q101 qualified

3. Applications

- DC-to-DC conversion
- Supply line switches
- · Battery charger
- · LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)
- · Inductive load driver (e.g. relays, buzzers and motors)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	60	V
I _C	collector current			-	-	1	Α
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$		-	-	2	Α
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	170	-	360	

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$



60 V, 1 A NPN low VCEsat BISS transistor

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		С
2	С	collector		В
3	В	base	3 2 1	E
			SOT89	sym123

6. Ordering information

Table 3. Ordering information

Type number	Package	ckage				
	Name	Description	Version			
PBSS4160X	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89			

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4160X	S41

60 V, 1 A NPN low VCEsat BISS transistor

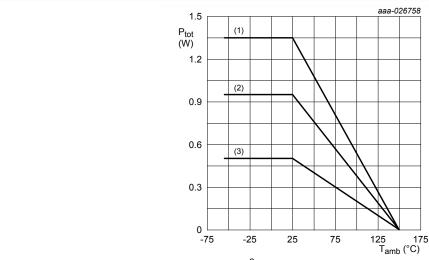
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	60	V
V_{CEO}	collector-emitter voltage	open base		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	7	V
I _C	collector current			-	1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	Α
I _B	base current			-	300	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	1	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	500	mW
			[2]	-	950	mW
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 6 cm².



- (1) FR4 PCB, single-sided copper, 6 cm²
- (2) FR4 PCB, single-sided copper, 1 cm²
- (3) FR4 PCB, single-sided copper, standard footprint

Fig. 1. **Power derating curves**

60 V, 1 A NPN low VCEsat BISS transistor

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$		in free air	[1]	-	-	250	K/W
	from junction to ambient		[2]	-	-	132	K/W
3.1.5.5.11	1	<u>[3]</u>	-	-	93	K/W	

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm²
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 6 cm².

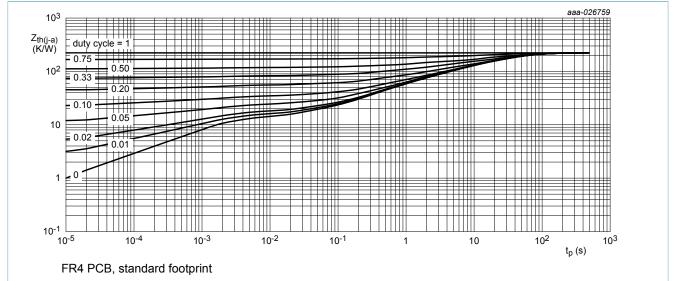
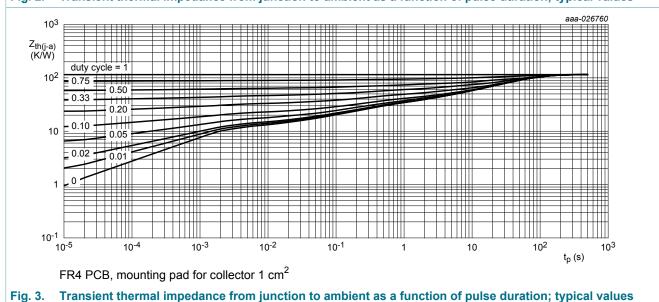
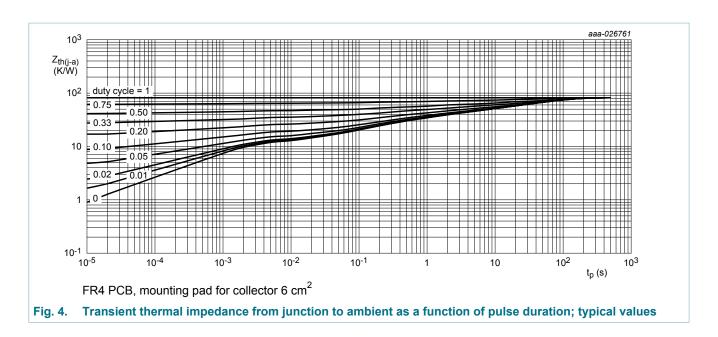


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



60 V, 1 A NPN low VCEsat BISS transistor



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60 V, 1 A NPN low VCEsat BISS transistor

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V_{CB} = 48 V; I_{E} = 0 A; T_{amb} = 25 °C		-	-	100	nA
	current	V _{CB} = 48 V; I _E = 0 A; T _j = 150 °C		-	-	10	μΑ
I _{CES}	collector-emitter cut-off current	$V_{CE} = 48 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ °C}$		-	-	100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	100	nA
h _{FE}	DC current gain	V_{CE} = 10 V; I_{C} = 500 mA; T_{amb} = 25 °C	[1]	170	-	360	
		V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C	[1]	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 500 mA; I_B = 50 mA; T_{amb} = 25 °C	[1]	-	-	200	mV
R _{CEsat}	collector-emitter saturation resistance			-	-	0.4	Ω
V _{BEsat}	base-emitter saturation voltage		[1]	-	-	1.2	V
V_{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C	[1]	-	-	1	V
f _T	transition frequency	V_{CE} = 10 V; I_{C} = 50 mA; f = 100 MHz; T_{amb} = 25 °C		-	180	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	6	-	pF

[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$

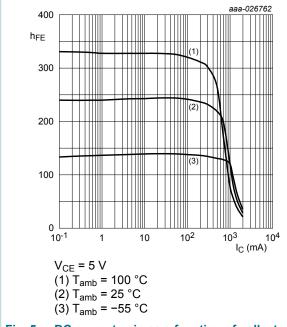


Fig. 5. DC current gain as a function of collector current; typical values

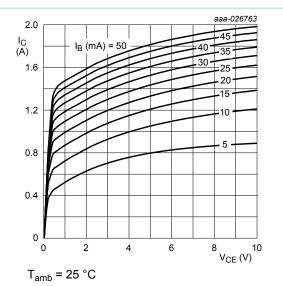


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

60 V, 1 A NPN low VCEsat BISS transistor

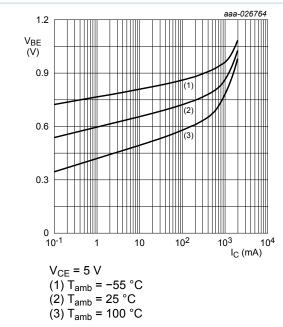


Fig. 7. Base-emitter voltage as a function of collector current; typical values

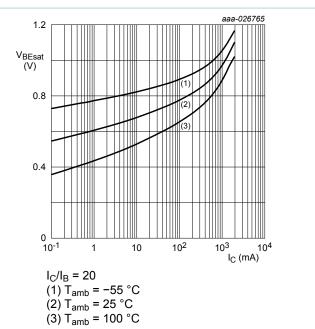


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

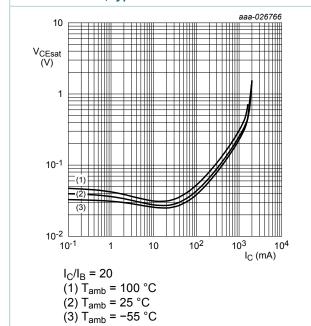


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

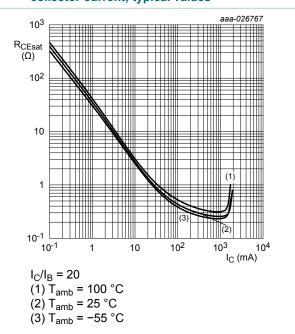
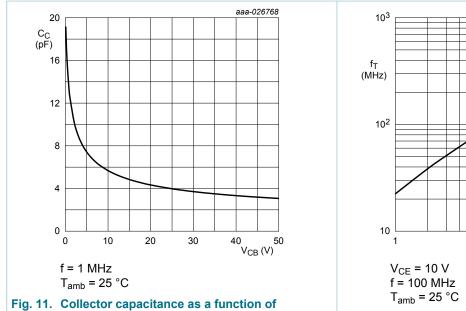


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

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60 V, 1 A NPN low VCEsat BISS transistor



collector-base voltage; typical values

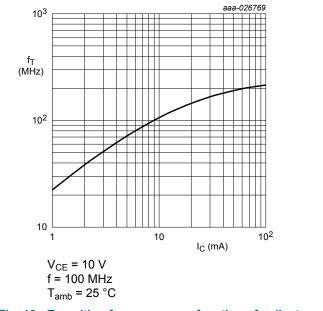


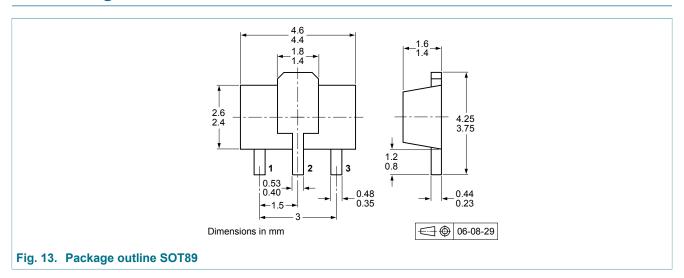
Fig. 12. Transition frequency as a function of collector current; typical values

11. Test information

Quality information

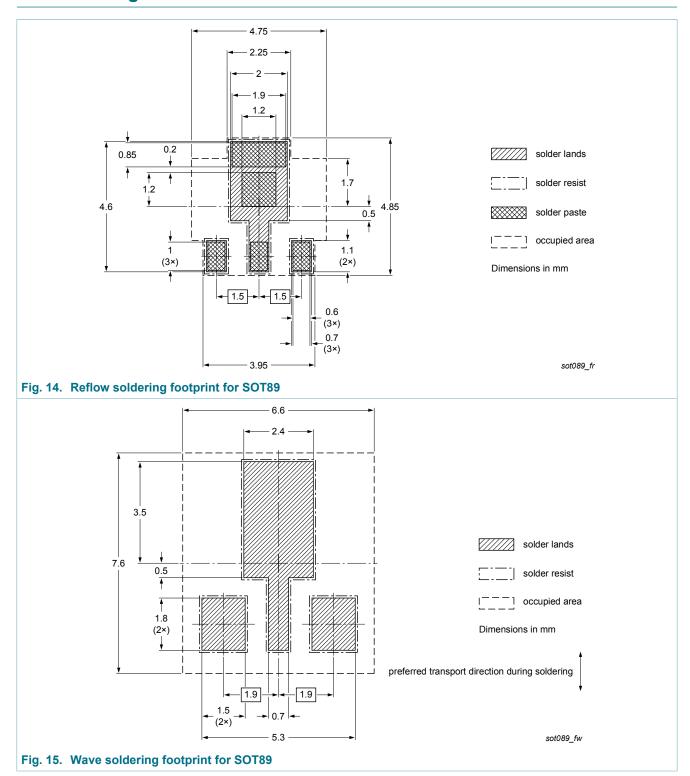
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



60 V, 1 A NPN low VCEsat BISS transistor

13. Soldering



60 V, 1 A NPN low VCEsat BISS transistor

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4160X v.1	20170523	Product data sheet	-	-

60 V, 1 A NPN low VCEsat BISS transistor

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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