PBSS5160U,115 Datasheet

https://www.DiGi-Electronics.com

DiGi Electronics Part Number	PBSS5160U,115-DG
Manufacturer	Nexperia USA Inc.
Manufacturer Product Number	PBSS5160U,115
Description	TRANS PNP 60V 0.7A SOT323
Detailed Description	Bipolar (BJT) Transistor PNP 60 V 700 mA 185MHz 415 mW Surface Mount SOT-323

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PBSS5160U,115	Nexperia USA Inc.
Series:	Product Status:
-	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	700 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ Ib, Ic:
60 V	340mV @ 100mA, 1A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA	150 @ 500mA, 5V
Power - Max:	Frequency - Transition:
415 mW	185MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q100	Surface Mount
Package / Case:	Supplier Device Package:
SC-70, SOT-323	SOT-323
Base Product Number:	
PBSS5160	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0075	

PBSS5160U

60 V, 1 A PNP low VCEsat transistor

9 November 2023 **Product data sheet**

1. General description

PNP low V_{CEsat} transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4160U

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

3. Applications

- High voltage DC-to-DC conversion
- High voltage MOSFET gate driving
- High voltage motor control
- High voltage power switches (e.g. motors, fans)

4. Quick reference data

Table 1. Quick reference data

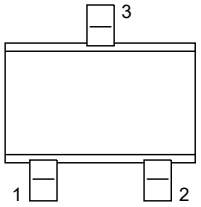
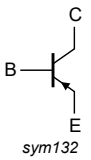
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-60	V
I_C	collector current		[1]	-	-	-1	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-	-2	A
R_{CEsat}	collector-emitter saturation resistance	I_C = -1 A; I_B = -100 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	255	340	mΩ

[1] Device mounted on a ceramic PCB, Al_2O_3, standard footprint.

nexperia

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-70 (SOT323)	 *sym132*
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS5160U	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS5160U	53%

[1] % = placeholder for manufacturing site code

8. Limiting values

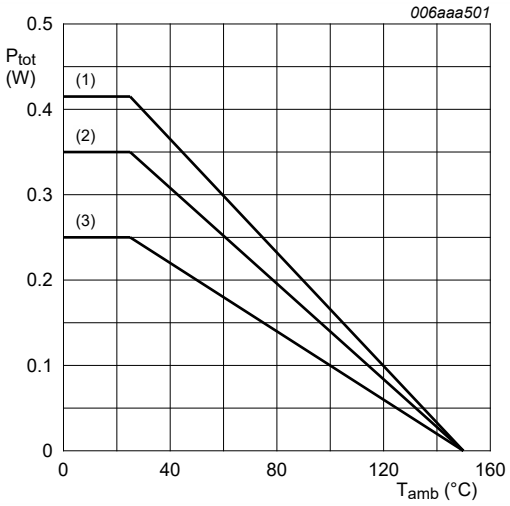
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-80	V
V_{CEO}	collector-emitter voltage	open base		-	-60	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current		[1]	-	-0.7	A
			[2]	-	-0.86	A
			[3]	-	-1	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	-2	A
I_B	base current			-	-300	mA
I_{BM}	peak base current	single pulse; $t_p \leq 1$ ms		-	-1	A
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	250	mW
			[2]	-	350	mW
			[3]	-	415	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C

Symbol	Parameter	Conditions		Min	Max	Unit
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm^2.
[3] Device mounted on a ceramic PCB, Al_2O_3, standard footprint.



(1) Ceramic PCB, Al_2O_3, standard footprint
(2) FR4 PCB, mounting pad for collector 1 cm^2
(3) FR4 PCB, standard footprint

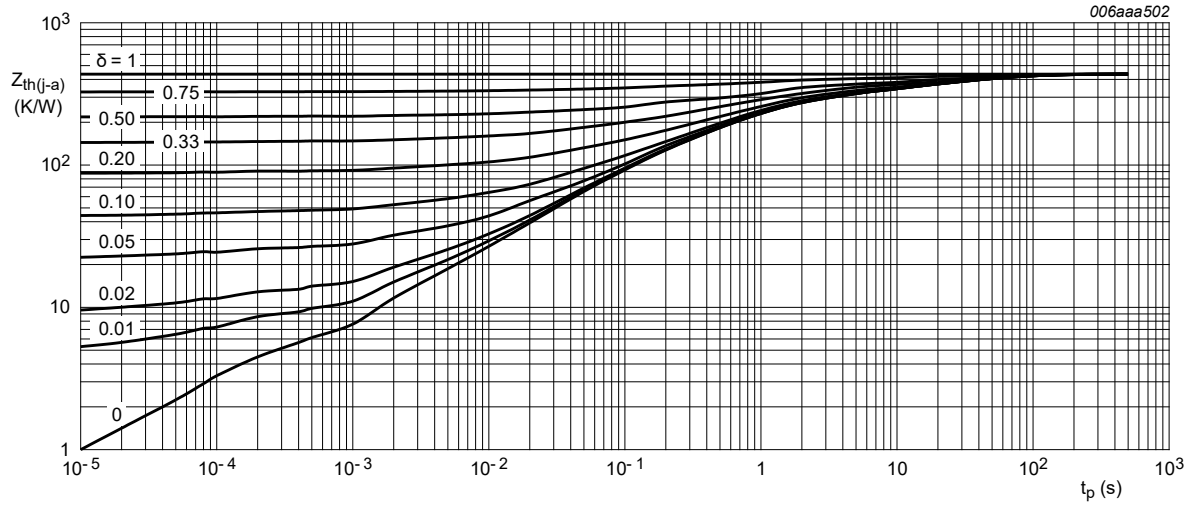
Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

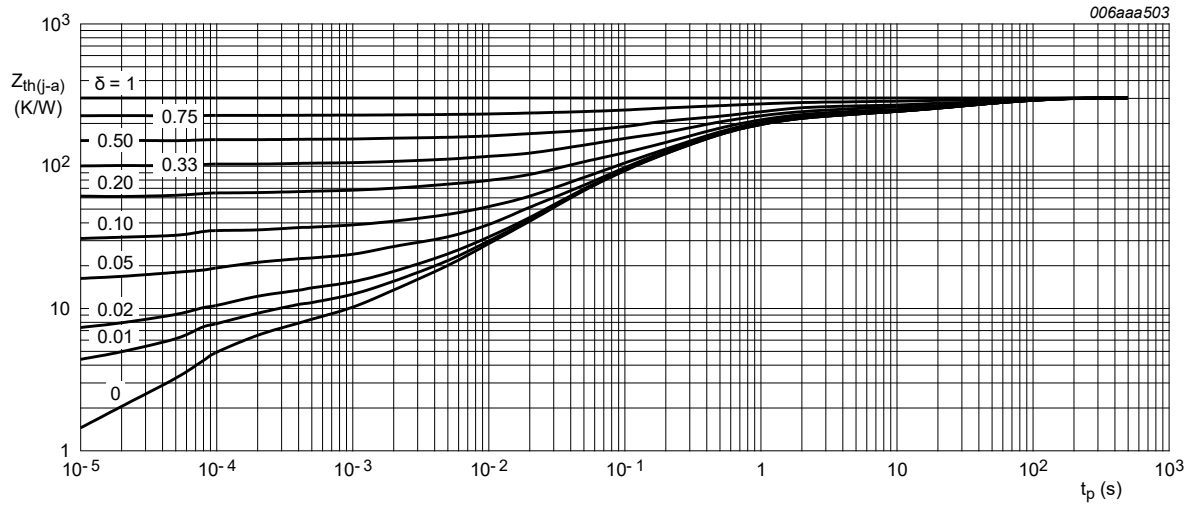
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
			[2]	-	-	357	K/W
			[3]	-	-	301	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	150	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm^2.
[3] Device mounted on a ceramic PCB, Al_2O_3, standard footprint.



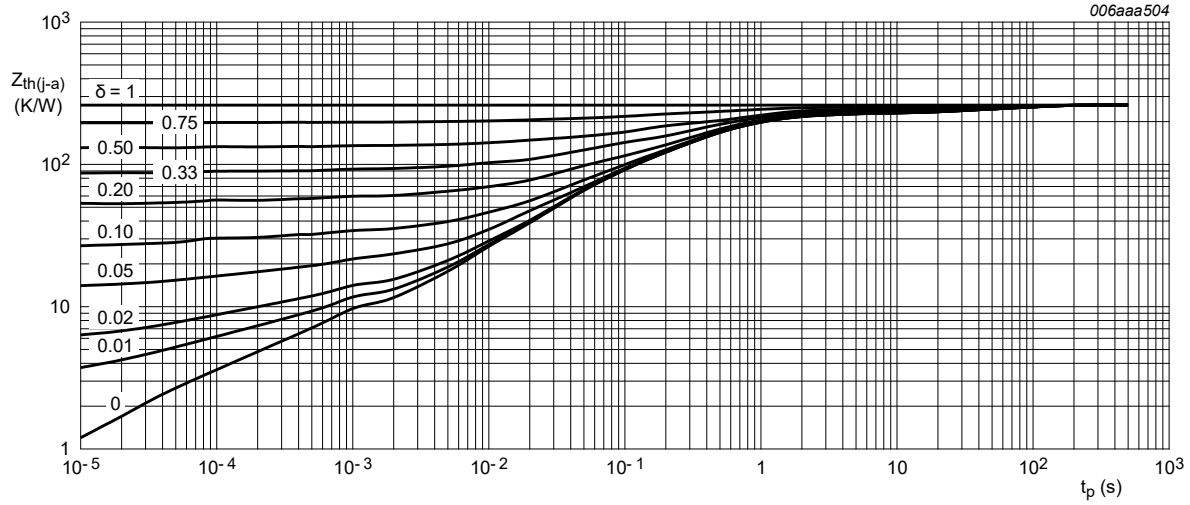
FR4 PCB, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 1 cm^2

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



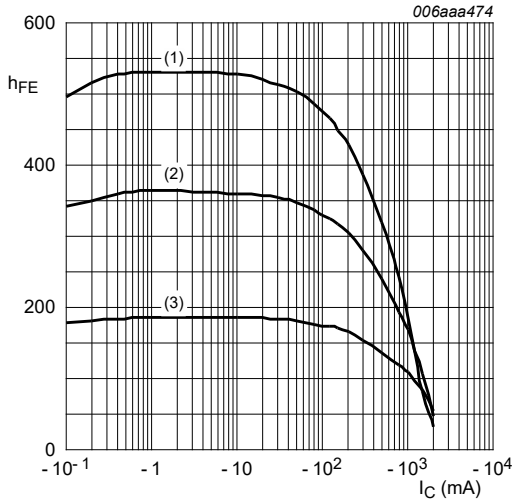
Ceramic PCB, Al_2O_3, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

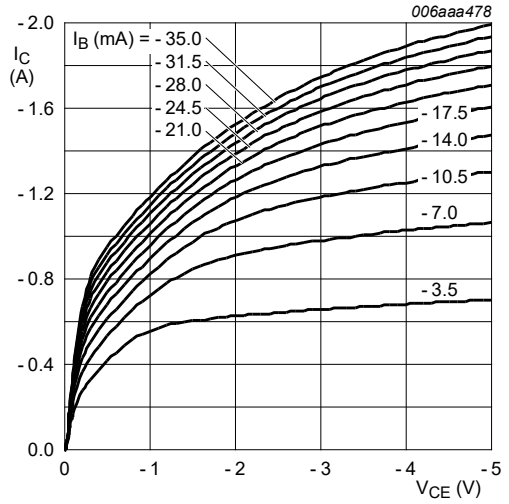
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -60 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-100	nA
		V_{CB} = -60 V; I_E = 0 A; T_j = 150 °C		-	-	-50	µA
I_{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-100	nA
I_{CES}	collector-emitter cut-off current	V_{CE} = -60 V; V_{BE} = 0 V; T_{amb} = 25 °C		-	-	-100	nA
h_{FE}	DC current gain	V_{CE} = -5 V; I_C = -1 mA; T_{amb} = 25 °C		200	350	-	
		V_{CE} = -5 V; I_C = -500 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		150	250	-	
		V_{CE} = -5 V; I_C = -1 A; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		100	160	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -100 mA; I_B = -1 mA; T_{amb} = 25 °C		-	-110	-175	mV
		I_C = -500 mA; I_B = -50 mA; T_{amb} = 25 °C		-	-135	-180	mV
		I_C = -1 A; I_B = -100 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	-255	-340	mV
R_{CEsat}	collector-emitter saturation resistance			-	255	340	mΩ
V_{BEsat}	base-emitter saturation voltage	I_C = -1 A; I_B = -50 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	-0.95	-1.1	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = -5 V; I_C = -1 A; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	-0.82	-0.9	V
t_d	delay time	I_C = -0.5 A; I_{Bon} = -25 mA; I_{Boff} = 25 mA; T_{amb} = 25 °C		-	11	-	ns
t_r	rise time			-	30	-	ns
t_{on}	turn-on time			-	41	-	ns
t_s	storage time			-	205	-	ns
t_f	fall time			-	55	-	ns
t_{off}	turn-off time			-	260	-	ns
f_T	transition frequency	V_{CE} = -10 V; I_C = -50 mA; f = 100 MHz; T_{amb} = 25 °C		150	185	-	MHz
C_c	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	9	15	pF



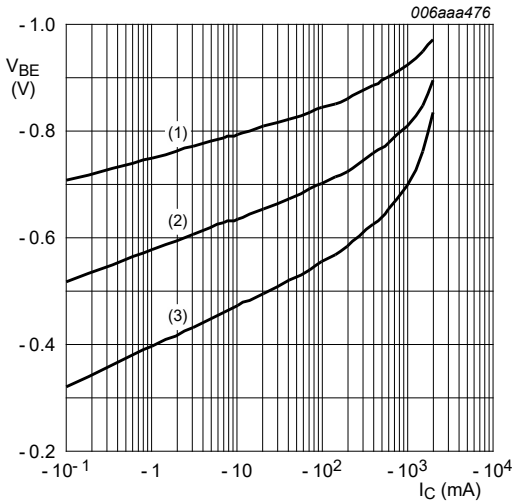
V_{CE} = -5 V
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 5. DC current gain as a function of collector current; typical values



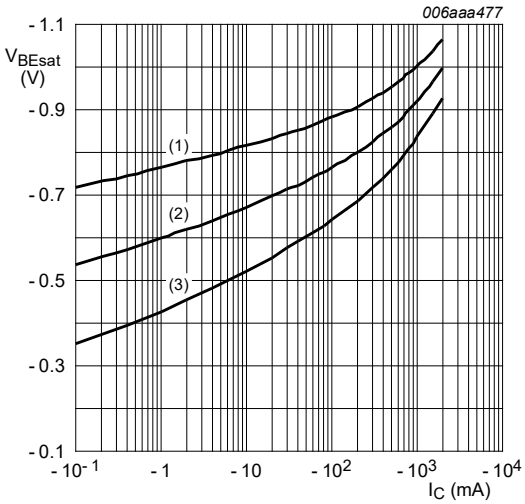
T_{amb} = 25 °C

Fig. 6. Collector current as a function of collector-emitter voltage; typical values



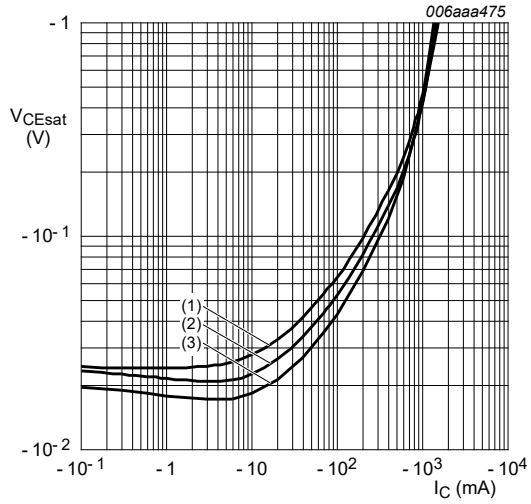
V_{CE} = -5 V
(1) T_{amb} = -55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 7. Base-emitter voltage as a function of collector current; typical values



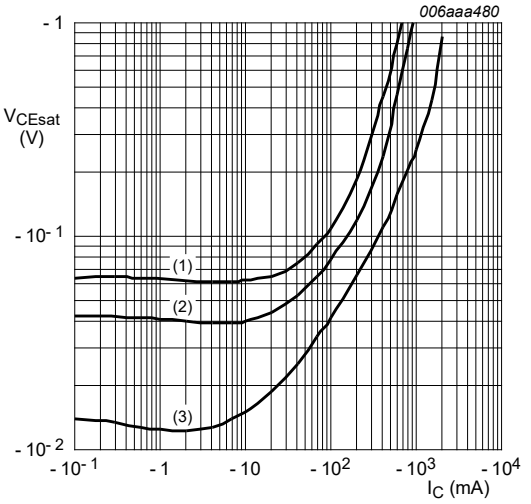
I_C/I_B = 20
(1) T_{amb} = -55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



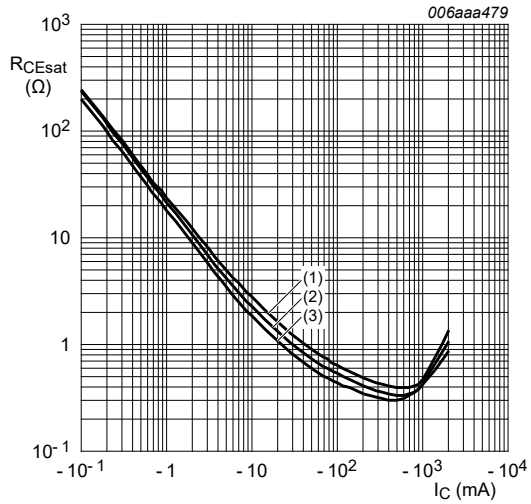
I_C/I_B = 20
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



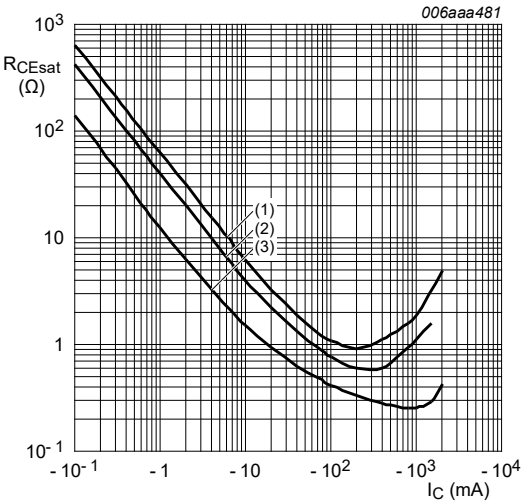
T_{amb} = 25 °C
(1) I_C/I_B = 100
(2) I_C/I_B = 50
(3) I_C/I_B = 10

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



I_C/I_B = 20
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values



T_{amb} = 25 °C
(1) I_C/I_B = 100
(2) I_C/I_B = 50
(3) I_C/I_B = 10

Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

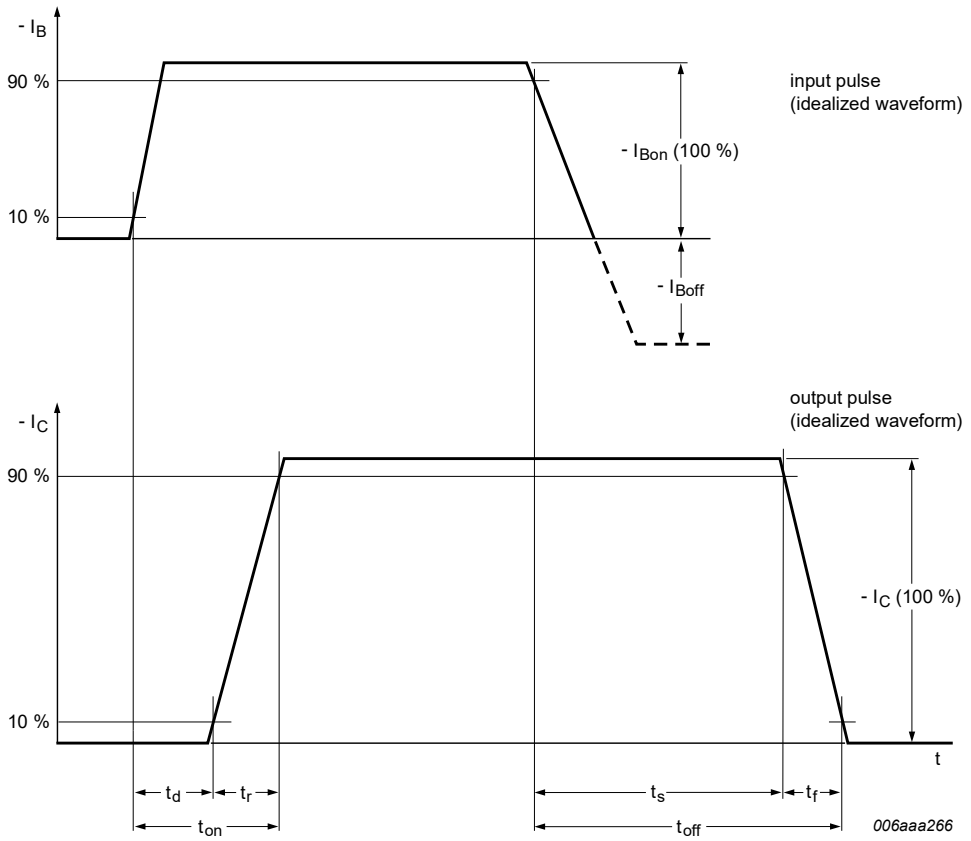


Fig. 13. Transistor switching time definition

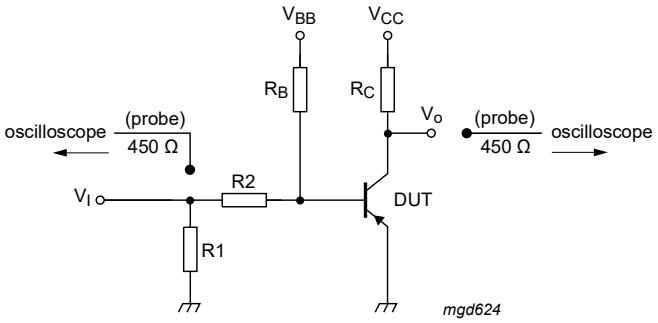


Fig. 14. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

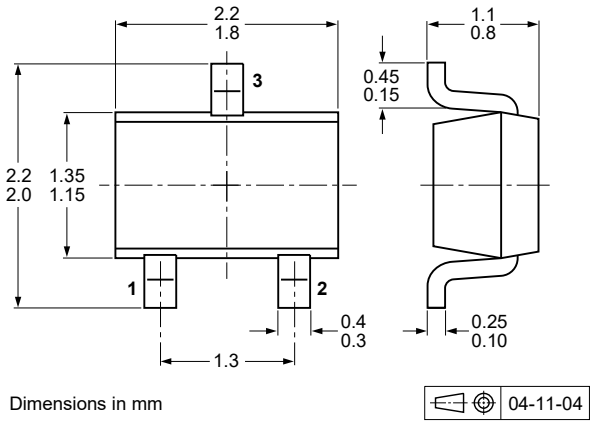


Fig. 15. Package outline SC-70 (SOT323)

13. Soldering

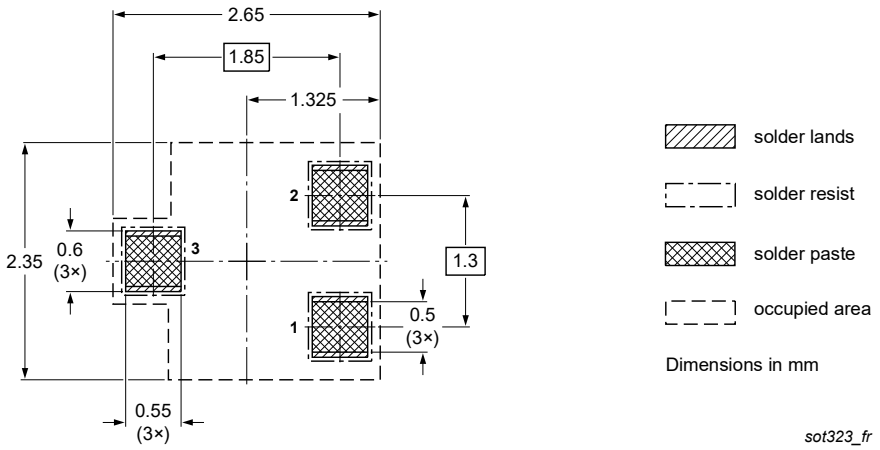


Fig. 16. Reflow soldering footprint for SC-70 (SOT323)

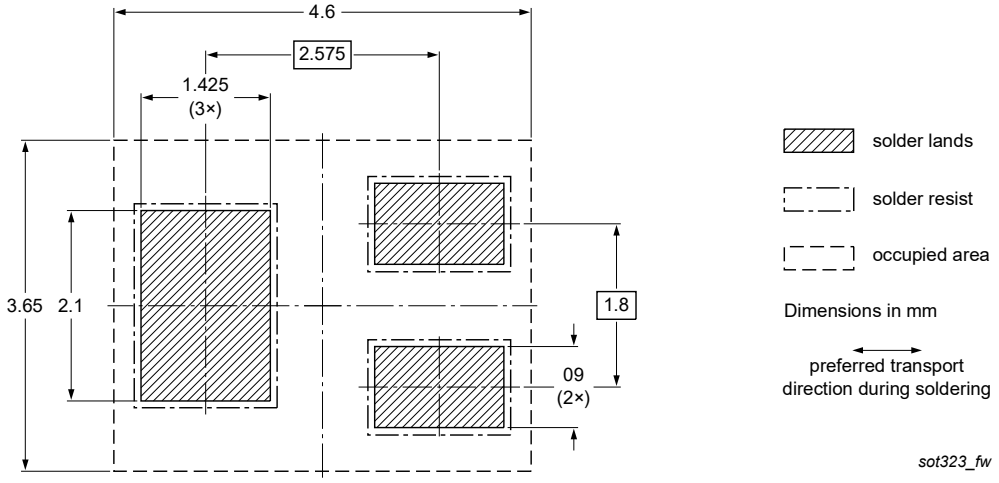


Fig. 17. Wave soldering footprint for SC-70 (SOT323)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS5160U v.5	20231109	Product data sheet	-	PBSS5160U v.4
Modifications:	• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate. • Packing information removed.			
PBSS5160U v.4	20081002	Product data sheet	-	PBSS5160U_3
PBSS5160U_3	20050811	Product data sheet	-	PBSS5160U_2
PBSS5160U_2	20040809	Objective data sheet	-	PBSS5160U_1
PBSS5160U_1	20040503	Objective data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Contents

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Date of release: 9 November 2023

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