

# PBSS5230QAZ Datasheet



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DiGi Electronics Part Number PBSS5230QAZ-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PBSS5230QAZ

Description TRANS PNP 30V 2A DFN1010D-3

Detailed Description Bipolar (BJT) Transistor PNP 30 V 2 A 170MHz 325 m

W Surface Mount DFN1010D-3



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
PBSS5230QAZ	Nexperia USA Inc.
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
PNP	2 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
30 V	210mV @ 50mA, 1A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA (ICBO)	60 @ 2A, 2V
Power - Max:	Frequency - Transition:
325 mW	170MHz
Operating Temperature:	Mounting Type:
150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
3-XDFN Exposed Pad	DFN1010D-3

## **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



## PBSS5230QA

## 30 V, 2 A PNP low VCEsat (BISS) transistor

23 August 2013

**Product data sheet** 

## 1. General description

PNP low  $V_{CEsat}$  Breakthrough In Small Signal (BISS) transistor in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

NPN complement: PBSS4230QA.

#### 2. Features and benefits

- Very low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High collector current gain h<sub>FE</sub> at high I<sub>C</sub>
- High energy efficiency due to less heat generation
- Reduced Printed-Circuit Board (PCB) area requirements
- Solderable side pads
- AEC-Q101 qualified

#### 3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-30	V
I <sub>C</sub>	collector current		-	-	-2	Α
I <sub>CM</sub>	peak collector current	t <sub>p</sub> ≤ 1 ms; pulsed	-	-	-3	Α
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C$ = -1 A; $I_B$ = -100 mA; pulsed; $t_p \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	-	120	180	mΩ



## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		C
2	E	emitter		В
3	С	collector	4 3	'*_
4	С	collector	2	sym132
			Transparent top view DFN1010D-3 (SOT1215)	

## 6. Ordering information

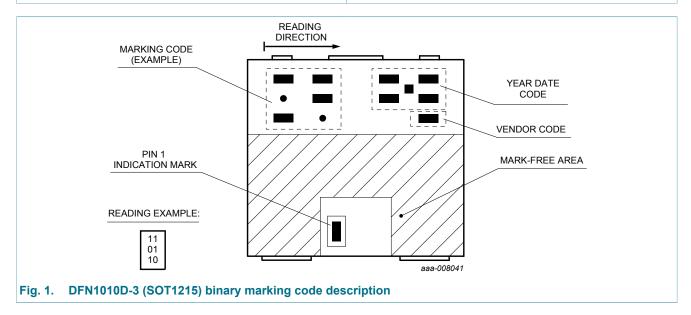
Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS5230QA	DFN1010D-3	plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals	SOT1215

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS5230QA	00 00 10



## 8. Limiting values

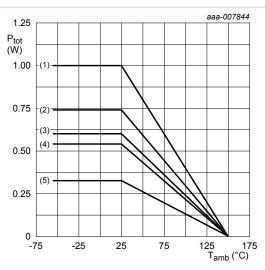
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-30	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-30	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-7	V
I <sub>C</sub>	collector current			-	-2	Α
I <sub>CM</sub>	peak collector current	t <sub>p</sub> ≤ 1 ms; pulsed		-	-3	Α
I <sub>B</sub>	base current			-	-0.3	Α
I <sub>BM</sub>	peak base current	t <sub>p</sub> ≤ 1 ms; pulsed		-	-1	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	325	mW
			[2]	-	600	mW
			[3]	-	740	mW
			[4]	-	540	mW
			[5]	-	1000	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm<sup>2</sup>.
- [4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, 4-layer copper, tin-plated mounting pad for collector 1 cm<sup>2</sup>.

#### 30 V, 2 A PNP low VCEsat (BISS) transistor



- (1) FR4 PCB, 4-layer copper, 1 cm<sup>2</sup>
- (2) FR4 PCB, single-sided copper, 6 cm<sup>2</sup>
- (3) FR4 PCB, single-sided copper, 1 cm<sup>2</sup>
- (4) FR4 PCB, 4-layer copper, standard footprint
- (5) FR4 PCB, single-sided copper, standard footprint

Fig. 2. Power derating curves

#### 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance	1	[1]	-	-	385	K/W
	from junction to ambient		[2]	-	-	209	K/W
ambient	ambient		[3]	-	-	169	K/W
			<u>[4]</u>	-	-	232	K/W
			<u>[5]</u>	-	-	125	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm<sup>2</sup>.
- [4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, 4-layer copper, tin-plated mounting pad for collector 1 cm<sup>2</sup>.

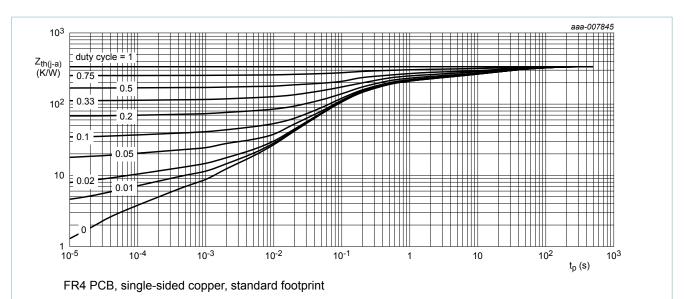


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

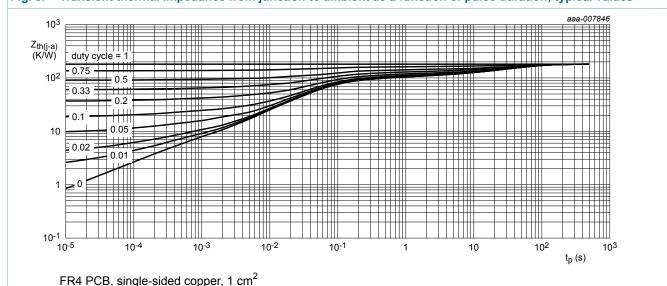
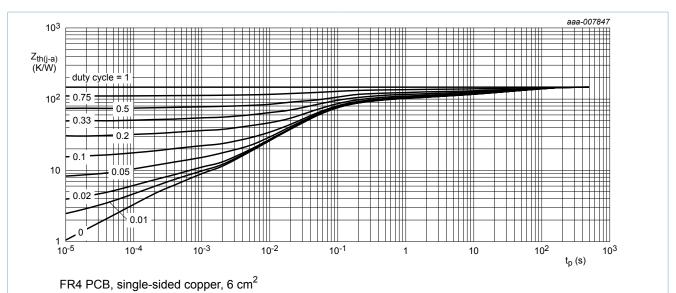


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

PBSS5230QA



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

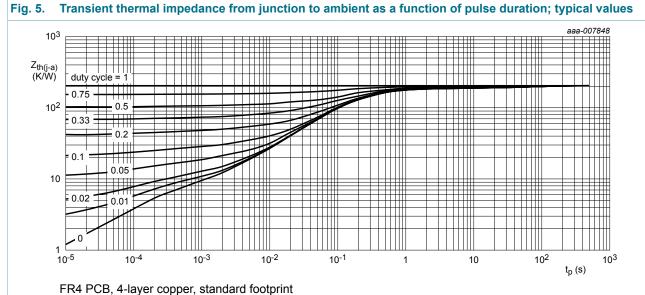
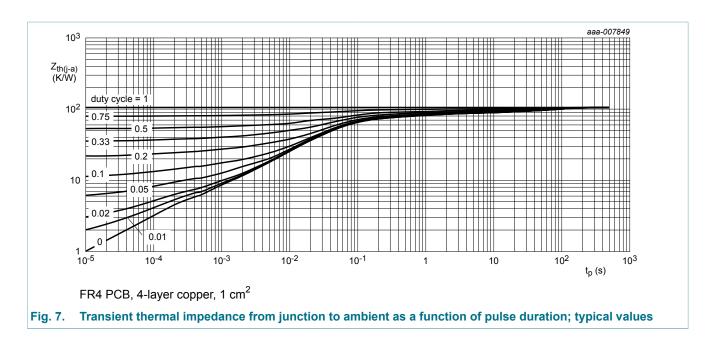


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

#### 30 V, 2 A PNP low VCEsat (BISS) transistor



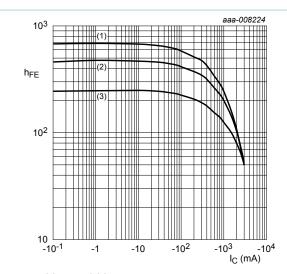
## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub> collector-base cut-off		V <sub>CB</sub> = -24 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
	current	V <sub>CB</sub> = -24 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-50	μA
I <sub>CES</sub>	collector-emitter cut-off current	$V_{CE}$ = -24 V; $V_{BE}$ = 0 V; $T_{amb}$ = 25 °C	-	-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -2 V; $I_{C}$ = -100 mA; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02 ; $T_{amb}$ = 25 °C; pulsed	250	425	-	
		$V_{CE}$ = -2 V; $I_{C}$ = -500 mA; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02 ; $T_{amb}$ = 25 °C; pulsed	180	295	-	
		$V_{CE}$ = -2 V; $I_{C}$ = -1 A; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02 ; $T_{amb}$ = 25 °C; pulsed	130	200	-	
		$V_{CE}$ = -2 V; $I_{C}$ = -2 A; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02 ; $T_{amb}$ = 25 °C; pulsed	60	95	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C$ = -500 mA; $I_B$ = -50 mA; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_{amb}$ = 25 °C	-	-70	-100	mV
		$I_C$ = -1 A; $I_B$ = -50 mA; $t_p$ ≤ 300 μs; $\delta$ ≤ 0.02 ; $T_{amb}$ = 25 °C	-	-140	-210	mV
		$I_{C}$ = -1 A; $I_{B}$ = -100 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 ; T_{amb} = 25 \ ^{\circ}C$	-	-120	-180	mV

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$I_{C}$ = -2 A; $I_{B}$ = -100 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C	-	-275	-410	mV
		$I_{C}$ = -2 A; $I_{B}$ = -200 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C	-	-220	-330	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C$ = -1 A; $I_B$ = -100 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	-	120	180	mΩ
$V_{BEsat}$	base-emitter saturation voltage	$I_C$ = -500 mA; $I_B$ = -50 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	-	-0.86	-1	V
		$I_{C}$ = -1 A; $I_{B}$ = -50 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C	-	-0.89	-1.05	V
	$I_{C}$ = -2 A; $I_{B}$ = -100 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C	-	-0.98	-1.15	V	
	$I_{C}$ = -2 A; $I_{B}$ = -200 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C	-	-1.02	-1.2	V	
$V_{BEon}$	base-emitter turn-on voltage	$V_{CE}$ = -2 V; $I_{C}$ = -0.5 A; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 ; T_{amb}$ = 25 °C	-	-0.77	-0.9	V
t <sub>d</sub>	delay time	$V_{CC}$ = -10 V; $I_{C}$ = -0.5 A; $I_{Bon}$ = -25 mA;	-	10	-	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = 25 mA; T <sub>amb</sub> = 25 °C	-	30	-	ns
t <sub>on</sub>	turn-on time		-	40	-	ns
t <sub>s</sub>	storage time		-	270	-	ns
t <sub>f</sub>	fall time		-	45	-	ns
t <sub>off</sub>	turn-off time		-	315	-	ns
f <sub>T</sub>	transition frequency	$V_{CE}$ = -10 V; $I_{C}$ = -50 mA; f = 100 MHz; $T_{amb}$ = 25 °C	120	170	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	14	16	pF



$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

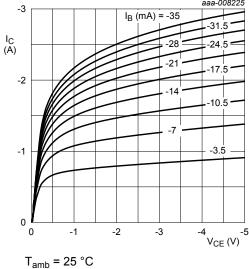
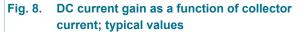
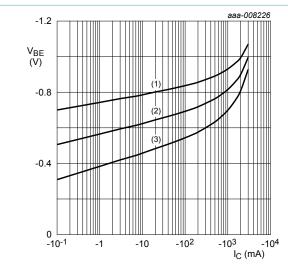


Fig. 9. Collector current as a function of collectoremitter voltage; typical values





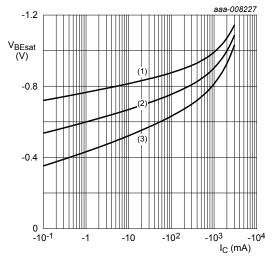
$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 10. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 20$$

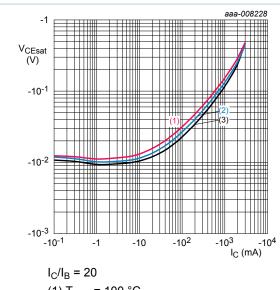
(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 11. Base-emitter saturation voltage as a function of collector current; typical values

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(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Fig. 12. Collector-emitter saturation voltage as a function of collector current; typical values

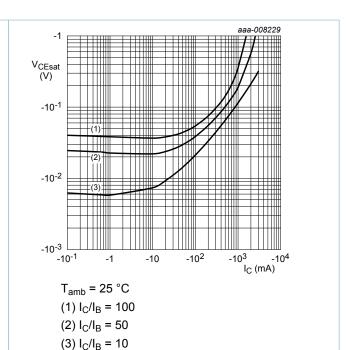
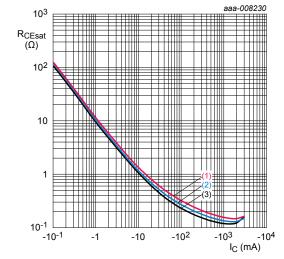


Fig. 13. Collector-emitter saturation voltage as a function of collector current; typical values



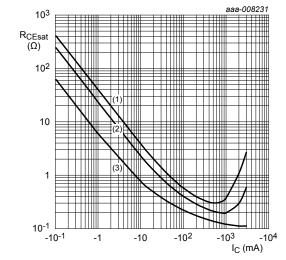
$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

$$(3) T_{amb} = -55 °C$$

Fig. 14. Collector-emitter saturation resistance as a function of collector current; typical values



$$T_{amb} = 25 \, ^{\circ}C$$

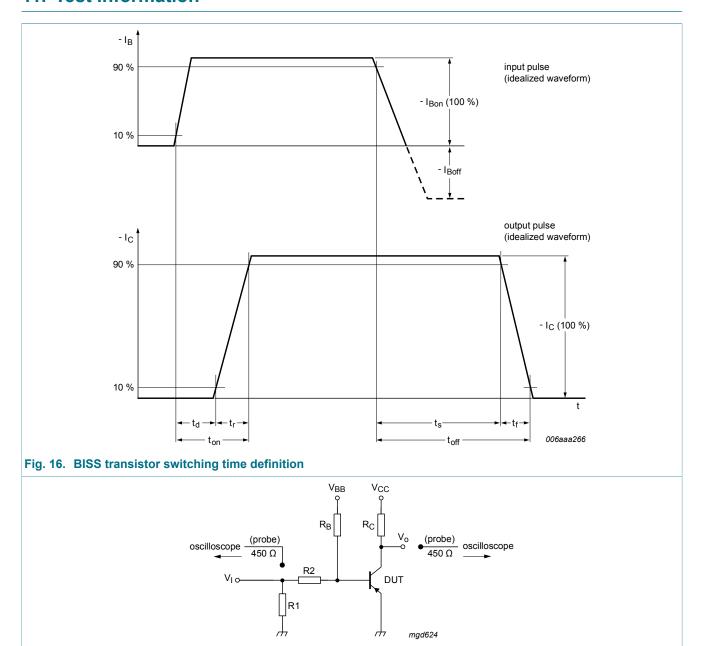
(1) 
$$I_C/I_B = 100$$

(2) 
$$I_C/I_B = 50$$

(3) 
$$I_C/I_B = 10$$

Fig. 15. Collector-emitter saturation resistance as a function of collector current; typical values

## 11. Test information



#### 11.1 Quality information

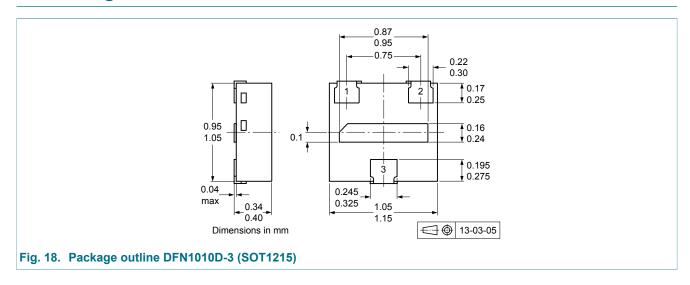
Fig. 17. Test circuit for switching times

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

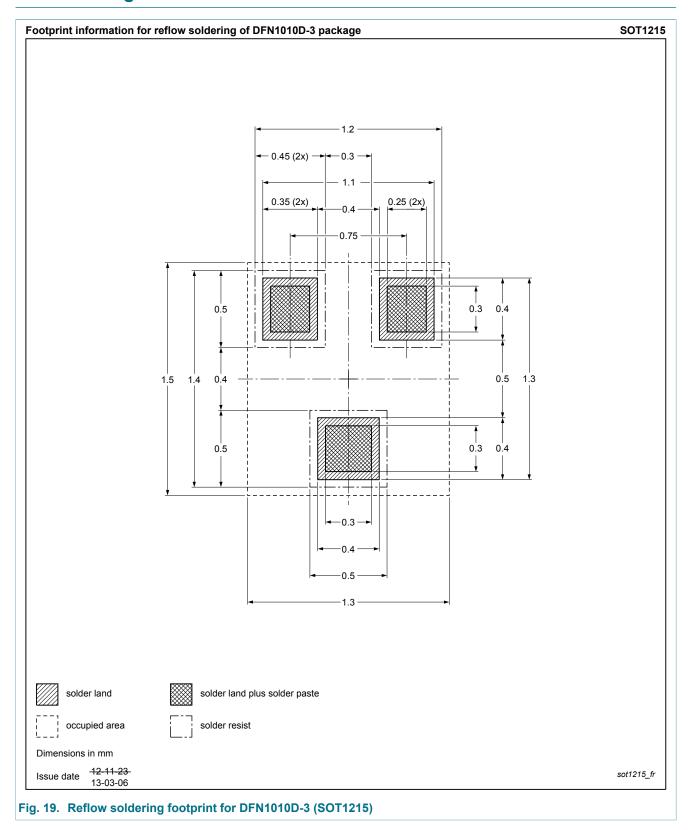
PBSS5230QA

30 V, 2 A PNP low VCEsat (BISS) transistor

## 12. Package outline



## 13. Soldering



PBSS5230QA

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30 V, 2 A PNP low VCEsat (BISS) transistor

## 14. Revision history

#### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS5230QA v.1	20130823	Product data sheet	-	-

#### 30 V, 2 A PNP low VCEsat (BISS) transistor

#### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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