

PBSS5320D,125 Datasheet

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DiGi Electronics Part Number Manufacturer Manufacturer Product Number Description Detailed Description

PBSS5320D,125-DG

Nexperia USA Inc.

PBSS5320D,125

TRANS PNP 20V 3A 6TSOP

Bipolar (BJT) Transistor PNP 20 V 3 A 100MHz 750 m W Surface Mount 6-TSOP

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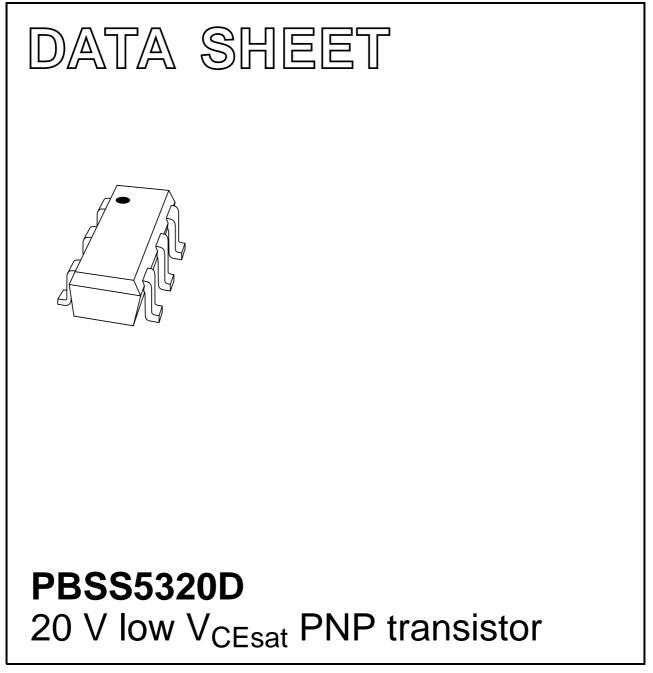
Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PBSS5320D,125	Nexperia USA Inc.
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	3 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
20 V	400mV @ 300mA, 3A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
100nA (ICBO)	150 @ 2A, 2V
Power - Max:	Frequency - Transition:
750 mW	100MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q100	Surface Mount
Package / Case:	Supplier Device Package:
SC-74, SOT-457	6-TSOP
Base Product Number:	
PBSS5320	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0075	

DISCRETE SEMICONDUCTORS



Product data sheet

2002 Jun 12



Product data sheet

PBSS5320D

20 V low V_{CEsat} PNP transistor

FEATURES

- Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation

APPLICATIONS

- Supply line switching circuits
- Battery management applications
- DC/DC converter applications
- Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers).

DESCRIPTION

 PNP low $\mathsf{V}_{\mathsf{CEsat}}$ transistor in a SOT457 (SC-74) plastic package.

MARKING

TYPE NUMBER	MARKING CODE
PBSS5320D	52

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage	-20	V
I _C	collector current (DC)	-3	А
I _{CM}	peak collector current	-5	А
R _{CEsat}	equivalent on-resistance	133	mΩ

PINNING

PIN	DESCRIPTION
1	collector
2	collector
3	base
4	emitter
5	collector
6	collector

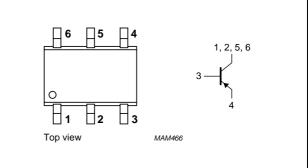


Fig.1 Simplified outline (SOT457; SC-74) and symbol.

20 V low V_{CEsat} PNP transistor

PBSS5320D

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	-20	V
V _{CEO}	collector-emitter voltage	open base	_	-20	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)		—	-3	A
I _{CM}	peak collector current		—	-5	A
I _B	base current		—	-500	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$; note 1	—	600	mW
		$T_{amb} \le 25 \ ^{\circ}C$; note 2	—	750	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Notes

- 1. Device mounted on a printed-circuit board, single side copper, tinplated, mounting pad for collector 1 cm².
- 2. Device mounted on a printed-circuit board, single side copper, tinplated, mounting pad for collector 6 cm².

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to	note 1	208	K/W
	ambient	note 2	160	K/W

Notes

- 1. Device mounted on a printed-circuit board, single side copper, tinplated, mounting pad for collector 1 cm².
- 2. Device mounted on a printed-circuit board, single side copper, tinplated, mounting pad for collector 6 cm².

20 V low V_{CEsat} PNP transistor

PBSS5320D

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = -20 \text{ V}; \text{ I}_{E} = 0$	-	-	-100	nA
		$V_{CB} = -20 \text{ V}; \text{ I}_{E} = 0; \text{ T}_{j} = 150 ^{\circ}\text{C}$	-	-	-50	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0$	-	-	-100	nA
h _{FE}	DC current gain	$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -100 \text{ mA}$	200	-	-	
		$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -500 \text{ mA}$	200	-	-	
		$V_{CE} = -2 \text{ V}; I_{C} = -1000 \text{ mA}; \text{ note } 1$	200	-	_	
		$V_{CE} = -2 \text{ V}; I_{C} = -2000 \text{ mA}; \text{ note } 1$	150	-	-	
V _{CEsat}	collector-emitter saturation	$I_{C} = -500 \text{ mA}; I_{B} = -5 \text{ mA}$	-	-	-130	mV
	voltage	$I_{\rm C} = -500 \text{ mA}; I_{\rm B} = -50 \text{ mA}$	-	-	-80	mV
		$I_{\rm C} = -1$ A; $I_{\rm B} = -50$ mA	-	-	-160	mV
		$I_{C} = -2$ A; $I_{B} = -20$ mA; note 1	_	-	-400	mV
		$I_{C} = -2 \text{ A}; I_{B} = -200 \text{ mA}; \text{ note } 1$	-	-	-250	mV
		$I_{C} = -3 \text{ A}; I_{B} = -300 \text{ mA}; \text{ note } 1$	-	-	-400	mV
R _{CEsat}	equivalent on-resistance	$I_{C} = -3 \text{ A}; I_{B} = -300 \text{ mA}; \text{ note } 1$	-	85	133	mΩ
V _{BEsat}	base-emitter saturation voltage	$I_{C} = -2$ A; $I_{B} = -200$ mA; note 1	-	-	-1.2	V
V _{BEon}	base-emitter turn-on voltage	$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -1 \text{ A}; \text{ note } 1$	-1.2	-	-	V
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; \text{ I}_{E} = \text{ I}_{e} = 0; \text{ f} = 1 \text{ MHz}$	-	-	50	pF
FT	transition frequency	$I_{C} = -200 \text{ mA}; V_{CE} = -10 \text{ V};$ f = 100 MHz	100	-	-	MHz

Note

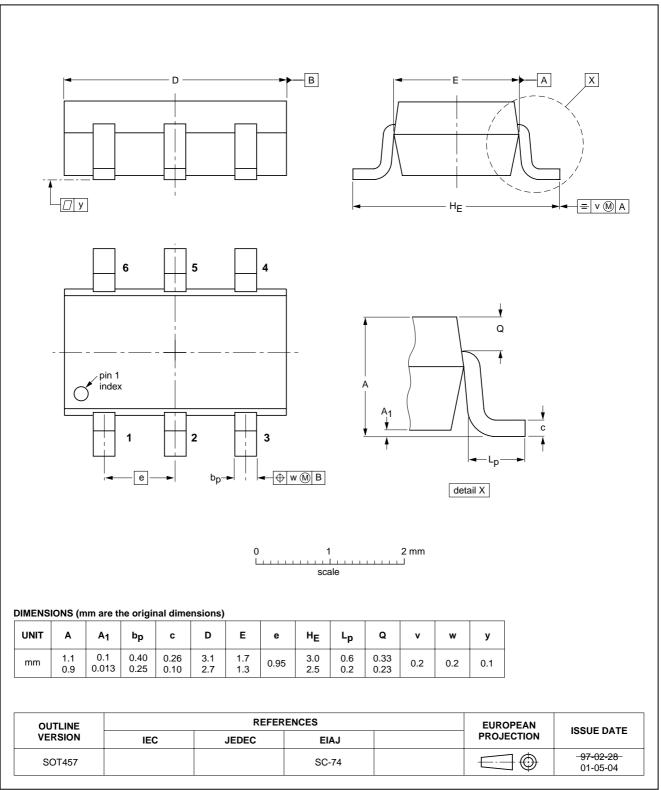
1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

SOT457

20 V low V_{CEsat} PNP transistor

PACKAGE OUTLINE

Plastic surface mounted package; 6 leads



PBSS5320D

20 V low V_{CEsat} PNP transistor

PBSS5320D

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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