

# PBSS9110T,215 Datasheet



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DiGi Electronics Part Number PBSS9110T,215-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PBSS9110T,215

Description TRANS PNP 100V 1A TO236AB

Detailed Description Bipolar (BJT) Transistor PNP 100 V 1 A 100MHz 480

mW Surface Mount TO-236AB



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### **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:				
PBSS9110T,215	Nexperia USA Inc.				
Series:	Product Status:				
	Active				
Transistor Type:	Current - Collector (Ic) (Max):				
PNP	1 A				
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:				
100 V	320mV @ 100mA, 1A				
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:				
100nA	150 @ 500mA, 5V				
Power - Max:	Frequency - Transition:				
480 mW	100MHz				
Operating Temperature:	Mounting Type:				
150°C (TJ)	Surface Mount				
Package / Case:	Supplier Device Package:				
TO-236-3, SC-59, SOT-23-3	TO-236AB				
Base Product Number:					
PBSS9110					

### **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



## **PBSS9110T**

### 100 V, 1 A PNP low VCEsat transistor

1 January 2023

**Product data sheet** 

### 1. General description

PNP low  $V_{\text{CEsat}}$  transistor in a SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS8110T

#### 2. Features and benefits

- Low collector-emitter saturation voltage  $V_{\mbox{\scriptsize CEsat}}$  and corresponding low RCEsat
- High collector current capability
- High collector current gain
- · Improved efficiency due to reduced heat generation

### 3. Applications

- Major application segments
  - · Automotive 42 V power
  - · Telecom infrastructure
  - Industrial
- DC/DC converters
- Peripheral drivers
  - Driver in low supply voltage applications (e.g. lamps and LEDs)
  - Inductive load driver (e.g. relays, buzzers and motors)

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-100	V
I <sub>C</sub>	collector current		-	-	-1	Α
I <sub>CM</sub>	peak collector current	limited by T <sub>j(max)</sub>	-	-	-3	Α
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C$ = -1 A; $I_B$ = -100 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	170	320	mΩ



100 V, 1 A PNP low VCEsat transistor

### 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	]3	
2	Е	emitter		C
3	С	collector		В
			SOT23	E sym132

### 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PBSS9110T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PBSS9110T	%U7

[1] % = placeholder for manufacturing site code

100 V, 1 A PNP low VCEsat transistor

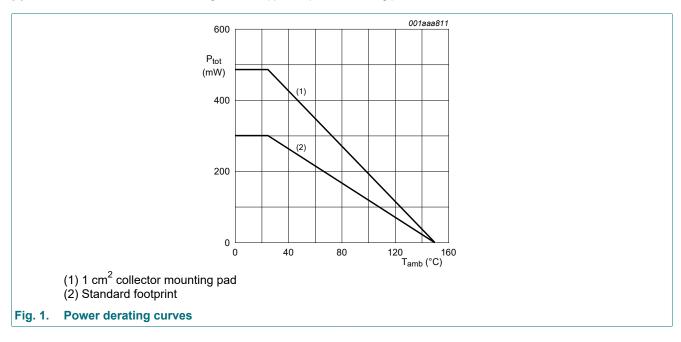
### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-120	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-100	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
Ic	collector current			-	-1	А
I <sub>CM</sub>	peak collector current	limited by T <sub>j(max)</sub>		-	-3	А
I <sub>B</sub>	base current			-	-300	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



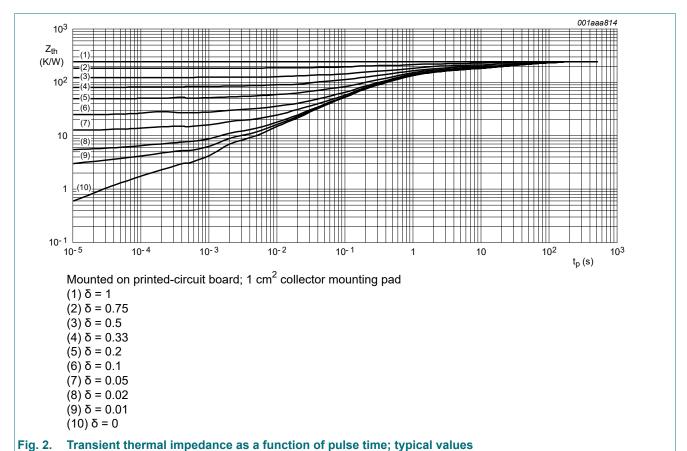
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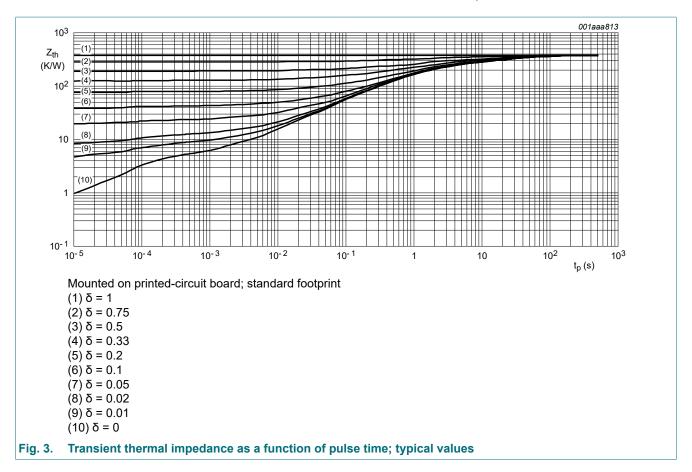
#### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
111(J-a)	thermal resistance from	in free air	[1]	-	-	417	K/W
	junction to ambient		[2]	-	-	260	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.





100 V, 1 A PNP low VCEsat transistor

### 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = -100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$	-120	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_C = -10 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-100	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage (collector open)	I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-5	-	-	V
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = -80 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
	current	V <sub>CB</sub> = -80 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = -80 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	150	-	-	
		V <sub>CE</sub> = -5 V; I <sub>C</sub> = -250 mA; T <sub>amb</sub> = 25 °C	150	-	-	
		$V_{CE}$ = -5 V; $I_{C}$ = -500 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	150	-	450	
		$V_{CE}$ = -5 V; $I_{C}$ = -1 A; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	125	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -250 mA; I <sub>B</sub> = -25 mA; T <sub>amb</sub> = 25 °C	-	-	-120	mV
		I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA; T <sub>amb</sub> = 25 °C	-	-	-180	mV
		$I_C$ = -1 A; $I_B$ = -100 mA; pulsed; $t_p \le$	-	-	-320	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	170	320	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = -1 A; I <sub>B</sub> = -100 mA; T <sub>amb</sub> = 25 °C	-	-	-1.1	V
$V_{BEon}$	base-emitter turn-on voltage	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -1 A; T <sub>amb</sub> = 25 °C	-	-	-1	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -10 V; I <sub>C</sub> = -50 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	17	pF

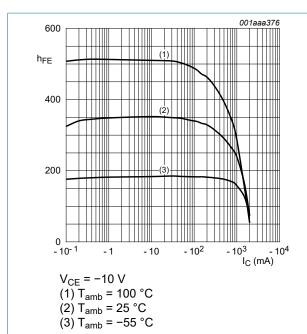


Fig. 4. DC current gain as a function of collector

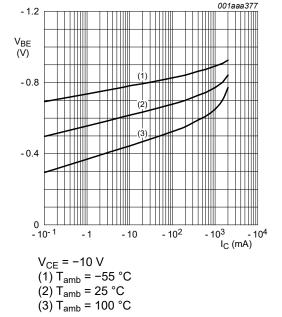
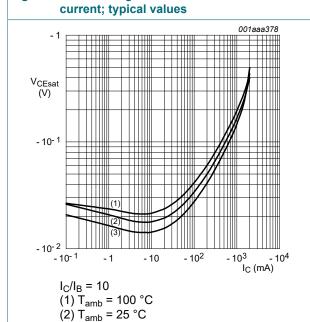


Fig. 5. Base-emitter voltage as a function of collector



(3) T<sub>amb</sub> = −55 °C

Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values

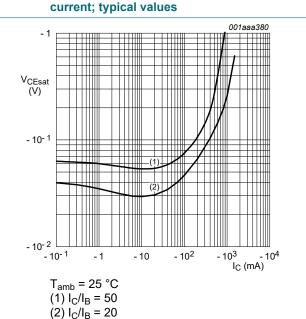


Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

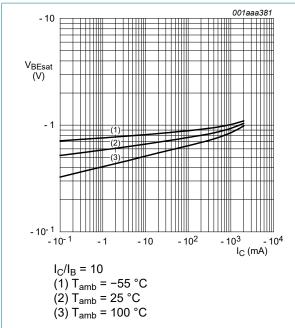


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

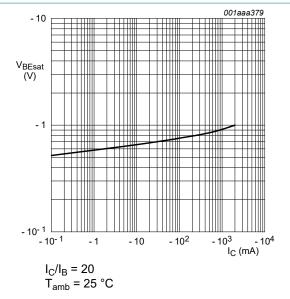


Fig. 9. Base-emitter saturation voltage as a function of collector current; typical values

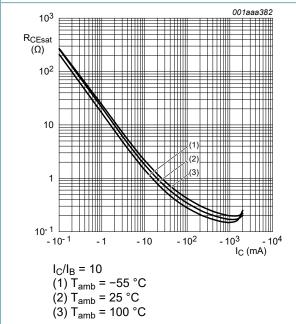


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

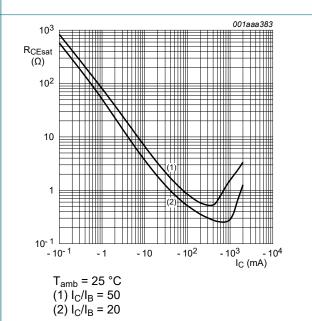
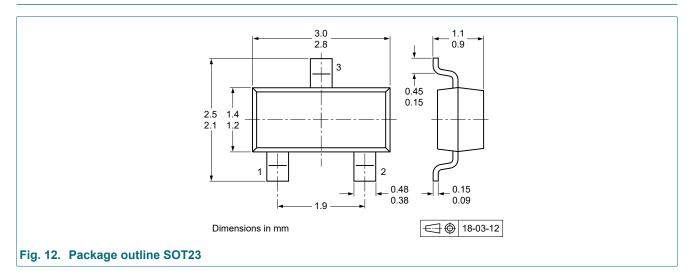


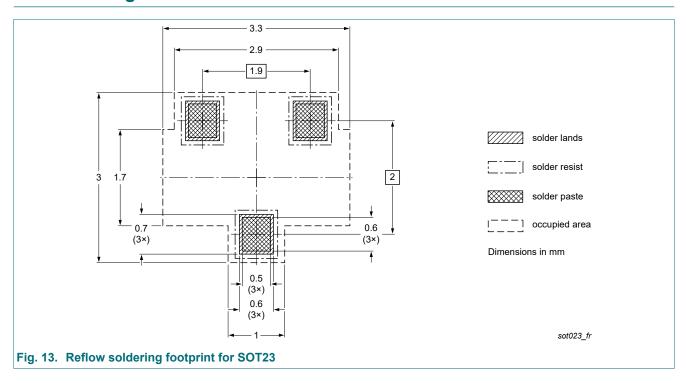
Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

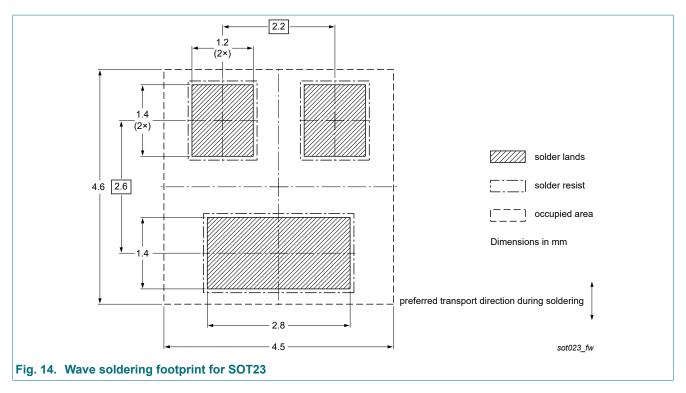
100 V, 1 A PNP low VCEsat transistor

### 11. Package outline



### 12. Soldering





100 V, 1 A PNP low VCEsat transistor

### 13. Revision history

#### **Table 8. Revision history**

Table 6. Revision mistory				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS9110T v.4	20230101	Product data sheet	-	PBSS9110T v.3
Modifications:		nged to non-automotive (-Q) product alternative	•	n. Please refer to nexperia.com for
PBSS9110T v.3	20220523	Product data sheet	-	PBSS9110T v.2
PBSS9110T v.2	20040513	Product data sheet	-	PBSS9110T v.1
PBSS9110T v.1	20040506	Product data sheet	-	-

#### 100 V, 1 A PNP low VCEsat transistor

#### 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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