

PDTC123TT,215 Datasheet



DiGi Electronics Part Number PD Manufacturer Ne Manufacturer Product Number PD Description TR Detailed Description Pr

PDTC123TT,215-DG Nexperia USA Inc. PDTC123TT,215 TRANS PREBIAS NPN 50V TO236AB Pre-Biased Bipolar Transistor (BJT) NPN - Pre-Biase

d 50 V 100 mA 250 mW Surface Mount TO-236AB

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:				
PDTC123TT,215	Nexperia USA Inc.				
Series:	Product Status:				
-	Active				
Transistor Type:	Current - Collector (Ic) (Max):				
NPN - Pre-Biased	100 mA				
Voltage - Collector Emitter Breakdown (Max):	Resistor - Base (R1):				
50 V	2.2 kOhms				
DC Current Gain (hFE) (Min) @ lc, Vce:	Vce Saturation (Max) @ lb, lc:				
30 @ 20mA, 5V	150mV @ 500μA, 10mA				
Current - Collector Cutoff (Max):	Power - Max:				
1μΑ	250 mW				
Mounting Type:	Package / Case:				
Surface Mount	TO-236-3, SC-59, SOT-23-3				
Supplier Device Package:	Base Product Number:				
ТО-236АВ	PDTC123				

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0095	

PDTC123T series

NPN resistor-equipped transistors; $R1 = 2.2 \text{ k}\Omega$, R2 = open

Rev. 01 — 10 March 2006

Product data sheet

1. Product profile

1.1 General description

NPN Resistor-Equipped Transistors (RET) family in Surface Mounted Device (SMD) plastic packages.

Table 1.Product overview

Type number	Package	Package			
	Philips	JEITA	JEDEC		
PDTC123TE	SOT416	SC-75	-	PDTA123TE	
PDTC123TK	SOT346	SC-59A	TO-236	PDTA123TK	
PDTC123TM	SOT883	SC-101	-	PDTA123TM	
PDTC123TS ^[1]	SOT54	SC-43A	TO-92	PDTA123TS	
PDTC123TT	SOT23	-	TO-236AB	PDTA123TT	
PDTC123TU	SOT323	SC-70	-	PDTA123TU	

[1] Also available in SOT54A and SOT54 variant packages (see Section 2).

1.2 Features

- Built-in bias resistors
- Simplifies circuit design
- 100 mA output current capability

1.3 Applications

- Digital applications
- Control of IC inputs

1.4 Quick reference data

Table 2.Quick reference data

- Reduces component countReduces pick and place costs
- Cost-saving alternative for BC847 series in digital applications
- Switching loads

Symbol Conditions Unit Parameter Min Тур Max 50 V VCEO collector-emitter voltage open base _ output current 100 mΑ I_{O} --R1 bias resistor 1 (input) 2.86 1.54 2.2 kΩ

nexperia

PDTC123T series

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2. Pinning information

Pin	Description	Simplified outline	Symbol
SOT54			
1	input (base)		
2	output (collector)		
3	GND (emitter)	001aab347	1 R1 006aaa218
SOT54A			
1	input (base)		
2	output (collector)		
3	GND (emitter)	001aab348	1 - R1 -
SOT54 va	ariant		
1	input (base)		
2	output (collector)		
3	GND (emitter)	() () () () () () () () () () () () () (1 - R1 -
SOT23; S	SOT323; SOT346; SOT416		
1	input (base)		
2	GND (emitter)	3	
3	output (collector)	1 2 006aaa144	1 R1 sym012
SOT883			
1	input (base)		
2	GND (emitter)		
3	output (collector)	2 Transparent top view	

PDTC123T_SER_1
Product data sheet

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3. Ordering information

Table 4. Ordering information							
Type number	Package	Package					
	Name	Description	Version				
PDTC123TE	SC-75	plastic surface mounted package; 3 leads	SOT416				
PDTC123TK	SC-59A	plastic surface mounted package; 3 leads	SOT346				
PDTC123TM	SC-101	leadless ultra small plastic package; 3 solder lands; body 1.0 \times 0.6 \times 0.5 mm	SOT883				
PDTC123TS ^[1]	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54				
PDTC123TT	-	plastic surface mounted package; 3 leads	SOT23				
PDTC123TU	SC-70	plastic surface mounted package; 3 leads	SOT323				

[1] Also available in SOT54A and SOT54 variant packages (see Section 2 and Section 9).

4. Marking

Table 5. Marking codes	
Type number	Marking code ^[1]
PDTC123TE	2B
PDTC123TK	GB
PDTC123TM	FB
PDTC123TS	TC123T
PDTC123TT	ZM*
PDTC123TU	*1T

[1] * = -: made in Hong Kong

* = p: made in Hong Kong

* = t: made in Malaysia

* = W: made in China

PDTC123T series

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5. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	50	V
V _{CEO}	collector-emitter voltage	open base	-	50	V
V _{EBO}	emitter-base voltage	open collector	-	5	V
lo	output current		-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	SOT416		<u>[1]</u> _	150	mW
	SOT346		<u>[1]</u> _	250	mW
	SOT883		[2][3]	250	mW
	SOT54		<u>[1]</u> _	500	mW
	SOT23		<u>[1]</u> _	250	mW
	SOT323		<u>[1]</u> _	200	mW
T _{stg}	storage temperature		-65	+150	°C
Т _ј	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB with 60 µm copper strip line, standard footprint.

6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air				
	SOT416		<u>[1]</u> _	-	833	K/W
	SOT346		<u>[1]</u> _	-	500	K/W
	SOT883		[2][3]	-	500	K/W
	SOT54		<u>[1]</u> _	-	250	K/W
	SOT23		<u>[1]</u> _	-	500	K/W
	SOT323		<u>[1]</u> _	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

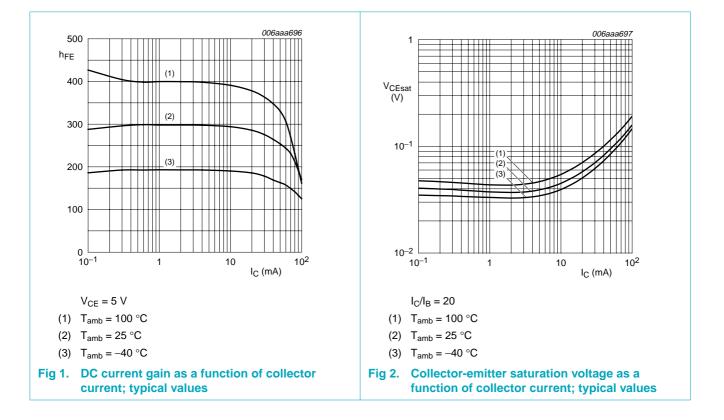
[3] Device mounted on an FR4 PCB with 60 μ m copper strip line, standard footprint.

PDTC123T series

NPN resistor-equipped transistors; R1 = 2.2 k Ω , R2 = open

7. Characteristics

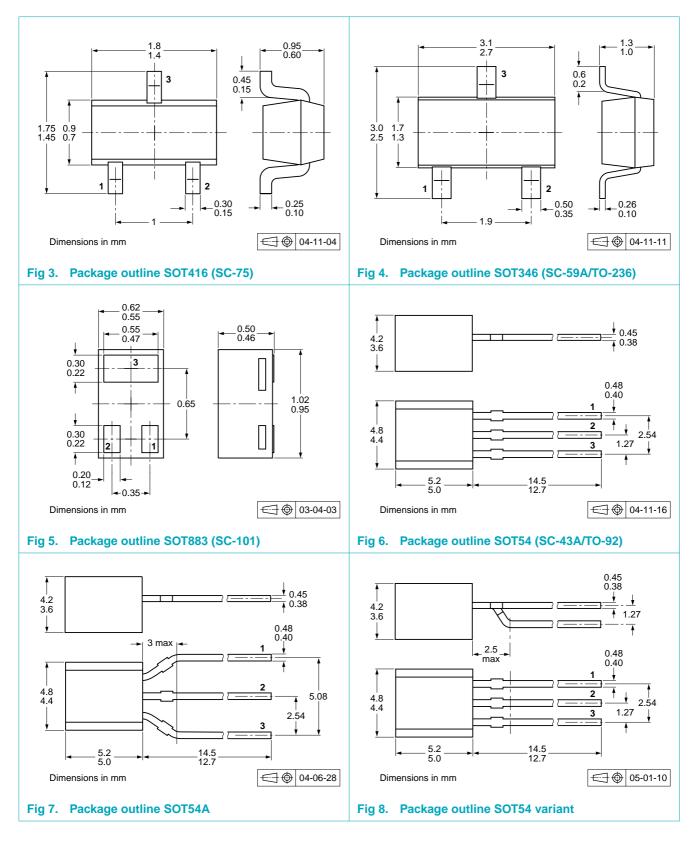
Table 8. $T_{amb} = 25$	Characteristics °C unless otherwise spec	ified.				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = 50 \text{ V}; \text{ I}_{E} = 0 \text{ A}$	-	-	100	nA
I _{CEO}	collector-emitter cut-off	$V_{CE} = 30 \text{ V}; \text{ I}_{B} = 0 \text{ A}$	-	-	1	μA
	current	$\label{eq:Vce} \begin{array}{l} V_{CE} = 30 \; V; \; I_{B} = 0 \; A; \\ T_{j} = 150 \; ^{\circ}C \end{array}$	-	-	50	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$	-	-	100	nA
h _{FE}	DC current gain	V_{CE} = 5 V; I_{C} = 20 mA	30	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_{C} = 10 mA; I_{B} = 0.5 mA	-	-	150	mV
R1	bias resistor 1 (input)		1.54	2.2	2.86	kΩ
C _c	collector capacitance	V_{CB} = 10 V; I_E = i_e = 0 A; f = 1 MHz	-	-	2.5	pF



PDTC123T series

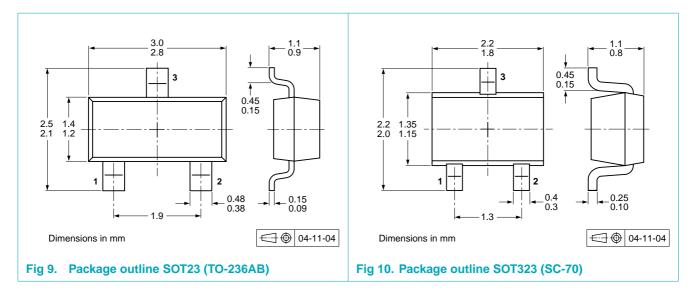
NPN resistor-equipped transistors; R1 = 2.2 k Ω , R2 = open

8. Package outline



PDTC123T series

NPN resistor-equipped transistors; R1 = 2.2 k Ω , R2 = open



9. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packin	Packing quantity		
			3000	5000	10000	
PDTC123TE	SOT416	4 mm pitch, 8 mm tape and reel	-115	-	-135	
PDTC123TK	SOT346	4 mm pitch, 8 mm tape and reel	-115	-	-135	
PDTC123TM	SOT883	2 mm pitch, 8 mm tape and reel	-	-	-315	
PDTC123TS	SOT54	bulk, straight leads	-	-412	-	
	SOT54A	tape and reel, wide pitch	-	-	-116	
		tape ammopack, wide pitch	-	-	-126	
	SOT54 variant	bulk, delta pinning	-	-112	-	
PDTC123TT	SOT23	4 mm pitch, 8 mm tape and reel	-215	-	-235	
PDTC123TU	SOT323	4 mm pitch, 8 mm tape and reel	-115	-	-135	

[1] For further information and the availability of packing methods, see Section 12.

PDTC123T series

NPN resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

10. Revision history

Table 10. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PDTC123T_SER_1	20060310	Product data sheet	-	-

PDTC123T series

NPN resistor-equipped transistors; R1 = 2.2 k Ω , R2 = open

11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.semiconductors.philips.com.

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