

PDTC143ET,235 Datasheet



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DiGi Electronics Part Number PDTC143ET,235-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PDTC143ET,235

Description TRANS PREBIAS NPN 50V TO236AB

Detailed Description Pre-Biased Bipolar Transistor (BJT) NPN - Pre-Biase

d 50 V 100 mA 250 mW Surface Mount TO-236AB



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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PDTC143ET,235	Nexperia USA Inc.
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN - Pre-Biased	100 mA
Voltage - Collector Emitter Breakdown (Max):	Resistor - Base (R1):
50 V	4.7 kOhms
Resistor - Emitter Base (R2):	DC Current Gain (hFE) (Min) @ Ic, Vce:
4.7 kOhms	30 @ 10mA, 5V
Vce Saturation (Max) @ lb, lc:	Current - Collector Cutoff (Max):
150mV @ 500μA, 10mA	1µА
Power - Max:	Mounting Type:
250 mW	Surface Mount
Package / Case:	Supplier Device Package:
TO-236-3, SC-59, SOT-23-3	TO-236AB
Base Product Number:	
PDTC143	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0095	



PDTC143ET

50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 4.7 k Ω

1 April 2023

Product data sheet

1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA143ET

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs

3. Applications

- · Digital application in industrial segments
- Cost-saving alternative for BC847 series in digital applications
- · Controlling IC inputs
- Switching loads

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	

[1] See "Section 11: Test information" for resistor calculation and test conditions.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	GND	ground (emitter)		R1
3	0	output (collector)		R2
			1 2	GND
			SOT23	sym007

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PDTC143ET	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTC143ET	802

[1] % = placeholder for manufacturing site code

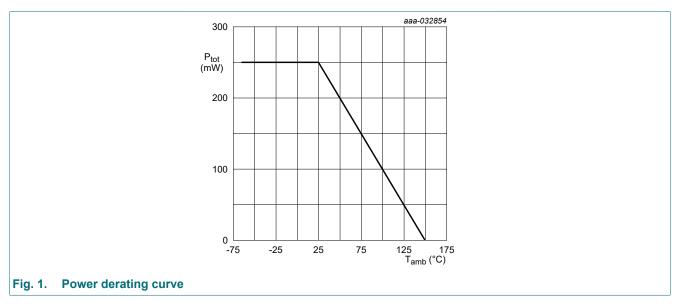
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	10	V
VI	input voltage			-10	30	V
Io	output current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.



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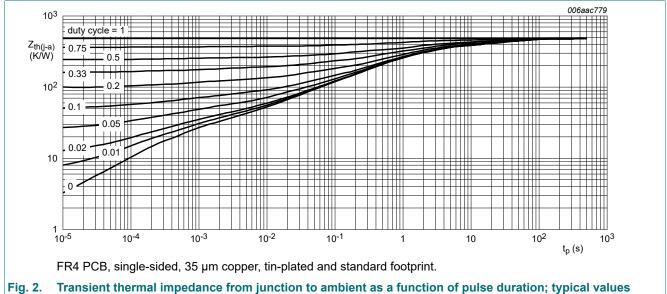
50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 4.7 k Ω

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

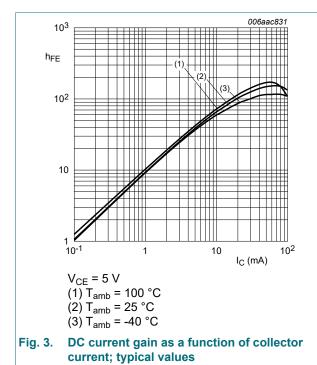


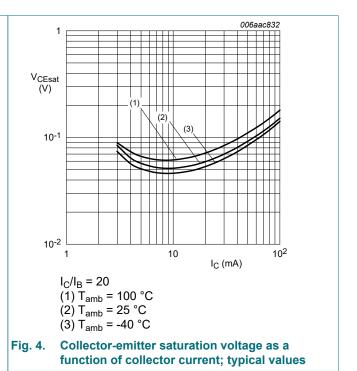
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$		50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CEO}	collector-emitter cut-off	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	1	μΑ
	current	V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	900	μΑ
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C		30	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$		-	-	150	mV
V _{I(off)}	off-state input voltage	V _{CE} = 5 V; I _C = 100 μA; T _{amb} = 25 °C		-	1.1	0.5	V
V _{I(on)}	on-state input voltage	$V_{CE} = 0.3 \text{ V}; I_{C} = 20 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		2.5	1.9	-	V
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	
C _c	collector capacitance	V_{CB} = 10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	-	2.5	pF
f _T	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	[2]	-	230	-	MHz

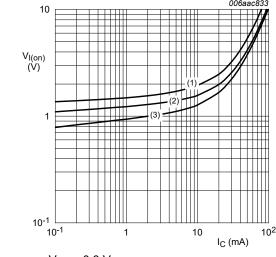
- [1] See "Section 11: Test information" for resistor calculation and test conditions.
- [2] Characteristics of built-in transistor.





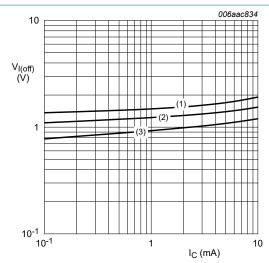
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50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 4.7 k Ω



 $V_{CE} = 0.3 V$

(1) T_{amb} = -40 °C (2) T_{amb} = 25 °C (3) T_{amb} = 100 °C



V_{CE} = 5 V (1) T_{amb} = -40 °C (2) T_{amb} = 25 °C

(3) $T_{amb} = 100 \, ^{\circ}C$

Fig. 5. On-state input voltage as a function of collector | Fig. 6. current; typical values



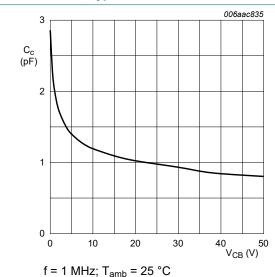
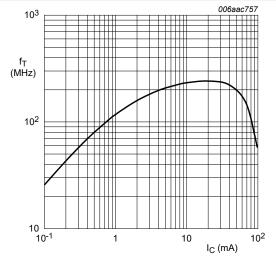


Fig. 7. Collector capacitance as a function of collector- Fig. 8. base voltage; typical values



 V_{CE} = 5 V; T_{amb} = 25 °C

Transition frequency as a function of collector current; typical values of built-in transistor

11. Test information

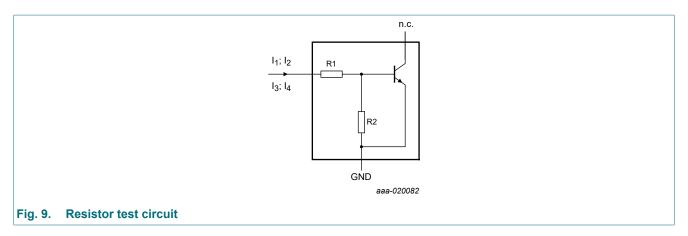
Resistor calculation

• Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$



Resistor test conditions

Table 8. Resistor test conditions

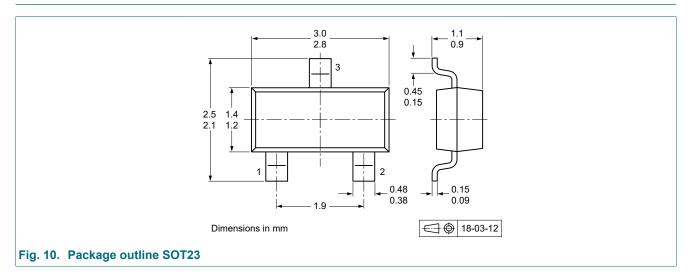
Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I ₁	l ₂	l ₃	14
PDTC143ET	4.7	4.7	600 μΑ	700 μΑ	-600 μΑ	-700 μA

7/12

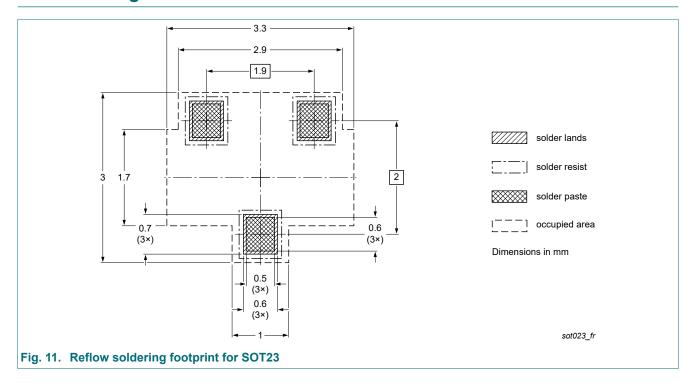
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50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 4.7 k Ω

12. Package outline

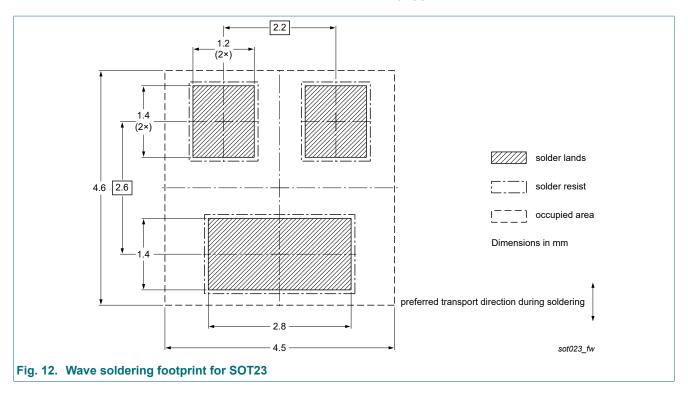


13. Soldering



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50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 4.7 k Ω



14. Revision history

Table 9. Revision history

Date about ID	Dalagae data	Data about status	Change	Cumanadaa	
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PDTC143ET v.11	20230401	Product data sheet	-	PDTC143E_SERIES v.10	
Modification:	 Family data sheet reduced to single type data sheet. The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Product changed to non automotive. Please refer to the automotive product(s) with -Q. Packing information removed. 				
PDTC143E_SERIES v.10	20111208	Product data sheet	-	PDTC143E_SERIES v.9	
PDTC143E_SERIES v.9	20040805	Product data sheet	-	PDTC143E_SERIES v.8	
PDTC143E_SERIES v.8	20040318	Product specification	-	PDTC143E_SERIES v.7	
PDTC143E_SERIES v.7	20040112	Product specification	-	PDTC143E_SERIES v.6	
PDTC143E_SERIES v.6	20030910	Product specification	-	PDTC143E_SERIES v.5	
PDTC143E_SERIES v.5	20030410	Product specification	-	-	

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50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 4.7 k Ω

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	3
9. Thermal characteristics	4
10. Characteristics	5
11. Test information	7
12. Package outline	8
13. Soldering	
14. Revision history	
15. Legal information	

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12 / 12

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