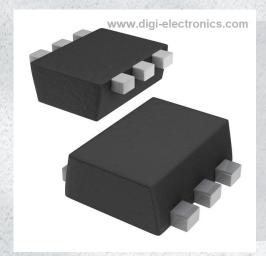


# PEMX1,115 Datasheet



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DiGi Electronics Part Number PEMX1,115-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PEMX1,115

Description TRANS 2NPN 40V 0.1A SOT666

**Detailed Description** Bipolar (BJT) Transistor Array 2 NPN (Dual) 40V 100

mA 100MHz 300mW Surface Mount SOT-666



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# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:			
PEMX1,115	Nexperia USA Inc.			
Series:	Product Status:			
	Not For New Designs			
Transistor Type:	Current - Collector (Ic) (Max):			
2 NPN (Dual)	100mA			
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:			
40V	200mV @ 5mA, 50mA			
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:			
100nA (ICBO)	120 @ 1mA, 6V			
Power - Max:	Frequency - Transition:			
300mW	100MHz			
Operating Temperature:	Mounting Type:			
150°C (TJ)	Surface Mount			
Package / Case:	Supplier Device Package:			
SOT-563, SOT-666	SOT-666			
Base Product Number:				
PFMX1				

# **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



# PEMX1

# NPN/NPN general purpose double transistor

29 December 2022

**Product data sheet** 

### 1. General description

NPN/NPN double transistor in a SOT666 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PEMT1

#### 2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm ultra thin package
- Replaces two SC-75/SC-89 packaged transistors on same PCB area
- Reduced required PCB area
- Reduced pick and place costs

# 3. Applications

· General purpose switching and amplification

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	40	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 6 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	120	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		(TR1 TR2)
4	E2	emitter TR2	0	
5	B2	base TR2	1 2 3	
6	C1	collector TR1	SOT666	sym020



#### NPN/NPN general purpose double transistor

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	per Package					
	Name	Description	Version			
PEMX1	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	SOT666			

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PEMX1	ZZ

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or			'	'	'
$V_{CBO}$	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	40	V
$V_{EBO}$	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current			-	200	mA
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Per device	-			'		
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

#### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

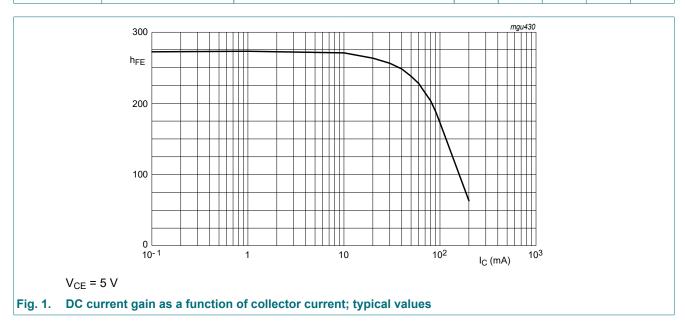
<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

#### NPN/NPN general purpose double transistor

# 10. Characteristics

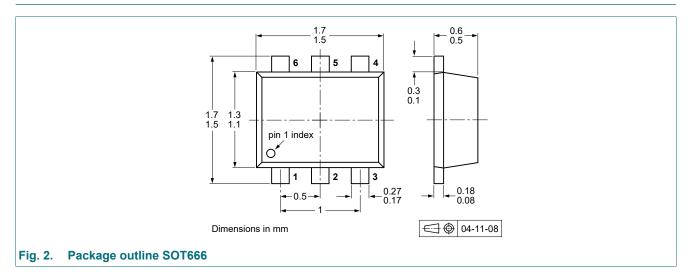
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	tor			'		
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 6 \text{ V}; I_{C} = 1 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	120	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C$ = 50 mA; $I_B$ = 5 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; $T_{amb}$ = 25 °C	-	-	200	mV
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 12 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; $f$ = 1 MHz; $T_{amb}$ = 25 °C	-	-	1.5	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 12 V; $I_{C}$ = 2 mA; f = 100 MHz; $T_{amb}$ = 25 °C	100	-	-	MHz

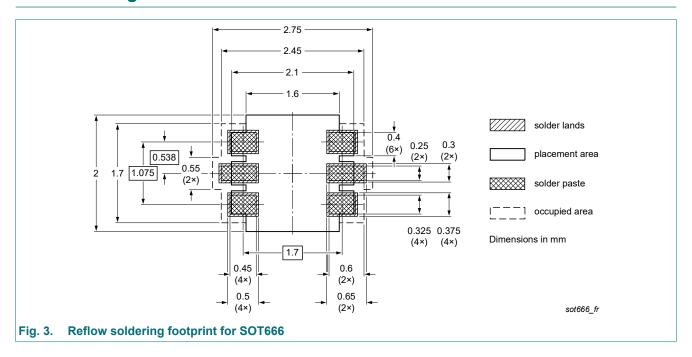


#### NPN/NPN general purpose double transistor

# 11. Package outline



# 12. Soldering



#### NPN/NPN general purpose double transistor

# 13. Revision history

#### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PEMX1 v.3	20221229	Product data sheet	-	PEMX1 v.2
Modifications:	Nexperia. • Legal texts ha	this data sheet has been rede ave been adapted to the new c anged to non-automotive qual	company name wher	, 0
PEMX1 v.2	20011107	Product data sheet	-	PEMX1 v.1
PEMX1 v.1	20010830	Product data sheet	-	-

### 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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PEMX1

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