

PMBT4403YSX Datasheet



DiGi Electronics Part Number PMBT4403YSX-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PMBT4403YSX

Description TRANS PNP 40V 0.6A TO236AB

Detailed Description Bipolar (BJT) Transistor PNP 40 V 600 mA 200MHz 2

50 mW Surface Mount TO-236AB

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PMBT4403YSX	Nexperia USA Inc.
Series:	Product Status:
-	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	600 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
40 V	750mV @ 50mA, 500mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
50nA (ICBO)	100 @ 10mA, 1V
Power - Max:	Frequency - Transition:
250 mW	200MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-236-3, SC-59, SOT-23-3	TO-236AB
Base Product Number:	
PMBT4403	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0075	



PMBT4403YS

40V, 600 mA double PNP switching transistor 2 July 2015

Product data sheet

1. General description

Double PNP switching transistor in a very small SOT363 (TSSOP6) Surface-Mounted Device (SMD) plastic package.

Double NPN complement: PMBT4401YS

2. Features and benefits

- Double general-purpose switching transistor
- AEC-Q101 qualified

3. Applications

Switching and linear amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
h _{FE}	DC current gain	V_{CE} = -2 V; I_{C} = -150 mA; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	100	-	300	
Per transistor						
V _{CEO}	collector-emitter voltage	open base	-	-	-40	V
I _C	collector current		-	-	-600	mA



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter TR1	654	6 5 4
2	В	base TR1		LA TOO
3	С	collector TR2	0	(TR1) TR2)
4	Е	emitter TR2	☐1 ☐2 ☐3 ——— —————————————————————————————————	
5	В	base TR2	TSSOP6 (SOT363)	1 2 3
6	С	collector TR1		sym018

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMBT4403YS	TSSOP6	plastic surface-mounted package; 6 leads	SOT363		

7. Marking

Table 4. Marking codes

Type number	Marking code [1]
PMBT4403YS	BJ%

[1] % = placeholder for manufacturing site code

Limiting values

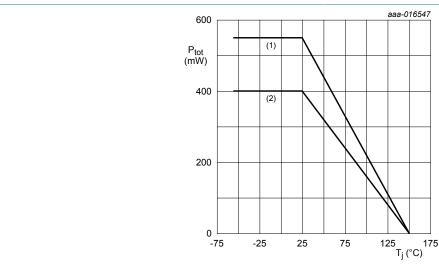
Table 5. **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transisto	r					
V _{CBO}	collector-base voltage	open emitter		-	-40	V
V_{CEO}	collector-emitter voltage	open base		-	-40	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
			[2]	-	300	mW
Per device			·			
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	400	mW
			[2]	-	550	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

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- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard
- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm²



(1) FR4 PCB; mounting pad for collector 1 cm²

(2) FR4 PCB; standard footprint

Per device: Power derating curves SOT363 (SC-88) Fig. 1.

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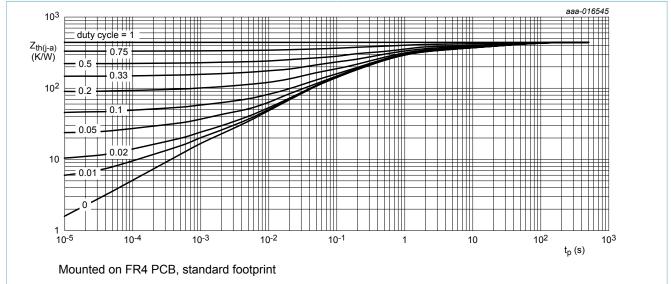
Product data sheet

Thermal characteristics

Table 6. **Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	Per transistor						
R _{th(j-a)} thermal resistance		in free air	[1]	-	-	500	K/W
	from junction to ambient		[2]	-	-	417	K/W
Per device			'		'		
R _{th(j-a)}	thermal resistance	in free air	[1]	-	-	313	K/W
from junction to ambient		[2]	-	-	227	K/W	

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm²



Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; Fig. 2. typical values

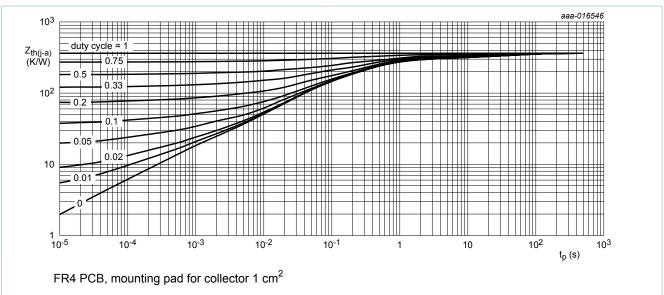
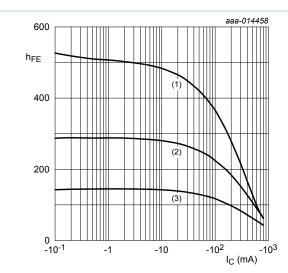


Fig. 3. Per Transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	or		·			
I _{CBO}	collector-base cut-off	V_{CB} = -40 V; I_E = 0 A; T_{amb} = 25 °C	-	-	-50	nA
	current	V_{CB} = -40 V; I_{E} = 0 A; T_{j} = 125 °C	-	-	-10	μA
I _{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I_{C} = 0 A; T_{amb} = 25 °C	-	-	-50	nA
h _{FE}	DC current gain	V_{CE} = -1 V; I_{C} = -0.1 mA; T_{amb} = 25 °C	30	-	-	
		V_{CE} = -1 V; I_{C} = -1 mA; T_{amb} = 25 °C	60	-	-	
		V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	-	
		V_{CE} = -2 V; I_{C} = -150 mA; $t_{p} \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	100	-	300	
		V_{CE} = -2 V; I_{C} = -500 mA; t_{p} ≤ 300 μ s; δ ≤ 0.02; T_{amb} = 25 °C	20	-	-	
V _{CEsat} collector-emitter saturation voltage		I_{C} = -150 mA; I_{B} = -15 mA; t_{p} ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	-400	mV
	I_{C} = -500 mA; I_{B} = -50 mA; t_{p} ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	-750	mV	
	base-emitter saturation voltage	I_{C} = -150 mA; I_{B} = -15 mA; t_{p} ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	-950	mV
		I_C = -500 mA; I_B = -50 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	-1.3	V
t _d	delay time	$I_C = -150 \text{ mA}; I_{Bon} = -15 \text{ mA};$	-	-	15	ns
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	30	ns
t _{on}	turn-on time		-	-	40	ns
t _s	storage time		-	-	300	ns
t _f	fall time		-	-	50	ns
t _{off}	turn-off time		-	-	350	ns
C _C	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	8.5	pF
C _E	emitter capacitance	V_{EB} = -500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	35	pF
f _T	transition frequency	V_{CE} = -10 V; I_{C} = -20 mA; f = 100 MHz; T_{amb} = 25 °C	200	-	-	MHz



$$V_{CE} = -2 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = -55 °C$$

Fig. 4. DC current gain as a function of collector

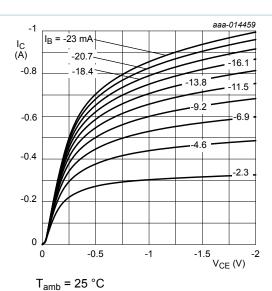
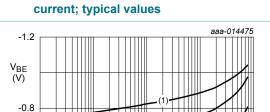
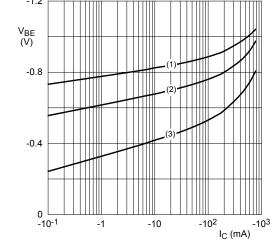


Fig. 5. Collector current as a function of collectoremitter voltage; typical values





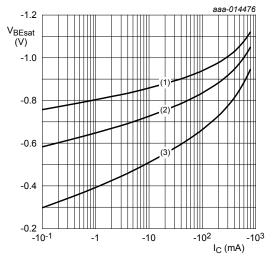
$$V_{CE} = -2 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Base-emitter voltage as a function of collector Fig. 6. current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

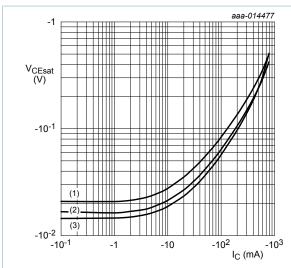
(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Base-emitter saturation voltage as a function of Fig. 7. collector current; typical values

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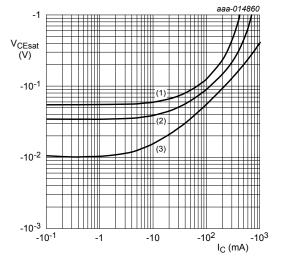
$$I_{\rm C}/I_{\rm B}=20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55$$
 °C

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values



$$T_{amb}$$
 = 25 °C

(1)
$$I_C/I_B = 100$$

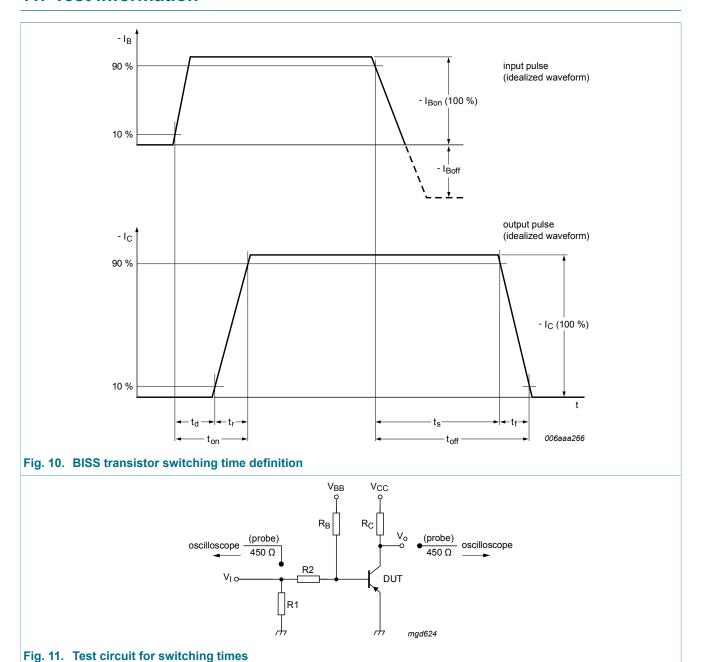
(2)
$$I_C/I_B = 50$$

(3)
$$I_C/I_B = 10$$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

40V, 600 mA double PNP switching transistor

11. Test information



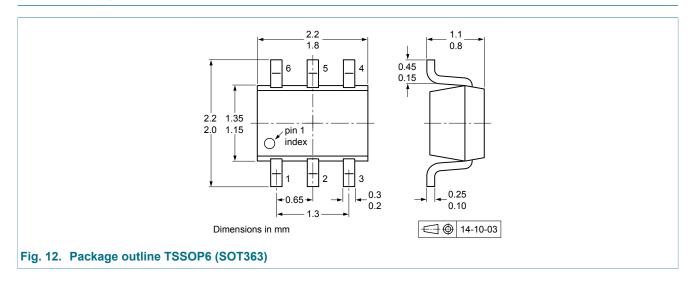
11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

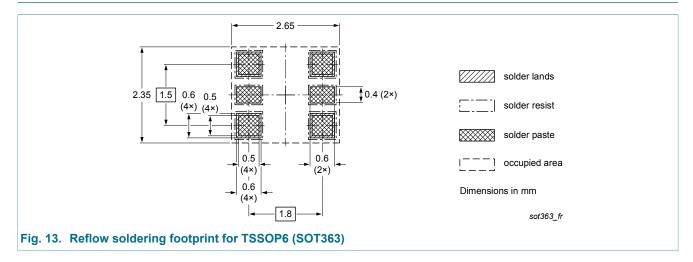
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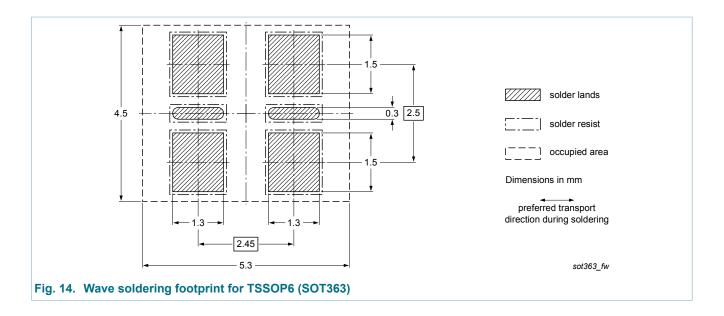
40V, 600 mA double PNP switching transistor

12. Package outline



13. Soldering





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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT4403YS v.1	20150702	Product data sheet	-	-

40V, 600 mA double PNP switching transistor

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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40V, 600 mA double PNP switching transistor

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