

PMBTA42-QVL Datasheet



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DiGi Electronics Part Number PMBTA42-QVL-DG

Manufacturer Nexperia USA Inc.

Manufacturer Product Number PMBTA42-QVL

Description PMBTA42-Q/SOT23/TO-236AB

Detailed Description Bipolar (BJT) Transistor NPN 300 V 100 mA 50MHz 2

50 mW Surface Mount TO-236AB



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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PMBTA42-QVL	Nexperia USA Inc.
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN	100 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
300 V	500mV @ 2mA, 20mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100nA (ICBO)	40 @ 30mA, 10V
Power - Max:	Frequency - Transition:
250 mW	50MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-236-3, SC-59, SOT-23-3	TO-236AB

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	
REACH Unaffected	



PMBTA42-Q

300 V, 100 mA NPN high-voltage transistor

Product data sheet

1. General description

NPN high-voltage transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High voltage (max. 300 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

· Telephony and professional communication equipment

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	300	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 1 mA; T _{amb} = 25 °C	25	-	-	
		V _{CE} = 10 V; I _C = 10 mA; T _{amb} = 25 °C	40	-	-	
		V_{CE} = 10 V; I_{C} = 30 mA; T_{amb} = 25 °C	40	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base]3	С
2	E	emitter		j
3	С	collector		В — (
			SOT23	 E sym021



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6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMBTA42-Q		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMBTA42-Q	%1D

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	300	V
V_{CEO}	collector-emitter voltage	open base		-	300	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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10. Characteristics

Table 7. Characteristics

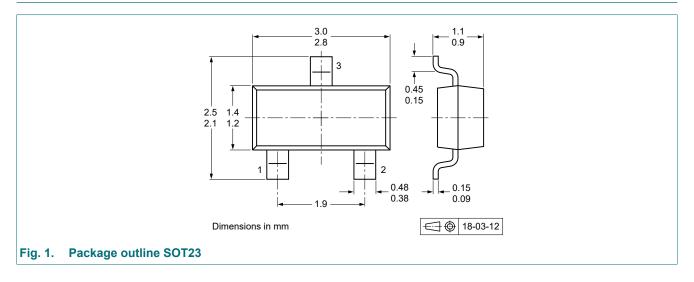
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 200 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 1 mA; T _{amb} = 25 °C	25	-	-	
		V_{CE} = 10 V; I_{C} = 10 mA; T_{amb} = 25 °C	40	-	-	
		V _{CE} = 10 V; I _C = 30 mA; T _{amb} = 25 °C	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 20 \text{ mA}; I_B = 2 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	-	500	mV
V _{BEsat}	base-emitter saturation voltage		-	-	900	mV
C _{re}	feedback capacitance	$V_{CB} = 20 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 ^{\circ}\text{C}$	-	-	3	F
f _T	transition frequency	V_{CE} = 20 V; I_{C} = 10 mA; f = 100 MHz; T_{amb} = 25 °C	50	-	-	MHz

11. Test information

Quality information

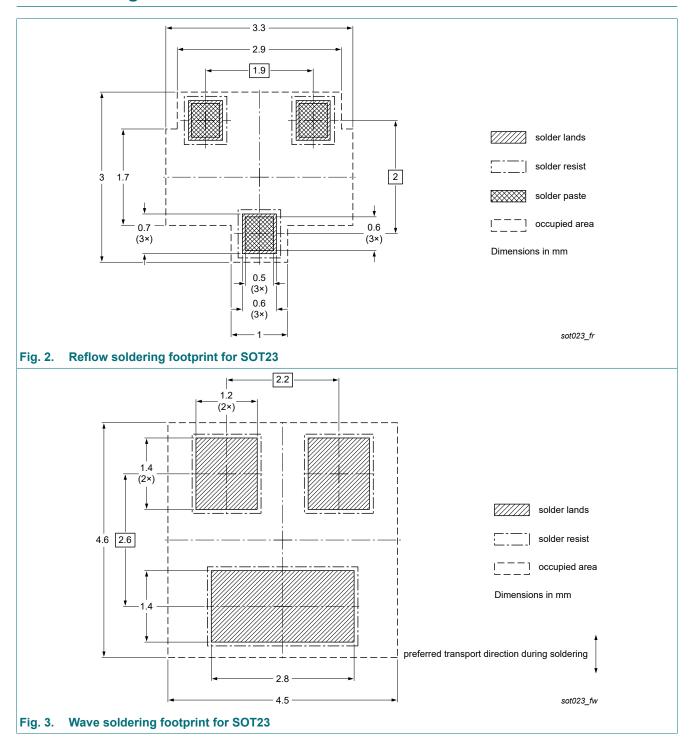
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



300 V, 100 mA NPN high-voltage transistor

13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBTA42-Q v.1	20230703	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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