

# PMBTA56,235 Datasheet

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iGi Electronics Part Number	PMBTA56,235-DG
Manufacturer	Nexperia USA Inc.
nufacturer Product Number	PMBTA56,235
Description	TRANS PNP 80V 0.5A TO236AB
Detailed Description	Bipolar (BJT) Transistor PNP 80 V 500 mA 50MHz 25 0 mW Surface Mount TO-236AB

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# Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PMBTA56,235	Nexperia USA Inc.
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	500 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
80 V	250mV @ 10mA, 100mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
500nA (ICBO)	100 @ 100mA, 1V
Power - Max:	Frequency - Transition:
250 mW	50MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-236-3, SC-59, SOT-23-3	ТО-236АВ
Base Product Number:	
PMBTA56	

# **Environmental & Export classification**

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0095	



PMBTA56 PNP general purpose transistor 1 April 2023

**Product data sheet** 

## 1. General description

PNP general-purpose transistor in a small SOT23 plastic package. NPN complement: PMBTA06.

## 2. Features and benefits

- High current (max. 500 mA)
- Low voltage (max. 80 V).

## 3. Applications

• General purpose switching and amplification, e.g. telephony and professional communication equipment.

## 4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-80	V
I <sub>C</sub>	collector current			-	-	-500	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C		100	-	-	

# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	E	emitter		С
3	C	collector		B E sym132

# 6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PMBTA56	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>		

# nexperia

# 7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMBTA56	%2G

[1] % = placeholder for manufacturing site code

# 8. Limiting values

## Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-1	А
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

# 9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

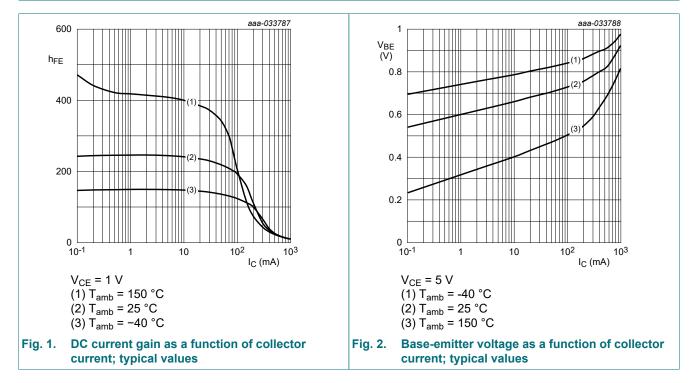
[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

# **10. Characteristics**

#### **Table 7. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified

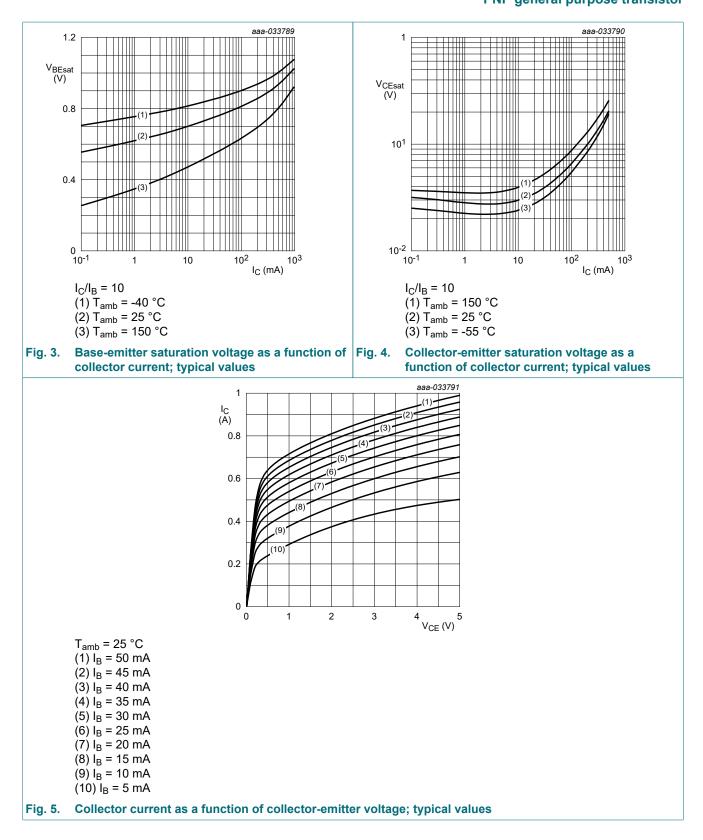
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = -100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-80	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = -1 mA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C	-80	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage (collector open)	$I_{E}$ = -100 µA; $I_{C}$ = 0 A; $T_{amb}$ = 25 °C	-5	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -80 V; I <sub>E</sub> = 0 A	-	-	-50	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A	-	-	-50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	100	-	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	100	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -100 mA; I <sub>B</sub> = -10 mA	-	-	-0.25	V
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	-	-	-1.2	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA; f = 100 MHz	50	-	-	MHz



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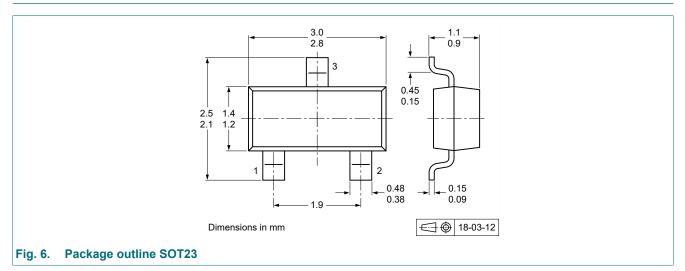
# PNP general purpose transistor

PMBTA56

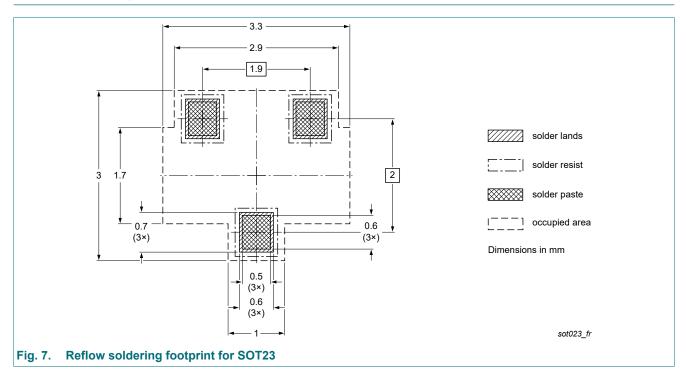


## **PNP** general purpose transistor

# 11. Package outline



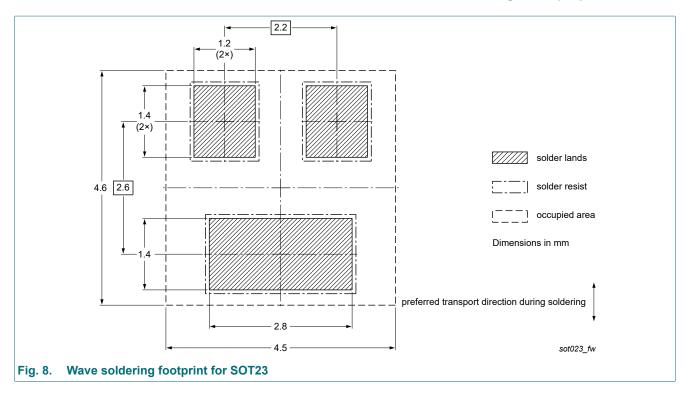
# 12. Soldering



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# PMBTA56

## **PNP** general purpose transistor



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# 13. Revision history

Data sheet ID Release date Data sheet status Change Supersede						
Data sneet ID	Release date	Data sneet status	Change notice	Supersedes		
PMBTA56 v.3	20230401	Product data sheet	-	PMBTA56 v.2		
Modifications:	of Nexperia • Legal texts	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Product changed to non automotive. Please refer to the automotive product(s) with -Q.</li> </ul>				
PMBTA56 v.2	20040109	Product data sheet	-	PMBTA56 v.1		
PMBTA56 v.1	19990409	Product specification	-	-		

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# 14. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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### **PNP** general purpose transistor

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