

PMV75UP,215 Datasheet



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DiGi Electronics Part Number	PMV75UP,215-DG
Manufacturer	Nexperia USA Inc.
Manufacturer Product Number	PMV75UP,215
Description	MOSFET P-CH 20V 2.5A TO236AB
Detailed Description	P-Channel 20 V 2.5A (Ta) 490mW (Ta), 5W (Tc) Surface Mount TO-236AB



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Purchase and inquiry

Manufacturer Product Number:

PMV75UP,215

Series:

-

FET Type:

P-Channel

Drain to Source Voltage (Vdss):

20 V

Drive Voltage (Max Rds On, Min Rds On):

1.8V, 4.5V

Vgs(th) (Max) @ Id:

900mV @ 250µA

Vgs (Max):

±12V

FET Feature:

-

Operating Temperature:

-55°C ~ 150°C (Tj)

Supplier Device Package:

TO-236AB

Base Product Number:

PMV75

Manufacturer:

Nexperia USA Inc.

Product Status:

Active

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

2.5A (Ta)

Rds On (Max) @ Id, Vgs:

102mOhm @ 2.5A, 4.5V

Gate Charge (Qg) (Max) @ Vgs:

7.5 nC @ 4.5 V

Input Capacitance (Ciss) (Max) @ Vds:

550 pF @ 10 V

Power Dissipation (Max):

490mW (Ta), 5W (Tc)

Mounting Type:

Surface Mount

Package / Case:

TO-236-3, SC-59, SOT-23-3

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.29.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



PMV75UP

20 V, P-channel Trench MOSFET

25 April 2014

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- Enhanced power dissipation capability: $P_{tot} = 1000 \text{ mW}$

3. Applications

- LED driver
- Power management
- High-side loadswitch
- Switching circuits

4. Quick reference data

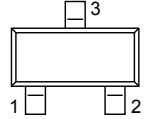
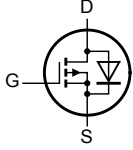
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25 \text{ }^\circ\text{C}$	-	-	-20	V
V_{GS}	gate-source voltage		-12	-	12	V
I_D	drain current	$V_{GS} = -4.5 \text{ V}$; $T_{amb} = 25 \text{ }^\circ\text{C}$; $t \leq 5 \text{ s}$	[1]	-	-3.2	A
Static characteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = -4.5 \text{ V}$; $I_D = -2.5 \text{ A}$; $T_j = 25 \text{ }^\circ\text{C}$	-	77	102	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm^2 .

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 TO-236AB (SOT23)	 017aaa257
2	S	source		
3	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMV75UP	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code [1]
PMV75UP	%CN

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ °C}$		-	-20	V
V_{GS}	gate-source voltage			-12	12	V
I_D	drain current	$V_{GS} = -4.5\text{ V}; T_{amb} = 25\text{ °C}; t \leq 5\text{ s}$	[1]	-	-3.2	A
		$V_{GS} = -4.5\text{ V}; T_{amb} = 25\text{ °C}$	[1]	-	-2.5	A
		$V_{GS} = -4.5\text{ V}; T_{amb} = 100\text{ °C}$	[1]	-	-1.6	A
I_{DM}	peak drain current	$T_{amb} = 25\text{ °C}; \text{single pulse}; t_p \leq 10\text{ }\mu\text{s}$		-	-10	A
P_{tot}	total power dissipation	$T_{amb} = 25\text{ °C}$	[2]	-	490	mW
			[1]	-	1000	mW
		$T_{sp} = 25\text{ °C}$		-	5000	mW
T_j	junction temperature			-55	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
Source-drain diode						
I_S	source current	$T_{amb} = 25\text{ °C}$	[1]	-	-0.9	A

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm^2 .
 [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

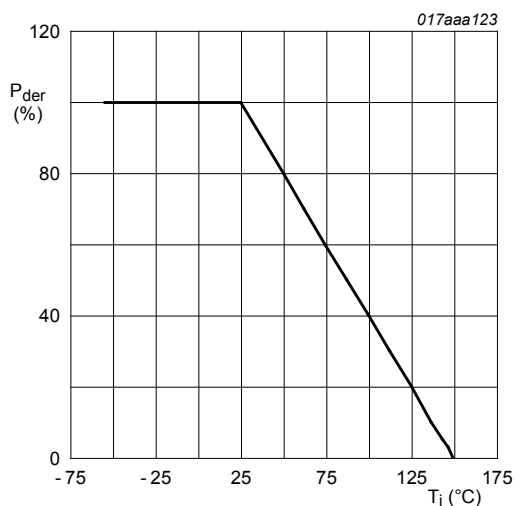


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ\text{C})}} \times 100\%$$

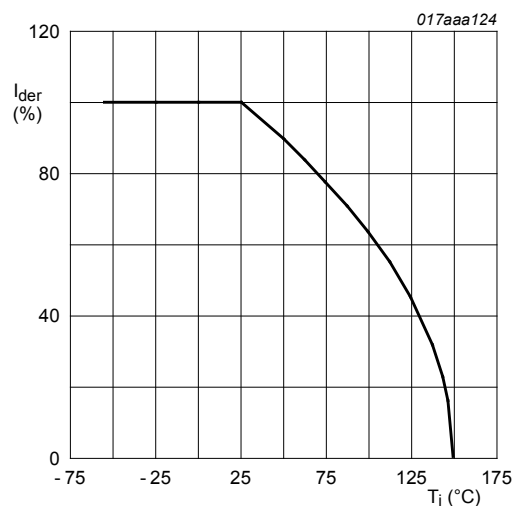
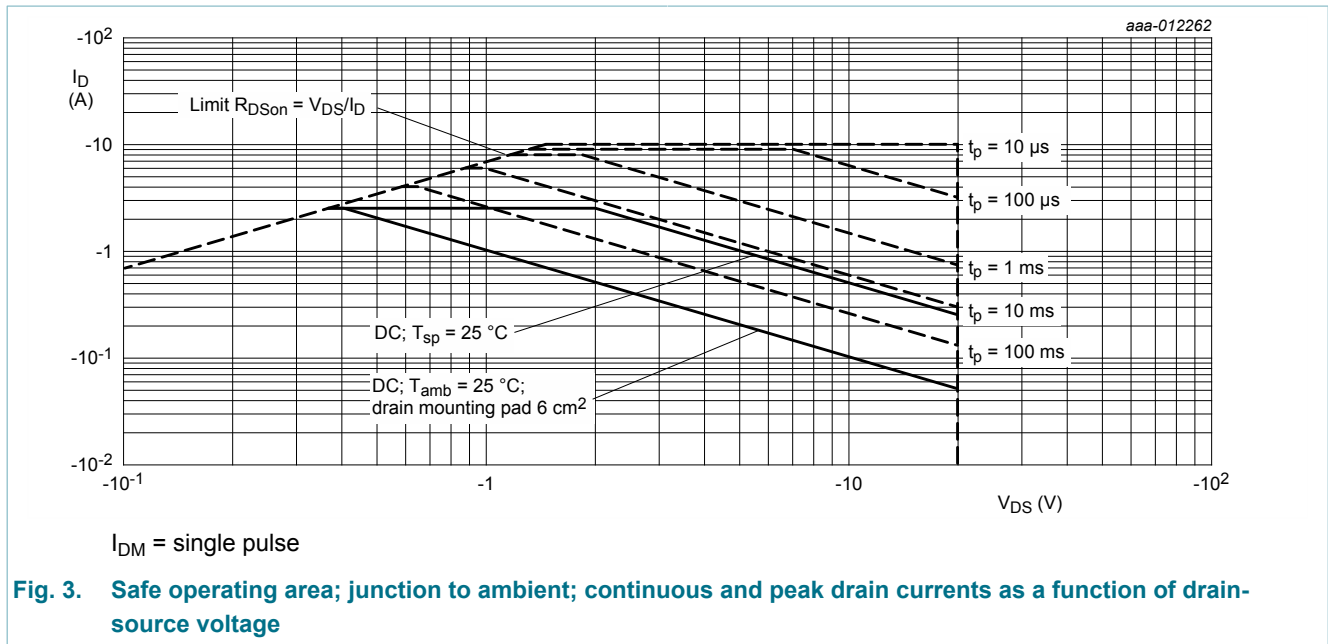


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^\circ\text{C})}} \times 100\%$$



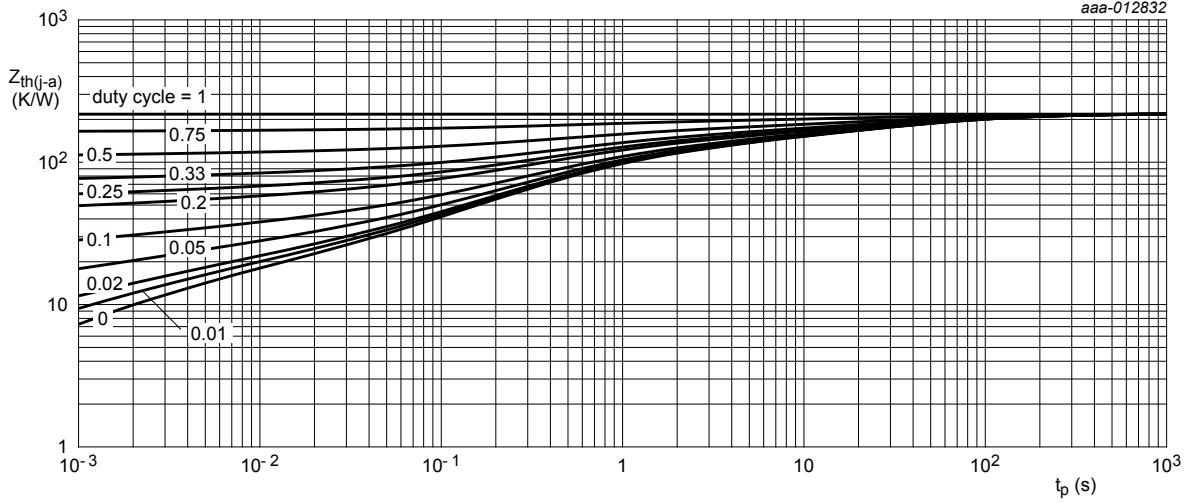
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	217	255	K/W
			[2]	-	105	124	K/W
		in free air; t ≤ 5 s	[2]	-	73	86	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	20	25	K/W

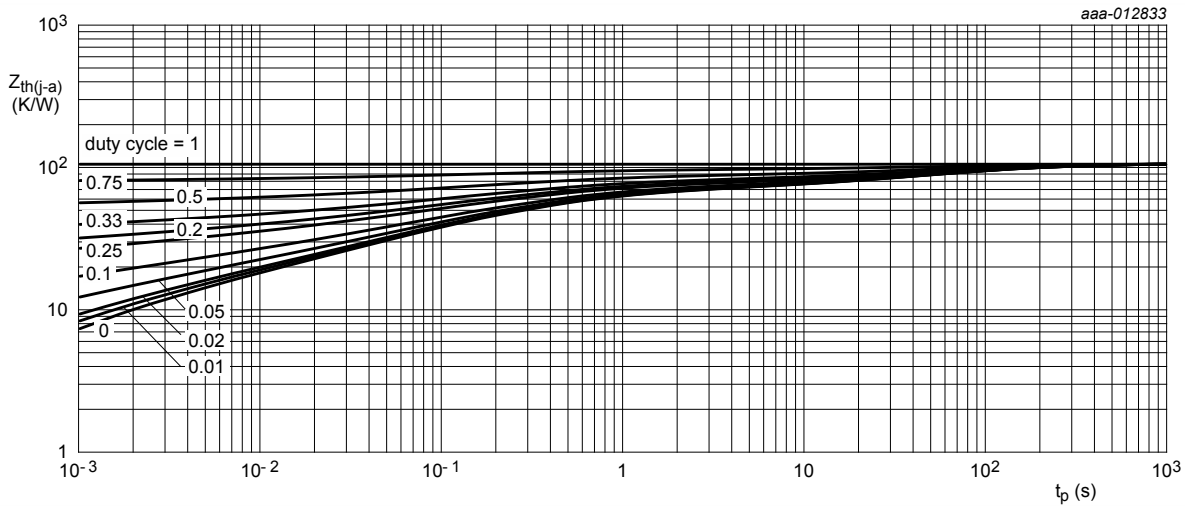
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 6 cm^2

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = -250 \mu A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = -250 \mu A$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ }^\circ C$	-0.47	-0.68	-0.9	V
I_{DSS}	drain leakage current	$V_{DS} = -20 V$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	-1	μA
I_{GSS}	gate leakage current	$V_{GS} = 12 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	100	nA
		$V_{GS} = -12 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	-100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = -4.5 V$; $I_D = -2.5 A$; $T_j = 25 \text{ }^\circ C$	-	77	102	m Ω
		$V_{GS} = -4.5 V$; $I_D = -2.4 A$; $T_j = 150 \text{ }^\circ C$	-	110	146	m Ω
		$V_{GS} = -2.5 V$; $I_D = -2.2 A$; $T_j = 25 \text{ }^\circ C$	-	95	125	m Ω
		$V_{GS} = -1.8 V$; $I_D = -1 A$; $T_j = 25 \text{ }^\circ C$	-	120	156	m Ω
g_{fs}	forward transconductance	$V_{DS} = -10 V$; $I_D = -2 A$; $T_j = 25 \text{ }^\circ C$	-	15	-	S
R_G	internal gate resistance (AC)	$f = 1 \text{ MHz}$	-	41	-	Ω
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$V_{DS} = -10 V$; $I_D = -2.5 A$; $V_{GS} = -4.5 V$; $T_j = 25 \text{ }^\circ C$	-	5	7.5	nC
Q_{GS}	gate-source charge		-	0.7	-	nC
Q_{GD}	gate-drain charge		-	0.9	-	nC
C_{iss}	input capacitance	$V_{DS} = -10 V$; $f = 1 \text{ MHz}$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	550	-	pF
C_{oss}	output capacitance		-	63	-	pF
C_{rss}	reverse transfer capacitance		-	53	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = -10 V$; $I_D = -2.5 A$; $V_{GS} = -4.5 V$; $R_{G(ext)} = 6 \Omega$; $T_j = 25 \text{ }^\circ C$	-	6	-	ns
t_r	rise time		-	14	-	ns
$t_{d(off)}$	turn-off delay time		-	120	-	ns
t_f	fall time		-	50	-	ns
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = -0.9 A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-0.8	-1.2	V

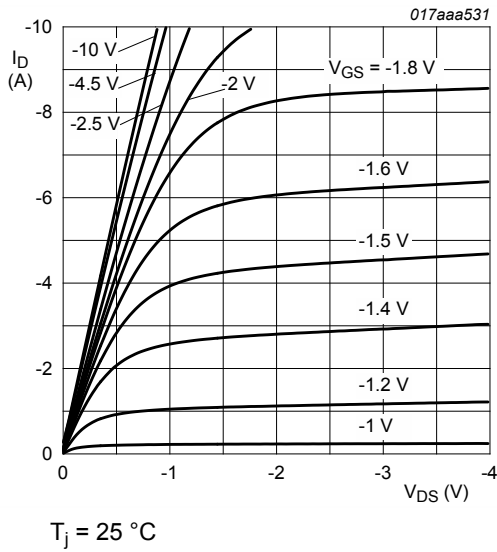


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

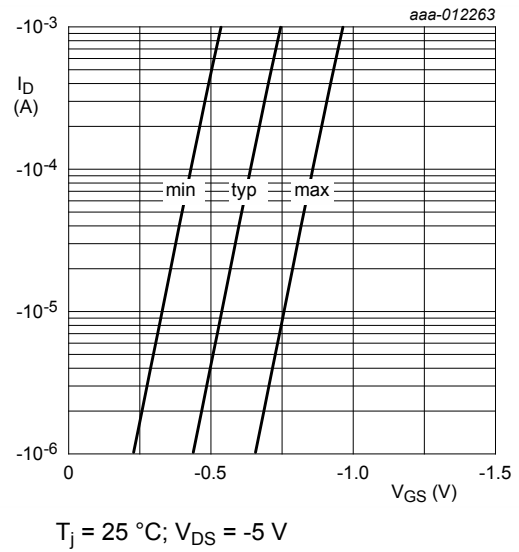


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

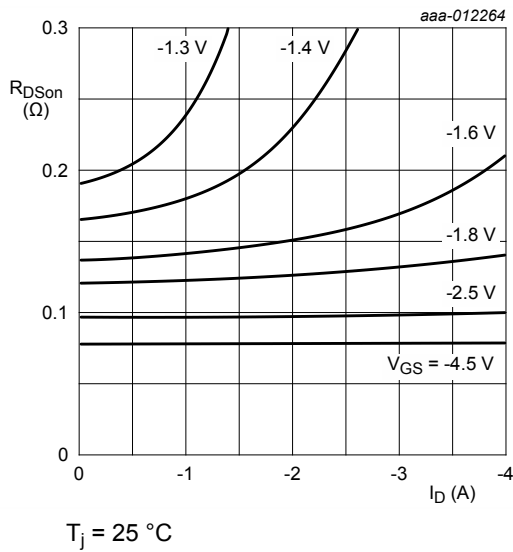


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

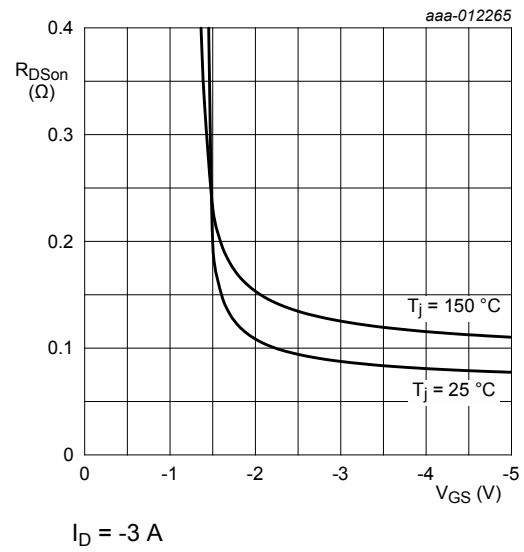
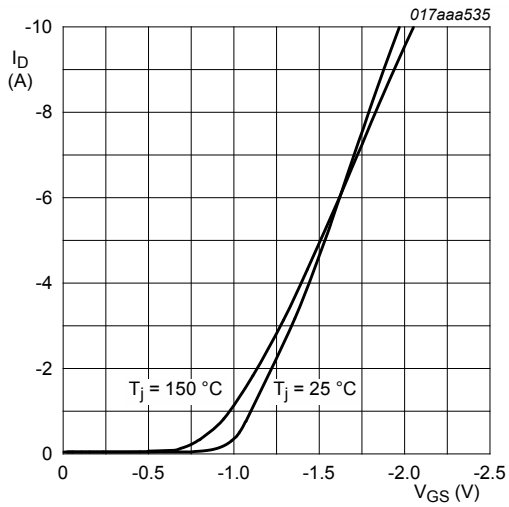


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



$$V_{DS} > I_D \times R_{DS(on)}$$

Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

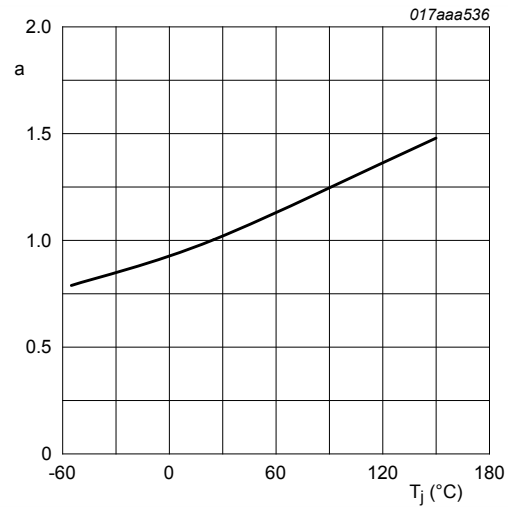
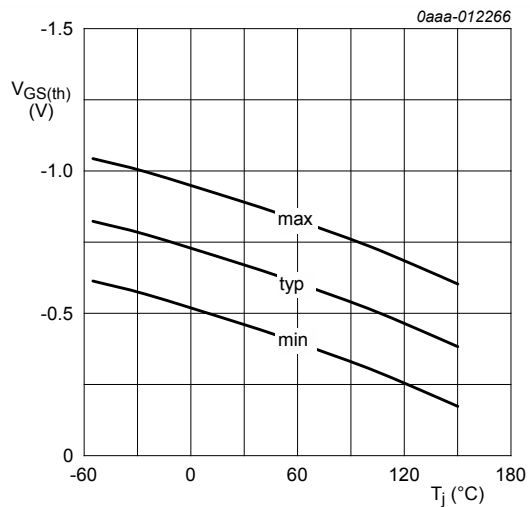


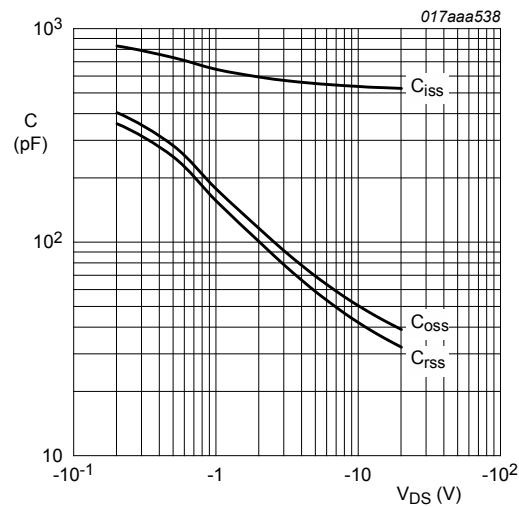
Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DS(on)}}{R_{DS(on)(25^\circ C)}}$$



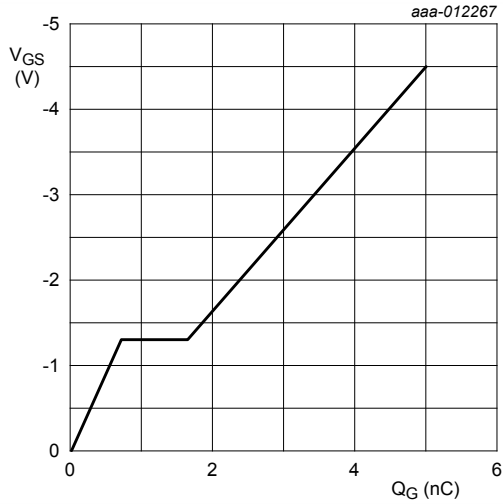
$$I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}$$

Fig. 12. Gate-source threshold voltage as a function of junction temperature



$$f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}$$

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = -2.8$ A; $V_{DS} = -10$ V; $T_{amb} = 25$ °C

Fig. 14. Gate-source voltage as a function of gate charge; typical values

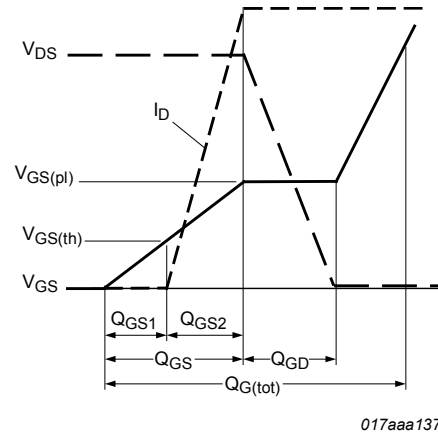
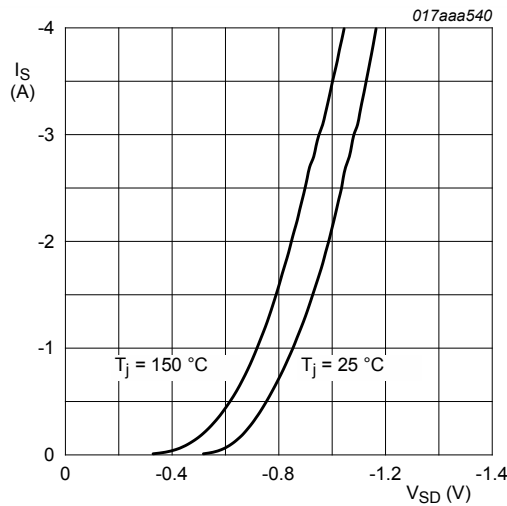


Fig. 15. Gate charge waveform definitions



$V_{GS} = 0$ V

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

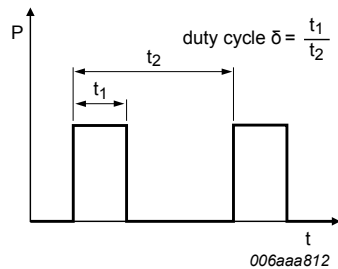


Fig. 17. Duty cycle definition

12. Package outline

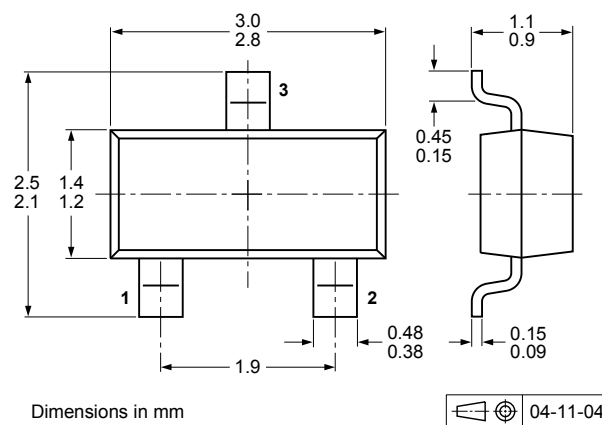


Fig. 18. Package outline TO-236AB (SOT23)

13. Soldering

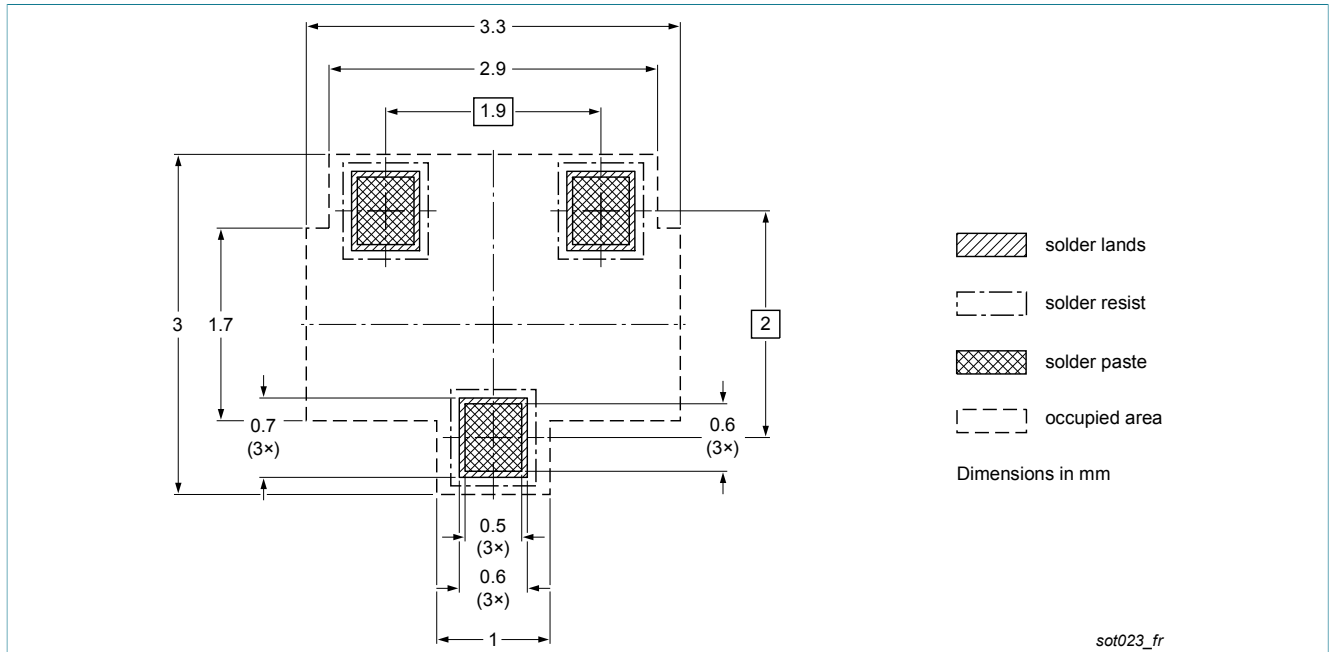


Fig. 19. Reflow soldering footprint for TO-236AB (SOT23)

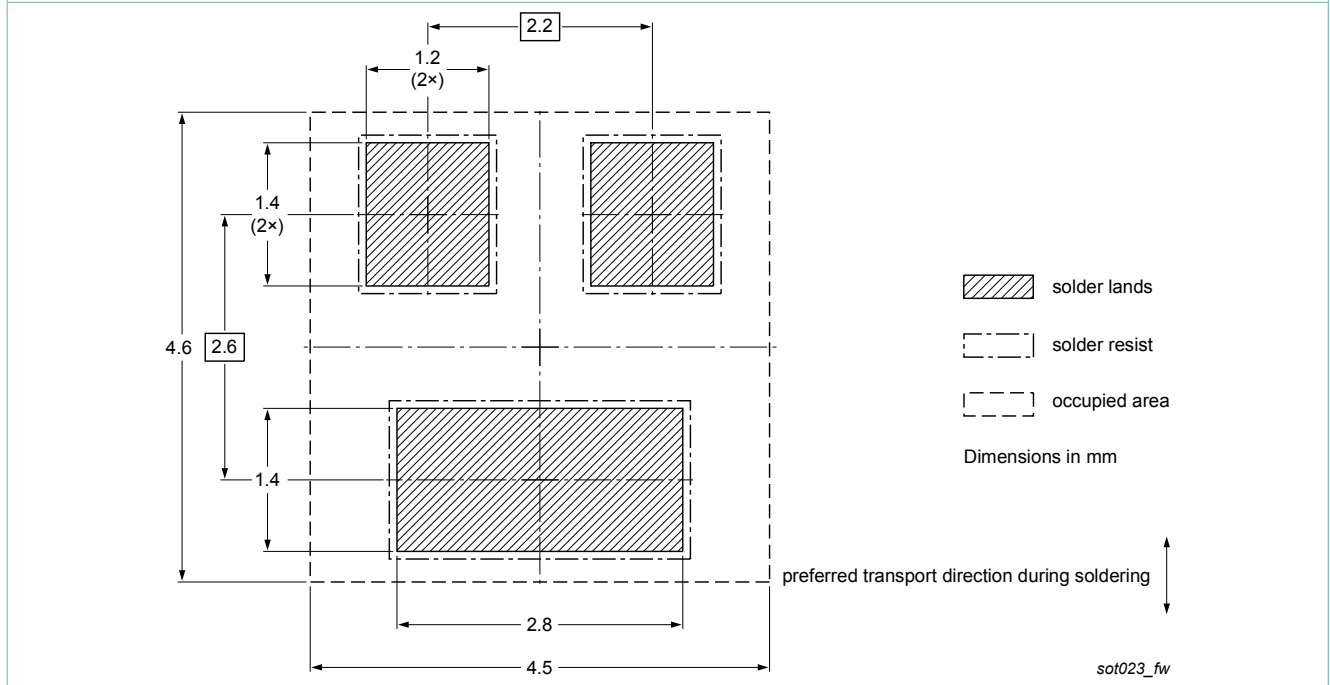


Fig. 20. Wave soldering footprint for TO-236AB (SOT23)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMV75UP v.1	20140425	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 25 April 2014

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