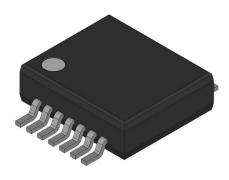


# 74HC11DB,118 Datasheet

www.digi-electronics.com



DiGi Electronics Part Number	74HC11DB,118-DG
Manufacturer	NXP Semiconductors
Manufacturer Product Number	74HC11DB,118
Description	IC GATE AND
Detailed Description	IC Channel

https://www.DiGi-Electronics.com



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



# **Purchase and inquiry**

Manufacturer Product Number:

74HC11DB,118

Series:

Base Product Number: 74HC11 Manufacturer: NXP Semiconductors Product Status: Active

# **Environmental & Export classification**

### Moisture Sensitivity Level (MSL):

Vendor Undefined

REACH Status:

**REACH Unaffected** 



**Product data sheet** 

# 1. General description

The 74HC11; 74HCT11 is a triple 3-input AND gate. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

### 2. Features and benefits

- Wide supply voltage range from 2.0 to 6.0 V
- CMOS low power dissipation
- High noise immunity
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Input levels:
  - For 74HC11: CMOS level
  - For 74HCT11: TTL level
- Complies with JEDEC standards:
  - JESD8C (2.7 V to 3.6 V)
  - JESD7A (2.0 V to 6.0 V)
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

### 3. Ordering information

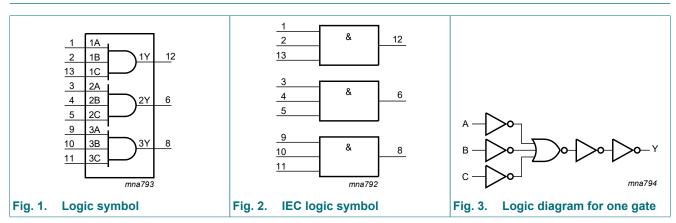
### Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
<u>74HC11D</u> 74HCT11D	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	<u>SOT108-1</u>
<u>74HC11PW</u> <u>74HCT11PW</u>	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	<u>SOT402-1</u>



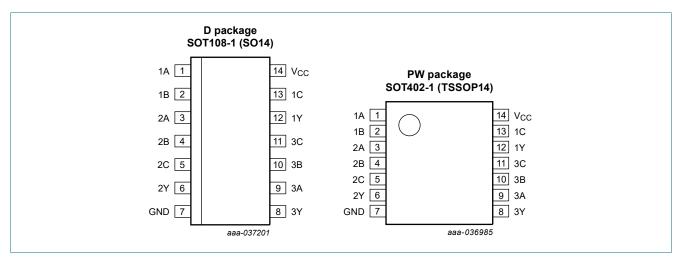
**Triple 3-input AND gate** 

# 4. Functional diagram



# 5. Pinning information

### 5.1. Pinning



### 5.2. Pin description

### Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A	1, 3, 9	data input
1B, 2B, 3B	2, 4, 10	data input
GND	7	ground (0 V)
1C, 2C, 3C	13, 5, 11	data input
1Y, 2Y, 3Y	12, 6, 8	data output
V <sub>CC</sub>	14	supply voltage

# 6. Functional description

### Table 3. Function selection

H = HIGH voltage level; L = LOW voltage level; X = don't care

Input	nput d					
nA	nB	nC	nY			
L	Х	Х	L			
Х	L	Х	L			
Х	Х	L	L			
Н	Н	Н	Н			

### 7. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>CC</sub>	supply voltage		-0.5	+7	V
I <sub>IK</sub>	input clamping current	$V_{\rm I} < -0.5 \text{ V or } V_{\rm I} > V_{\rm CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I <sub>OK</sub>	output clamping current	$V_{\rm O} < -0.5 \text{ V or } V_{\rm O} > V_{\rm CC} + 0.5 \text{ V}$ [1]	-	±20	mA
lo	output current	$-0.5 V < V_O < V_{CC} + 0.5 V$	-	±25	mA
I <sub>CC</sub>	supply current		-	50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	[2]	-	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT108-1 (SO14) package: P<sub>tot</sub> derates linearly with 10.1 mW/K above 100 °C.

For SOT402-1 (TSSOP14) package:  $\mathsf{P}_{tot}$  derates linearly with 7.3 mW/K above 81 °C.

# 8. Recommended operating conditions

### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions	74HC11		74HCT11			Unit	
			Min	Тур	Max	Min	Тур	Max	
V <sub>CC</sub>	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
Vo	output voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 2.0 V	-	-	625	-	-	-	ns/V
		V <sub>CC</sub> = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V <sub>CC</sub> = 6.0 V	-	-	83	-	-	-	ns/V

# 9. Static characteristics

### Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	1
74HC11		1				1	1			1
V <sub>IH</sub>	HIGH-level	V <sub>CC</sub> = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
	input voltage	V <sub>CC</sub> = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V <sub>CC</sub> = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
VIL	LOW-level	V <sub>CC</sub> = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
	input voltage	V <sub>CC</sub> = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V <sub>CC</sub> = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V
V <sub>OH</sub>	HIGH-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>								
	output voltage	I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 6.0 V	5.9	6.0	-	5.9	-	5.9	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V	3.98	4.32	-	3.84	-	3.7	-	V
		I <sub>O</sub> = -5.2 mA; V <sub>CC</sub> = 6.0 V	5.48	5.81	-	5.34	-	5.2	-	V
V <sub>OL</sub>	LOW-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>								
	output voltage	I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 6.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V	-	0.16	0.26	-	0.33	-	0.4	V
lı	input leakage current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 6.0 V$	-	-	±0.1	-	±1	-	±1	μA
I <sub>CC</sub>	supply current	$V_{I} = V_{CC} \text{ or GND; } I_{O} = 0 \text{ A;}$ $V_{CC} = 6.0 \text{ V}$	-	-	2.0	-	20	-	40	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF
74HCT1	1	1	1	1	1	1			1	-
V <sub>IH</sub>	HIGH-level input voltage	$V_{CC}$ = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	2.0	-	V
V <sub>IL</sub>	LOW-level input voltage	$V_{CC}$ = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = -20 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -4.0 mA	3.98	4.32	-	3.84	-	3.7	-	V
V <sub>OL</sub>	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = 20 μA	-	0	0.1	-	0.1	-	0.1	V
I	input leakage current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 5.5$ V	-	-	±0.1	-	±1	-	±1	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	2.0	-	20	-	40	μA

74HC\_HCT11

### **Nexperia**

# 74HC11; 74HCT11

### **Triple 3-input AND gate**

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Мах	]
00	additional supply current	per input pin; $V_1 = V_{CC} - 2.1 \text{ V}; I_0 = 0 \text{ A};$ other inputs at $V_{CC}$ or GND; $V_{CC} = 4.5 \text{ V}$ to 5.5 V	-	100	360	-	450	-	490	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF

### 10. Dynamic characteristics

### Table 7. Dynamic characteristics

GND = 0 V;  $C_L = 50 pF$ ; for test circuit see Fig. 5.

Symbol	I Parameter Conditions 25 °C -		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit			
			Min	Тур	Мах	Min	Max	Min	Max	
74HC11										-
t <sub>pd</sub>	propagation	nA, nB to nY; see Fig. 4 [1]								
	delay	V <sub>CC</sub> = 2.0 V	-	32	100	-	125	-	150	ns
		V <sub>CC</sub> = 4.5 V	-	12	20	-	25	-	30	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF	-	9	-	-	-	-	-	ns
		V <sub>CC</sub> = 6.0 V	-	10	17	-	21	-	26	ns
tt	transition time	see <u>Fig. 4</u> [2]								
		V <sub>CC</sub> = 2.0 V	-	19	75	-	95	-	110	ns
		V <sub>CC</sub> = 4.5 V	-	7	15	-	19	-	22	ns
		V <sub>CC</sub> = 6.0 V	-	6	13	-	16	-	19	ns
C <sub>PD</sub>	power dissipation capacitance	per package; [3] $V_I = GND$ to $V_{CC}$	-	18	-	-	-	-	-	pF
74HCT11	1									
t <sub>pd</sub>	propagation	nA, nB to nY; see Fig. 4 [1]								
	delay	V <sub>CC</sub> = 4.5 V	-	16	24	-	30	-	36	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF	-	11	-	-	-	-	-	ns
t <sub>t</sub>	transition time	V <sub>CC</sub> = 4.5 V; see <u>Fig. 4</u> [2]	-	7	15	-	19	-	22	ns
C <sub>PD</sub>	power dissipation capacitance	per package; [3] V <sub>I</sub> = GND to V <sub>CC</sub> - 1.5 V	-	20	-	-	-	-	-	pF

[1]  $t_{pd}$  is the same as  $t_{PHL}$  and  $t_{PLH}$ . [2]  $t_t$  is the same as  $t_{THL}$  and  $t_{TLH}$ . [3]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in µW):  $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

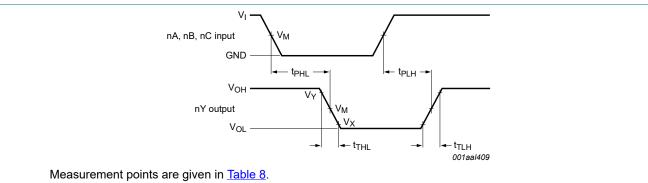
V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of outputs.

### **Triple 3-input AND gate**

### 10.1. Waveforms and test circuit

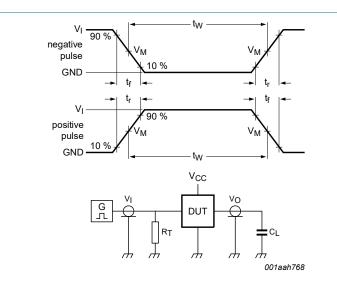


 $V_{\text{OL}}$  and  $V_{\text{OH}}$  are typical voltage output levels that occur with the output load.

#### Fig. 4. Input to output propagation delays

#### Table 8. Measurement points

Туре	Input	Dutput			
	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>	
74HC11	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>	0.1V <sub>CC</sub>	0.9V <sub>CC</sub>	
74HCT11	1.3 V	1.3 V	0.1V <sub>CC</sub>	0.9V <sub>CC</sub>	



Test data is given in Table 9.

Definitions test circuit:

 $R_T$  = termination resistance should be equal to output impedance  $Z_o$  of the pulse generator;

C<sub>L</sub> = load capacitance including jig and probe capacitance.

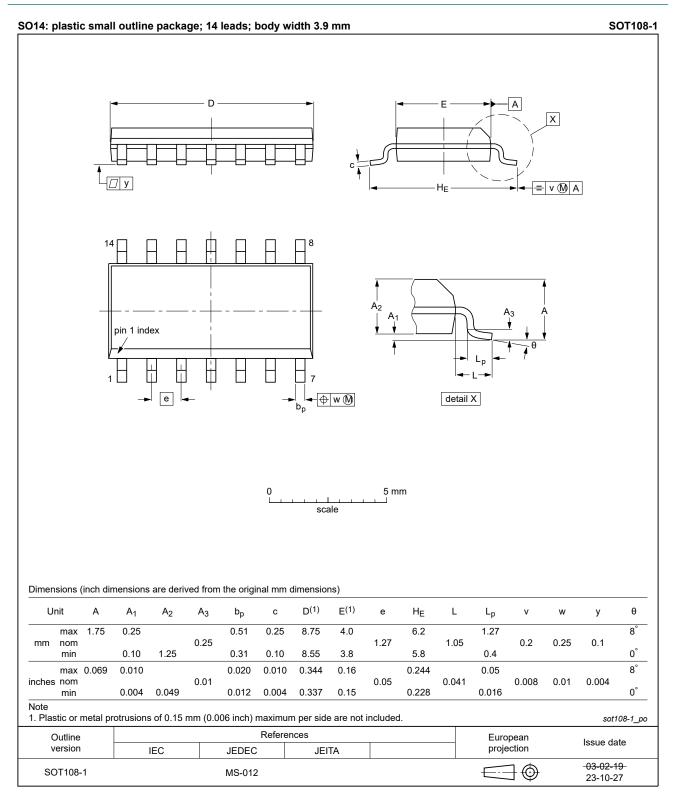
### Fig. 5. Test circuit for measuring switching times

Table 9. Test dat	a			
Туре	Input		Load	Test
	VI	t <sub>r</sub> , t <sub>f</sub>	CL	
74HC11	V <sub>CC</sub>	6.0 ns	15 pF, 50 pF	t <sub>PLH</sub> , t <sub>PHL</sub>
74HCT11	3.0 V	6.0 ns	15 pF, 50 pF	t <sub>PLH</sub> , t <sub>PHL</sub>

74HC\_HCT11

**Triple 3-input AND gate** 

# 11. Package outline



#### Fig. 6. Package outline SOT108-1 (SO14)

### Nexperia

# 74HC11; 74HCT11

### **Triple 3-input AND gate**

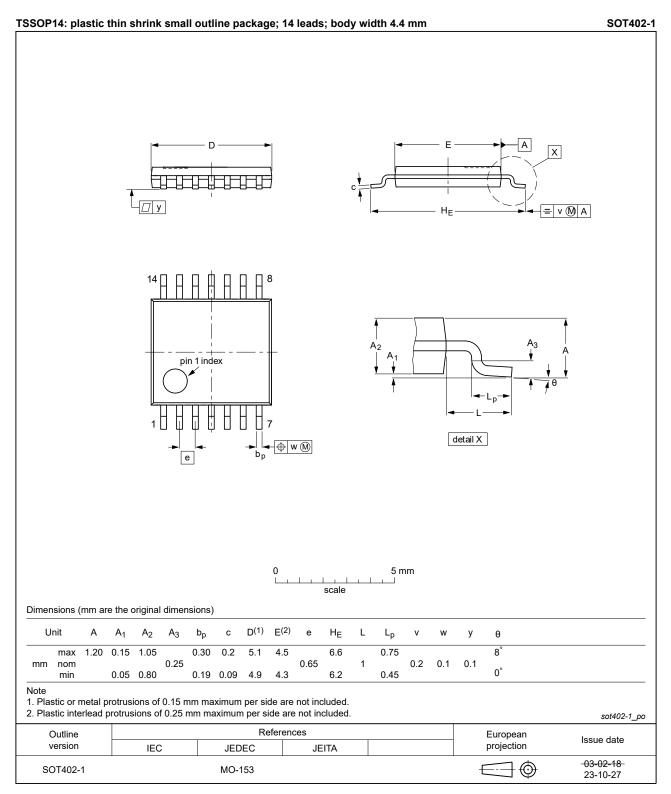


Fig. 7. Package outline SOT402-1 (TSSOP14)

# 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
TTL	Transistor-Transistor Logic

# 13. Revision history

Table 11. Revision history	Release date	Data aboat atatus	Change notice	Supercodes	
Document ID	Release date	Data sheet status	Change notice	Supersedes	
74HC_HCT11 v.9	20240221	Product data sheet	-	74HC_HCT11 v.8	
Modifications:	<ul> <li><u>Section 2</u>: ESD specification updated according to the latest JEDEC standard.</li> <li><u>Fig. 6</u>, <u>Fig. 7</u>: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153</li> </ul>				
74HC_HCT11 v.8	20210811	Product data sheet	-	74HC_HCT11 v.7	
Modifications:	<ul> <li><u>Section 2</u> updated.</li> <li>Type number 74HC11DB (SOT337-1/SSOP14) removed.</li> </ul>				
74HC_HCT11 v.7	20210108	Product data sheet	-	74HC_HCT11 v.6	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number 74HCT11DB (SOT337-1/SSOP14) removed.</li> <li>Section 7: Derating values for P<sub>tot</sub> total power dissipation have been updated.</li> </ul>				
74HC_HCT11 v.6	20151119	Product data sheet	-	74HC_HCT11 v.5	
Modifications:	Type numbers 74HC11N and 74HCT11N (SOT27-1) removed.				
74HC_HCT11 v.5	20111216	Product data sheet	-	74HC_HCT11 v.4	
Modifications:	Legal pages updated.				
74HC_HCT11 v.4	20100325	Product data sheet	-	74HC_HCT11 v.3	
74HC_HCT11 v.3	20100209	Product data sheet	-	74HC_HCT11_CNV v.2	
74HC_HCT11_CNV v.2	19970827	Product specification	-	-	

#### **Triple 3-input AND gate**

### 14. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

#### Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <u>http://www.nexperia.com/profile/terms</u>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### **Trademarks**

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

**Product data sheet** 

All information provided in this document is subject to legal disclaimers.

10 / 11

# Contents

1. General description	1
2. Features and benefits	
3. Ordering information	
4. Functional diagram	
5. Pinning information	
5.1. Pinning	2
5.2. Pin description	
6. Functional description	3
7. Limiting values	
8. Recommended operating conditions	3
9. Static characteristics	
10. Dynamic characteristics	5
10.1. Waveforms and test circuit	6
11. Package outline	7
12. Abbreviations	9
13. Revision history	9
14. Legal information	10

© Nexperia B.V. 2024. All rights reserved

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 21 February 2024

74HC\_HCT11



# **OUR CERTIFICATE**

DiGi provide top-quality products and perfect service for customer worldwide through standardization, technological innovation and continuous improvement. DiGi through third-party certification, we striciy control the quality of products and services. Welcome your RFQ to Email: Info@DiGi-Electronics.com

DCI	DCI		
QUALITY MANAGEMENT SYSTEM CERTIFICATE	ENVIRONMENTAL MANAGEMENT SYSTEM CERTIFICATE	OCCUPATIONAL HEALTH & SAFETY MANAGEMENT SYSTEM CERTIFICATE	の可能可能可能 CERTIFICATE OF INCORPORATION
DIGI ELECTRONICS HK LIMITED	DIGI ELECTRONICS HK LIMITED	DIGI ELECTRONICS HK LIMITED	A. A. B. A. B. W. Hanniby and By that
RATINGS SHE IN HIS COMMERCIAL EXTREMENTAL AND STREET, MONGHO	PLATENTS 207, HO HOR COMMITTEE CALLES HAVE VER CHEET, MONORO	FLATENUE 207, HO HOUS COMPETENCE OF THE 2 MAYA VIEW STREET, MONGAO	DELERATIONCE INCLAMPSO 网络電子性者作用公司
GB/T 19001-2016 ktt ISO9001:2015	GB/T 24001-2016 idt ISO14001:2015	GB/T45001-2020 idt ISO45001:2018	$0 \rightarrow 0$ B, B $\rightarrow 0$ A, H B 122 B $\subset \odot$ G $\rightarrow H >$ 11 DN: Any Incorporated In Namy Early under the Comparise Ordinaria $A \rightarrow 0$ , $A \rightarrow A \rightarrow B$ , $A \rightarrow A \rightarrow C \rightarrow C + C \rightarrow N$ (Theoret T22 D for Larms of Hong Kong, and Balling Compare is
Ref Ref Participation components	Retto nagagante	For the Index of all interviews	Constant with in the Last in Yang Wong, and the lost dompany is it is a lost a limited company.
tankan more min men more more men men men men men men men men men me	tomantener men photosener men metalementener meneration Manalit	torinamientes 2008 Inter land can Can 2008-000-00 Jacobierto Maria Maria	★ # 4 # 0 ± 0 − Λ + − Λ ± + ± + # ± − Name# 04. 12 heavy 200.
			Oldentrikalis or in edit a kale or Min. Au L. L. DERING Programmer Auguston Mingrade Specific Automatistrature Auguston
In the second se	The second secon	Control tests of a state of the state o	In Heps: 公司各場合公司中局工作用:工作品中提供学校公司名表式市场大型公司者包括基本中 工程品名提用: TableAdd #: TableAdd #: TableAdd #: TableAdd #: TableA





Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.