

# **PMBT4403YSX** Datasheet

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PMI	DiGi Electronics Part Number
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TRA	Description
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PMBT4403YSX-DG

NXP USA Inc.

PMBT4403YSX

TRANS PNP 40V 0.6A SOT23

Bipolar (BJT) Transistor PNP 40 V 600 mA 200MHz 2 50 mW Surface Mount SOT-23

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### Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PMBT4403YSX	NXP USA Inc.
Series:	Product Status:
-	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	600 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
40 V	750mV @ 50mA, 500mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
50nA (ICBO)	100 @ 10mA, 1V
Power - Max:	Frequency - Transition:
250 mW	200MHz
Operating Temperature:	Grade:
150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
TO-236-3, SC-59, SOT-23-3	SOT-23

### **Environmental & Export classification**

 ECCN:
 HTSUS:

 EAR99
 8541.21.0075



40V, 600 mA double PNP switching transistor 2 July 2015

Product data sheet

### 1. General description

Double PNP switching transistor in a very small SOT363 (TSSOP6) Surface-Mounted Device (SMD) plastic package.

Double NPN complement: PMBT4401YS

### 2. Features and benefits

- Double general-purpose switching transistor
- AEC-Q101 qualified

### 3. Applications

Switching and linear amplification

### 4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per transistor						
h <sub>FE</sub>	DC current gain	$V_{CE} = -2 \text{ V}; \text{ I}_{C} = -150 \text{ mA};  \text{t}_{p} \le 300  \mu\text{s};$ $\delta \le 0.02; \text{ T}_{amb} = 25 \text{ °C}$	100	-	300	
Per transistor						
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-40	V
I <sub>C</sub>	collector current		-	-	-600	mA



40V, 600 mA double PNP switching transistor

### 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter TR1	6 5 4	6 5 4
2	В	base TR1		
3	С	collector TR2		$\left( \begin{array}{c} TR1 \end{array} \right)$
4	E	emitter TR2		
5	В	base TR2	TSSOP6 (SOT363)	1 2 3
6	С	collector TR1	-	sym018

### 6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PMBT4403YS	TSSOP6	plastic surface-mounted package; 6 leads	SOT363

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PMBT4403YS	BJ%

[1] % = placeholder for manufacturing site code

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### 8. Limiting values

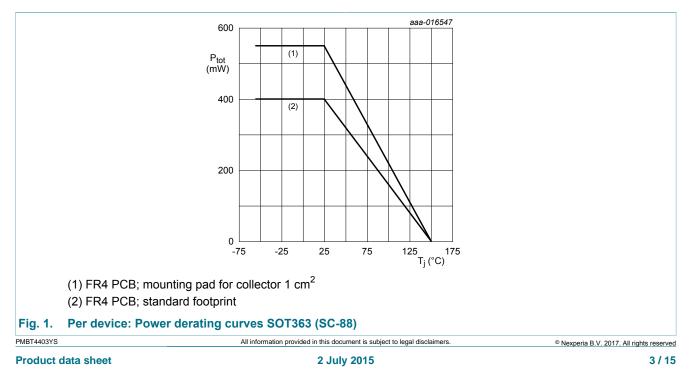
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transis	tor					
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-40	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-40	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-600	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-800	mA
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
			[2]	-	300	mW
Per device			,			-
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	400	mW
			[2]	-	550	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>

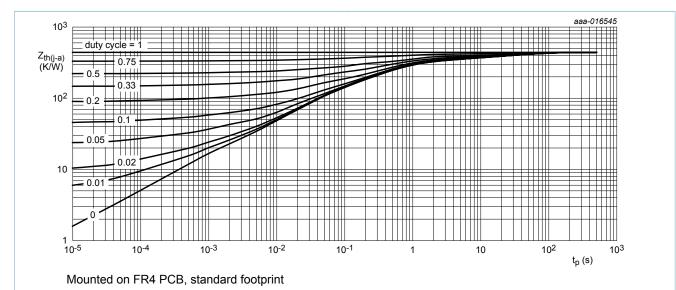


40V, 600 mA double PNP switching transistor

### 9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transistor							
R <sub>th(j-a)</sub> thermal resistance		in free air	[1]	-	-	500	K/W
	from junction to ambient		[2]	-	-	417	K/W
Per device	,						
R <sub>th(j-a)</sub>	thermal resistance	in free air	[1]	-	-	313	K/W
	from junction to ambient		[2]	-	-	227	K/W

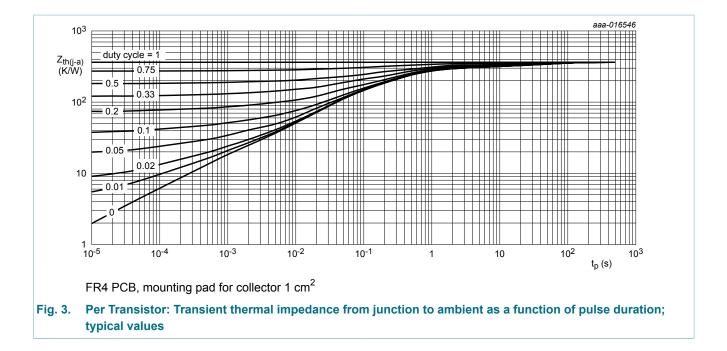
## Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>



## Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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#### 40V, 600 mA double PNP switching transistor



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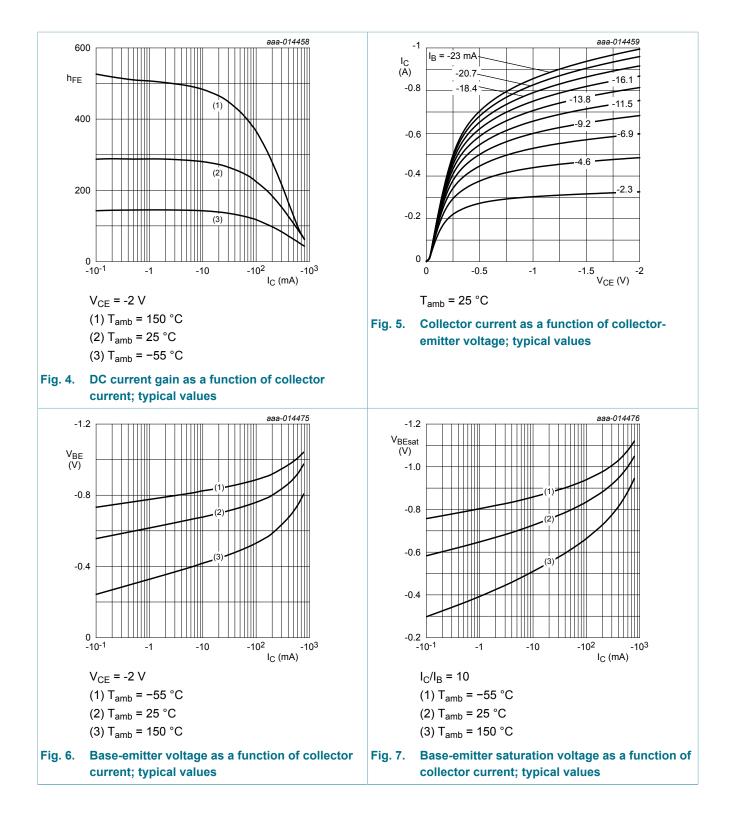
40V, 600 mA double PNP switching transistor

### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transis	tor	· · · · · · · · · · · · · · · · · · ·	I			
I <sub>CBO</sub>	collector-base cut-off	$V_{CB}$ = -40 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-50	nA
	current	$V_{CB}$ = -40 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 125 °C	-	-	-10	μA
I <sub>EBO</sub>	emitter-base cut-off current			-	-50	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; I <sub>C</sub> = -0.1 mA; T <sub>amb</sub> = 25 °C	30	-	-	
		$V_{CE}$ = -1 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	60	-	-	
		$V_{CE}$ = -1 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	100	-	-	
		$\label{eq:Vce} \begin{split} V_{CE} &= -2 \text{ V; } \text{ I}_{C} = -150 \text{ mA; } \text{t}_{p} \leq 300  \mu\text{s;} \\ \delta \leq 0.02 \text{; } \text{ T}_{amb} = 25 ^{\circ}\text{C} \end{split}$	100	-	300	
		$\begin{split} V_{CE} &= -2 \ V; \ I_C = -500 \ mA; \ t_p \leq 300 \ \mu s; \\ \delta &\leq 0.02; \ T_{amb} = 25 \ ^\circ C \end{split}$	20	-	-	
OLOUI	collector-emitter saturation voltage	$I_{C}$ = -150 mA; $I_{B}$ = -15 mA; $t_{p}$ ≤ 300 µs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	-400	mV
		$I_{C}$ = -500 mA; $I_{B}$ = -50 mA; $t_{p}$ ≤ 300 µs; $\delta$ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	-750	mV
V <sub>BEsat</sub> base-emitter satura voltage	base-emitter saturation voltage	$I_{C}$ = -150 mA; $I_{B}$ = -15 mA; $t_{p}$ ≤ 300 µs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	-950	mV
		$\begin{split} I_{C} &= -500 \text{ mA; } I_{B} = -50 \text{ mA; } t_{p} \leq 300  \mu\text{s;} \\ \delta \leq 0.02;  T_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	-	-1.3	V
t <sub>d</sub>	delay time	I <sub>C</sub> = -150 mA; I <sub>Bon</sub> = -15 mA;	-	-	15	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = 15 mA; T <sub>amb</sub> = 25 °C	-	-	30	ns
t <sub>on</sub>	turn-on time	-	-	-	40	ns
t <sub>s</sub>	storage time	-	-	-	300	ns
t <sub>f</sub>	fall time	-	-	-	50	ns
t <sub>off</sub>	turn-off time		-	-	350	ns
C <sub>C</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	8.5	pF
C <sub>E</sub>	emitter capacitance	$V_{EB}$ = -500 mV; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	35	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -10 V; I <sub>C</sub> = -20 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	200	-	-	MHz

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#### 40V, 600 mA double PNP switching transistor



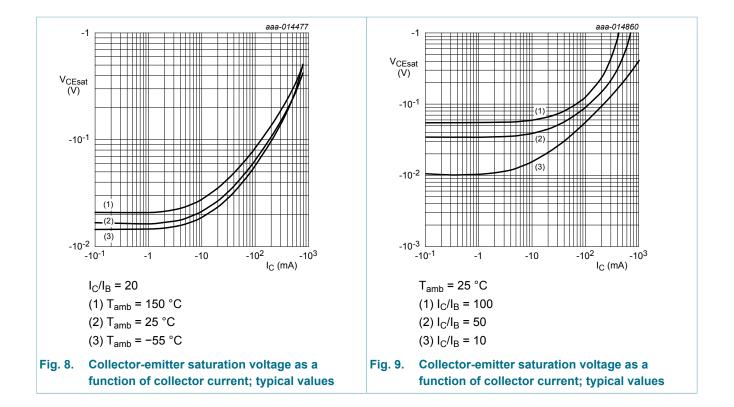
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#### 40V, 600 mA double PNP switching transistor



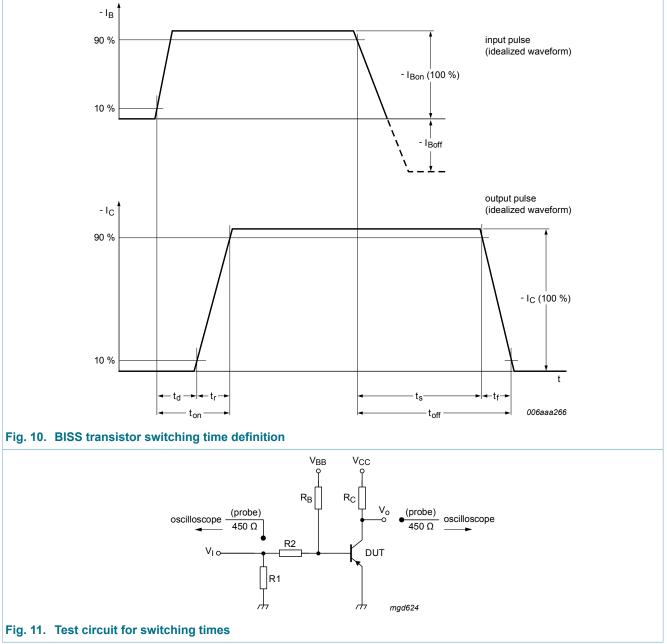
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#### 40V, 600 mA double PNP switching transistor

# 11. Test information



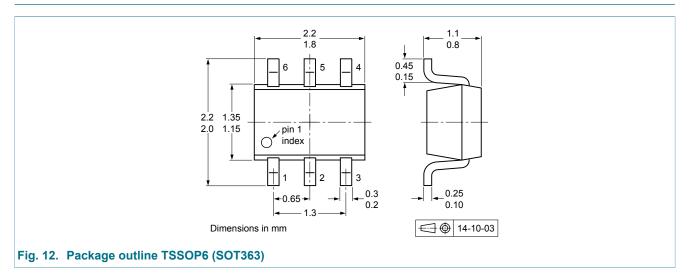
#### **11.1 Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

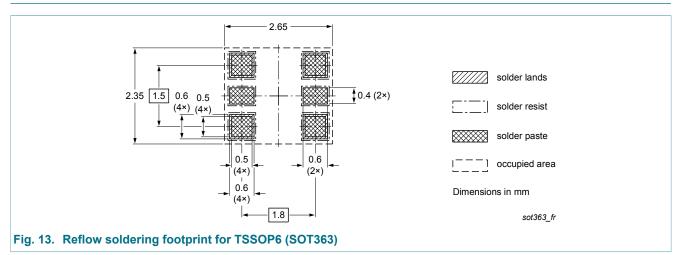
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### 12. Package outline

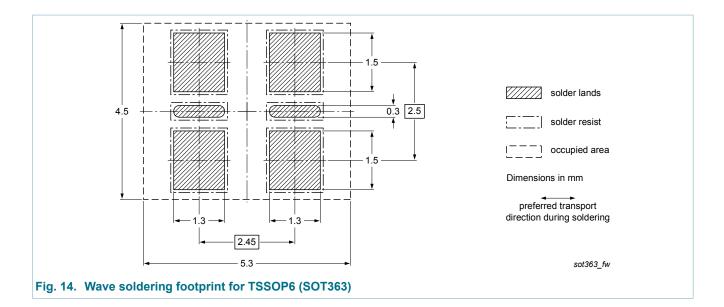


### 13. Soldering



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#### 40V, 600 mA double PNP switching transistor



40V, 600 mA double PNP switching transistor

### 14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT4403YS v.1	20150702	Product data sheet	-	-

#### 40V, 600 mA double PNP switching transistor

#### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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### PMBT4403YS

#### 40V, 600 mA double PNP switching transistor

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