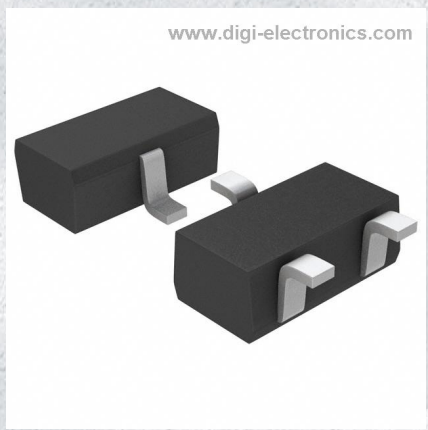
2SD262100L Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number	2SD262100L-DG
Manufacturer	Panasonic Electronic Components
Manufacturer Product Number	2SD262100L
Description	TRANS NPN 100V 0.02A SSSMINI3
Detailed Description	Bipolar (BJT) Transistor NPN 100 V 20 mA 200MHz 100 mW Surface Mount SSSMini3-F1

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
2SD262100L	Panasonic Electronic Components
Series:	Product Status:
-	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
NPN	20 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ Ib, Ic:
100 V	200mV @ 1mA, 10mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
1µA	400 @ 2mA, 10V
Power - Max:	Frequency - Transition:
100 mW	200MHz
Operating Temperature:	Mounting Type:
125°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
SOT-723	SSSMini3-F1
Base Product Number:	
2SD2621	

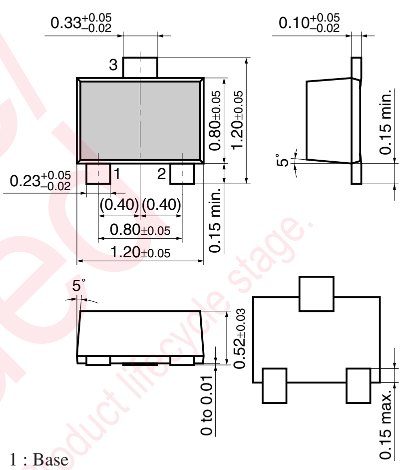
Environmental & Export classification

Moisture Sensitivity Level (MSL):	ECCN:
1 (Unlimited)	EAR99
HTSUS:	
8541.21.0075	

2SD2621

Silicon NPN epitaxial planar type

For low-frequency driver amplification



1 : Base
2 : Emitter
3 : Collector

SSSMini3-F1 Package

Marking Symbol: 3B

■ Features

- High forward current transfer ratio h_{FE}
- Low collector-emitter saturation voltage $V_{CE(sat)}$
- High emitter-base voltage (Collector open) V_{EBO}

■ Absolute Maximum Ratings $T_a = 25°C$

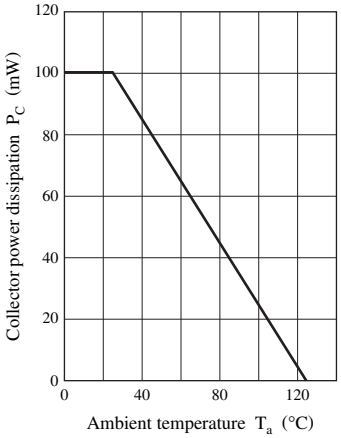
Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	100	V
Collector-emitter voltage (Base open)	V_{CEO}	100	V
Emitter-base voltage (Collector open)	V_{EBO}	15	V
Collector current	I_C	20	mA
Peak collector current	I_{CP}	50	mA
Collector power dissipation	P_C	100	mW
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	−55 to +125	°C

■ Electrical Characteristics $T_a = 25°C \pm 3°C$

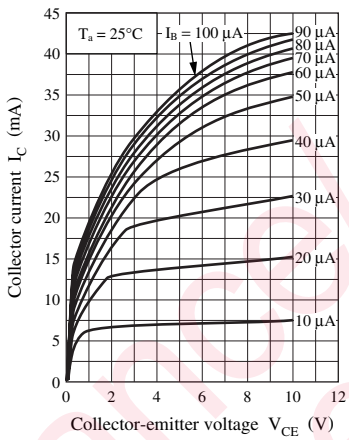
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 10\ \mu A$, $I_E = 0$	100			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = 1$ mA, $I_B = 0$	100			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10\ \mu A$, $I_C = 0$	15			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 60$ V, $I_E = 0$			0.1	μA
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = 60$ V, $I_B = 0$			1.0	μA
Forward current transfer ratio	h_{FE}	$V_{CE} = 10$ V, $I_C = 2$ mA	400		1 200	—
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 10$ mA, $I_B = 1$ mA		0.05	0.20	V
Transition frequency	f_T	$V_{CB} = 10$ V, $I_E = -2$ mA, f = 200 MHz		200		MHz
Noise voltage	NV	$V_{CE} = 10$ V, $I_C = 1$ mA, $G_V = 80$ dB $R_g = 100$ kΩ, Function = FLAT		80		mV

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

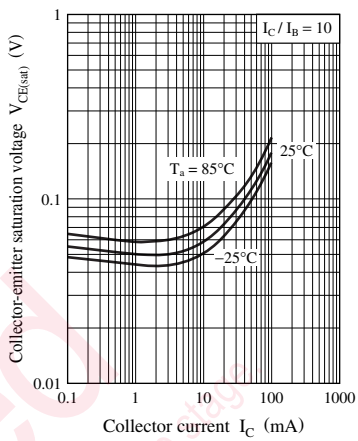
P_C — T_a



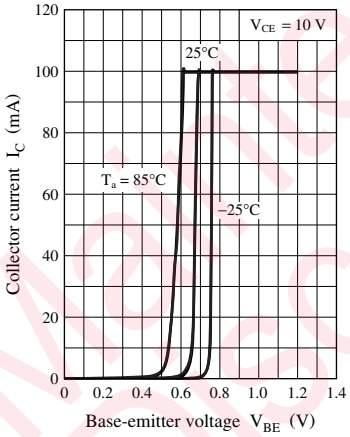
I_C — V_{CE}



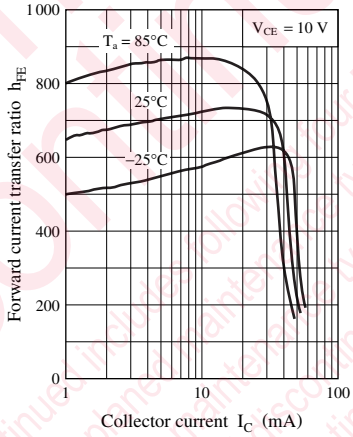
$V_{CE(sat)}$ — I_C



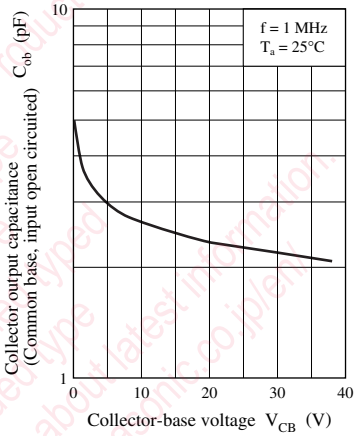
I_C — V_{BE}



h_{FE} — I_C



C_{ob} — V_{CB}



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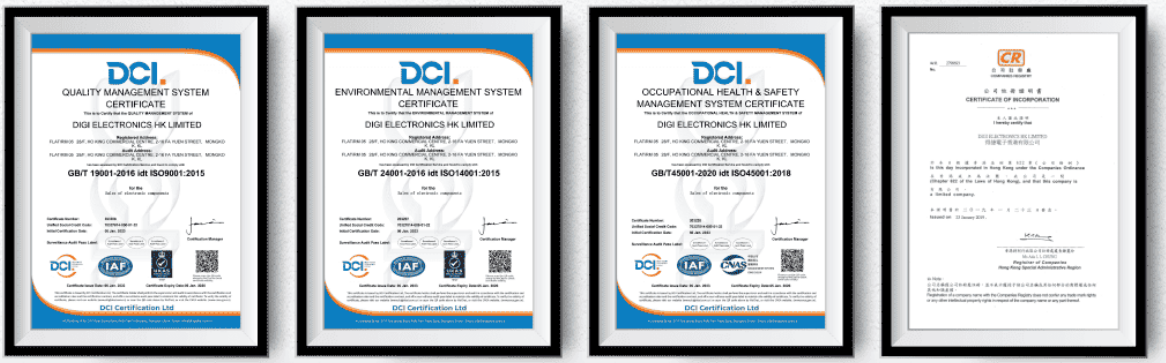

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