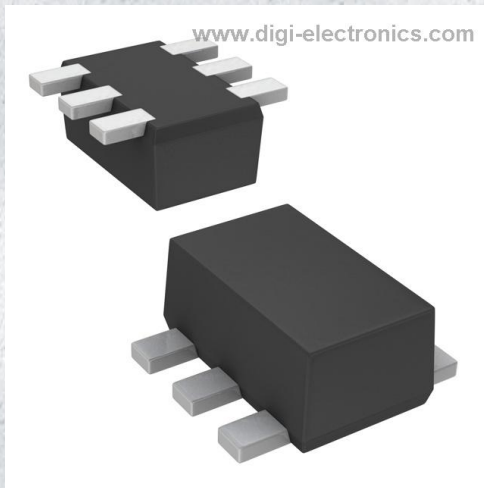


UP04214G0L Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	UP04214G0L-DG
Manufacturer	Panasonic Electronic Components
Manufacturer Product Number	UP04214G0L
Description	TRANS PREBIAS DUAL NPN SSMINI6
Detailed Description	Pre-Biased Bipolar Transistor (BJT) 2 NPN - Pre-Biased (Dual) 50V 100mA 150MHz 125mW Surface Mount SSMINI6-F2



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Purchase and inquiry

Manufacturer Product Number:

UP04214G0L

Series:

-

Transistor Type:

2 NPN - Pre-Biased (Dual)

Voltage - Collector Emitter Breakdown (Max):

50V

Resistor - Emitter Base (R2):

47kOhms

Vce Saturation (Max) @ Ib, Ic:

250mV @ 300μA, 10mA

Frequency - Transition:

150MHz

Mounting Type:

Surface Mount

Supplier Device Package:

SSMini6-F2

Manufacturer:

Panasonic Electronic Components

Product Status:

Obsolete

Current - Collector (Ic) (Max):

100mA

Resistor - Base (R1):

10kOhms

DC Current Gain (hFE) (Min) @ Ic, Vce:

80 @ 5mA, 10V

Current - Collector Cutoff (Max):

500nA

Power - Max:

125mW

Package / Case:

SOT-563, SOT-666

Base Product Number:

UP0421

Environmental & Export classification

Moisture Sensitivity Level (MSL):

1 (Unlimited)

HTSUS:

8541.21.0075

ECCN:

EAR99

UP04214G

Silicon NPN epitaxial planar type

For switching/digital circuits

■ Features

- Two elements incorporated into one package (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half

■ Basic Part Number

- UNR2210 × 2

■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	50	V
Collector-emitter voltage (Base open)	V_{CEO}	50	V
Collector current	I_{C}	100	mA
Total power dissipation	P_{T}	125	mW
Junction temperature	T_{j}	125	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +125	$^\circ\text{C}$

■ Package

- Code

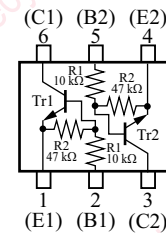
SSMini6-F2

- Pin Name

1: Emitter (Tr1)	4: Emitter (Tr2)
2: Base (Tr1)	5: Base (Tr2)
3: Collector (Tr2)	6: Collector (Tr1)

■ Marking Symbol: BR

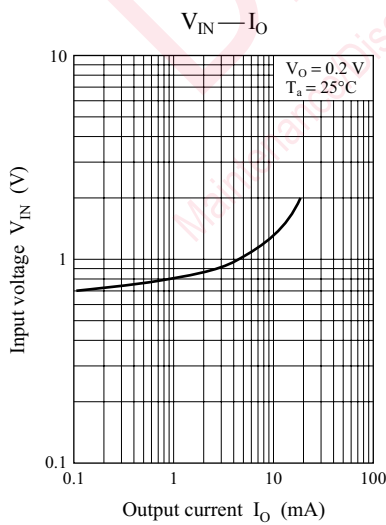
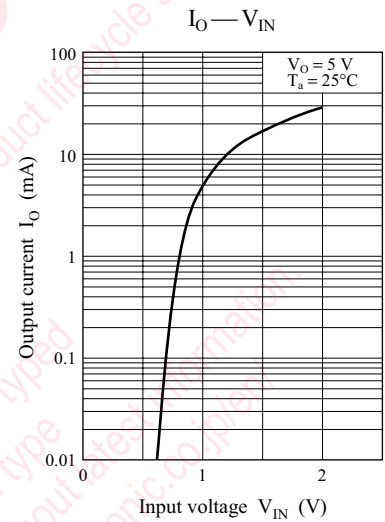
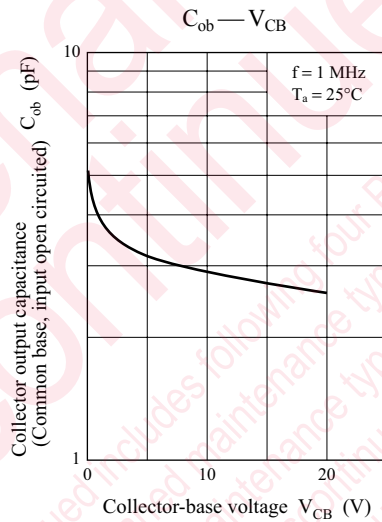
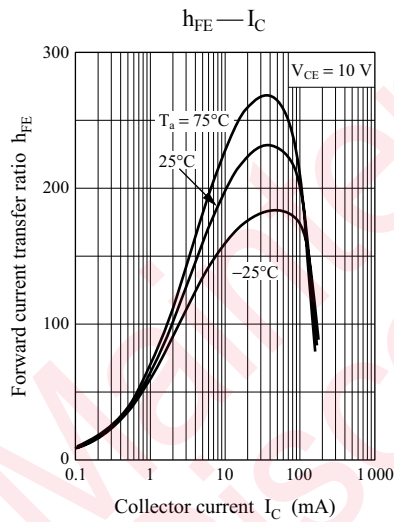
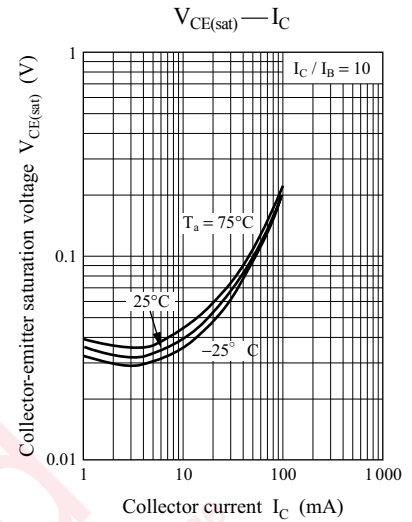
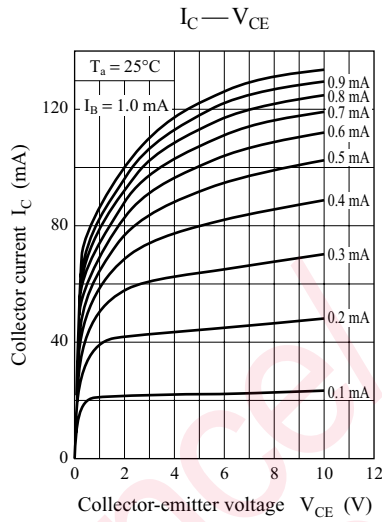
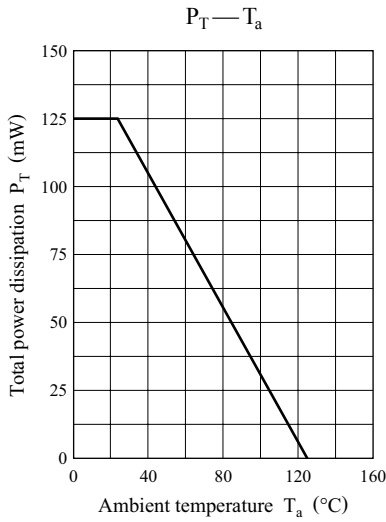
■ Internal Connection



■ Electrical Characteristics $T_a = 25^\circ\text{C} \pm 3^\circ\text{C}$

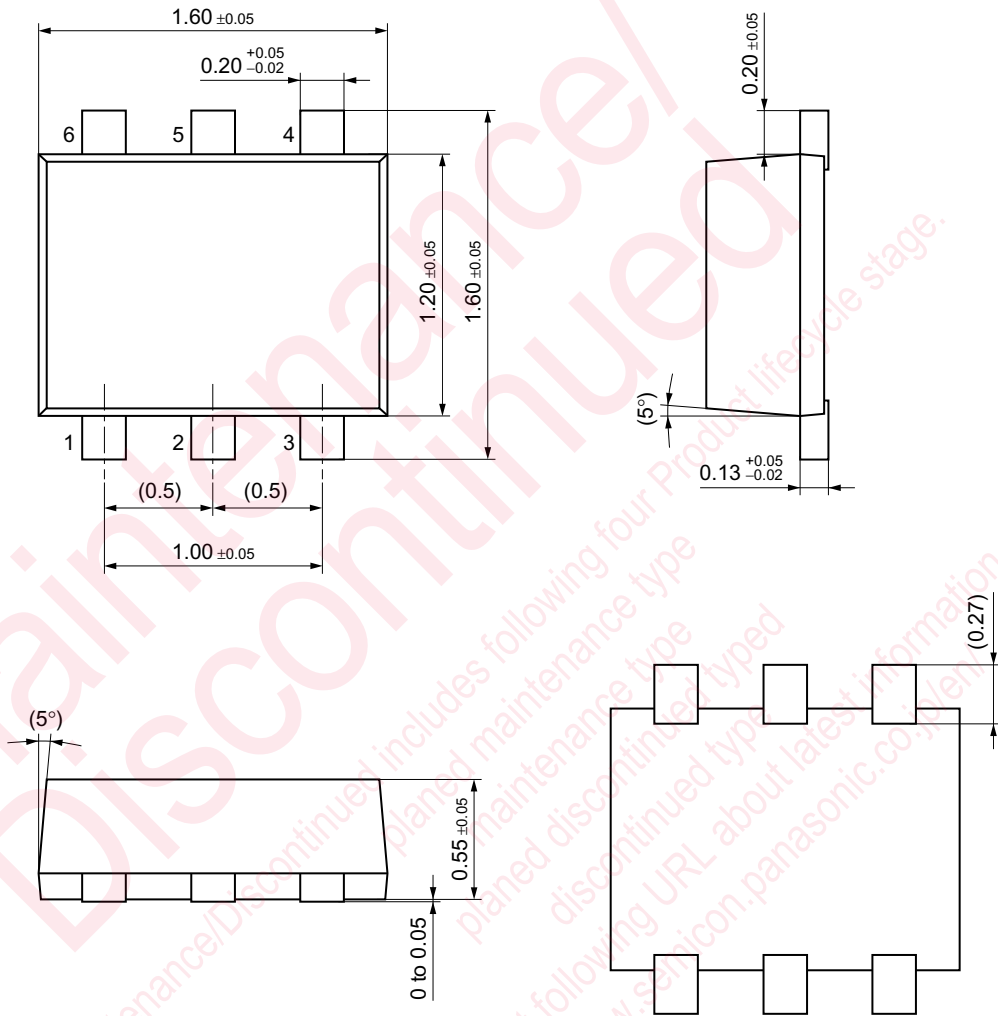
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_{\text{C}} = 10 \mu\text{A}, I_{\text{E}} = 0$	50			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_{\text{C}} = 2 \text{ mA}, I_{\text{B}} = 0$	50			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{\text{CB}} = 50 \text{ V}, I_{\text{E}} = 0$			0.1	μA
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{\text{CE}} = 50 \text{ V}, I_{\text{B}} = 0$			0.5	μA
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{\text{EB}} = 6 \text{ V}, I_{\text{C}} = 0$			0.2	mA
Forward current transfer ratio	h_{FE}	$V_{\text{CE}} = 10 \text{ V}, I_{\text{C}} = 5 \text{ mA}$	80			—
Collector-emitter saturation voltage	$V_{\text{CE(sat)}}$	$I_{\text{C}} = 10 \text{ mA}, I_{\text{B}} = 0.3 \text{ mA}$			0.25	V
Output voltage high-level	V_{OH}	$V_{\text{CC}} = 5 \text{ V}, V_{\text{B}} = 0.5 \text{ V}, R_{\text{L}} = 1 \text{ k}\Omega$	4.9			V
Output voltage low-level	V_{OL}	$V_{\text{CC}} = 5 \text{ V}, V_{\text{B}} = 2.5 \text{ V}, R_{\text{L}} = 1 \text{ k}\Omega$			0.2	V
Input resistance	R_{I}		-30%	10	+30%	$\text{k}\Omega$
Resistance ratio	$R_{\text{I}} / R_{\text{2}}$		0.17	0.21	0.25	—
Transition frequency	f_{T}	$V_{\text{CB}} = 10 \text{ V}, I_{\text{E}} = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.



SSMini6-F2

Unit: mm



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