

ALVC2G04USE-E Datasheet



DiGi Electronics Part Number Manufacturer Manufacturer Product Number Description Detailed Description ALVC2G04USE-E-DG Renesas Electronics Corporation ALVC2G04USE-E IC UNI-LOGIC GATE BUFFER SMD Element Bit per Element Output

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Manufacturer Product Number: ALVC2G04USE-E Series:

Base Product Number: ALVC2G04 Manufacturer: Renesas Electronics Corporation Product Status: Obsolete

Environmental & Export classification

RoHS Status:	
ROHS3 Compliant	
ECCN:	
EAR99	

REACH Status:
REACH Unaffected
HTSUS:
8542.39.0001

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April 1st, 2010 Renesas Electronics Corporation

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Triple Inverter Buffers

REJ03D0161-0200Z (Previous ADE-205-631A (Z)) Rev.2.00 Dec.17.2003

Description

The HD74ALVC2G04 has triple inverters in an 8 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range : 1.2 to 3.6 V Operating temperature range: -40 to +85°C
- All inputs V_{IH} (Max.) = 3.6 V (@V_{CC} = 0 V to 3.6 V) All outputs V₀ (Max.) = 3.6 V (@V_{CC} = 0 V)
- Output current $\pm 2 \text{ mA} (@V_{CC} = 1.2 \text{ V})$

 $\pm 4 \text{ mA} (@V_{CC} = 1.4 \text{ V to } 1.6 \text{ V})$ $\pm 6 \text{ mA} (@V_{CC} = 1.65 \text{ V to } 1.95 \text{ V})$ $\pm 18 \text{ mA} (@V_{CC} = 2.3 \text{ V to } 2.7 \text{ V})$ $\pm 24 \text{ mA} (@V_{CC} = 3.0 \text{ V to } 3.6 \text{ V})$

Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74ALVC2G04USE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)



Outline and Article Indication



Function Table

Input	A	Output Y
Н		L
L		Н
H:	High level	

L: Low level



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V _{CC}	-0.5 to 4.6	V	
Input voltage range ^{*1}	VI	-0.5 to 4.6	V	
Output voltage range *1, 2	Vo	–0.5 to $V_{\text{CC}}\text{+}0.5$	V	Output : H or L
		-0.5 to 4.6	_	V _{CC} : OFF
Input clamp current	I _{IK}	-50	mA	V ₁ < 0
Output clamp current	Ι _{ΟΚ}	±50	mA	$V_{\rm O}$ < 0 or $V_{\rm O}$ > $V_{\rm CC}$
Continuous output current	lo	±50	mA	$V_{O} = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±100	mA	
Maximum power dissipation at Ta = 25° C (in still air) ^{*3}	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 4.6 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Item	Symbol	Min	Мах	Unit	Conditions
Supply voltage range	V _{CC}	1.2	3.6	V	
Input voltage range	VI	0	3.6	V	
Output voltage range	Vo	0	V _{CC}	V	
Output current	I _{OH}		-2	mA	V _{CC} = 1.2 V
			-4		V _{CC} = 1.4 V
			-6		V _{CC} = 1.65 V
			-18		V _{CC} = 2.3 V
			-24		V _{CC} = 3.0 V
	I _{OL}		2		V _{CC} = 1.2 V
			4		V _{CC} = 1.4 V
			6		V _{CC} = 1.65 V
			18		V _{CC} = 2.3 V
			24		V _{CC} = 3.0 V
Input transition rise or fall rate	Δt / Δv	0	20	ns / V	V_{CC} = 1.2 to 2.7 V
		0	10		V _{CC} = 3.3±0.3 V
Operating free-air temperature	Та	-40	85	°C	

Recommended Operating Conditions

Note: Unused or floating inputs must be held high or low.



Electrical Characteristics

$(Ta = -40 \text{ to } 85^{\circ}C)$

Item	Symbol	V_{cc} (V) *	Min	Тур	Max	Unit	Test conditions
Input voltage	VIH	1.2	V _{CC} ×0.75	_		V	
		1.4 to 1.6	V _{CC} ×0.7	_	_	_	
		1.65 to 1.95	V _{CC} ×0.7	_	_	-	
		2.3 to 2.7	1.7	_	_	_	
		3.0 to 3.6	2.0	_	_	-	
	V _{IL}	1.2	_	—	V _{CC} ×0.25	-	
		1.4 to 1.6		_	V _{CC} ×0.3	-	
		1.65 to 1.95	_	—	V _{CC} ×0.3	-	
		2.3 to 2.7	_	—	0.7	-	
		3.0 to 3.6	_	_	0.8	-	
Output voltage	V _{OH}	Min to Max	V _{CC} -0.2	—		V	I _{OH} = -100 μA
		1.2	0.9	—		-	$I_{OH} = -2 \text{ mA}$
		1.4	1.1	_	_	-	$I_{OH} = -4 \text{ mA}$
		1.65	1.2	_		-	I _{OH} = -6 mA
		2.3	1.7	_		-	I _{OH} = -18 mA
		3.0	2.2	—		-	I _{OH} = -24 mA
	V _{OL}	Min to Max	_	—	0.2	-	I _{OL} = 100 μA
		1.2		_	0.3	-	$I_{OL} = 2 \text{ mA}$
		1.4		_	0.3	-	$I_{OL} = 4 \text{ mA}$
		1.65	_	—	0.3	-	$I_{OL} = 6 \text{ mA}$
		2.3		_	0.55	-	I _{OL} = 18 mA
		3.0	_	—	0.55	-	I _{OL} = 24 mA
Input current	I _{IN}	3.6		_	±5	μΑ	V_{IN} = 3.6 V or GND
Quiescent supply current	I _{CC}	3.6		_	10	μA	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0		—	5	μA	V _{IN} or V _O = 0 to 3.6 V
Input capacitance	CIN	3.3		5.0		рF	$V_{IN} = V_{CC} \text{ or } GND$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



Switching Characteristics

 $(Ta = -40 \text{ to } 85^{\circ}C)$

 $V_{CC} = 1.2 V$

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	—	5.0	—	ns	C _L = 15 pF	A	Ŷ

 $V_{CC} = 1.5 \pm 0.1 \text{ V}$

ltem	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	2.0	_	7.0	ns	C _L = 15 pF	А	Ŷ

 $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	1.5	—	5.0	ns	C _L = 30 pF	А	Ŷ

 $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	1.0	—	3.5	ns	C _L = 30 pF	A	Ŷ

 $V_{CC} = 3.3 \pm 0.3 V$

Item	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	1.0	—	2.5	ns	C _L = 30 pF	А	Ÿ

Operating Characteristics

$(Ta = 25^{\circ}C)$

Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	CPD	1.5		10.0	_	pF	f = 10 MHz
capacitance		1.8		10.0	_	-	
		2.5		11.0	_	-	
		3.3		12.0		_	

Test Circuit



Waveforms



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Package Dimensions





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