SY10E151JY Datasheet

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DiGi Electronics Part Number	SY10E151JY-DG
Manufacturer	Microchip Technology
Manufacturer Product Number	SY10E151JY
Description	IC FF D-TYPE SNGL 6BIT 28PLCC
Detailed Description	Flip Flop 1 Element D-Type 6 Bit Positive Edge 28-LCC (J-Lead)

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
SY10E151JY	Microchip Technology
Series:	Product Status:
10E	Obsolete
Function:	Type:
Master Reset	D-Type
Output Type:	Number of Elements:
Complementary	1
Number of Bits per Element:	Clock Frequency:
6	1.4 GHz
Max Propagation Delay @ V, Max CL:	Trigger Type:
-	Positive Edge
Current - Output High, Low:	Voltage - Supply:
-	-4.2V ~ -5.46V
Current - Quiescent (Iq):	Operating Temperature:
78 mA	-40°C ~ 85°C (TA)
Mounting Type:	Supplier Device Package:
Surface Mount	28-PLCC (11.48x11.48)
Package / Case:	Base Product Number:
28-LCC (J-Lead)	10E151

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	2 (1 Year)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	

NOT RECOMMENDED FOR NEW DESIGNS

6-BIT D REGISTER

SY10E151
SY100E151

FEATURES

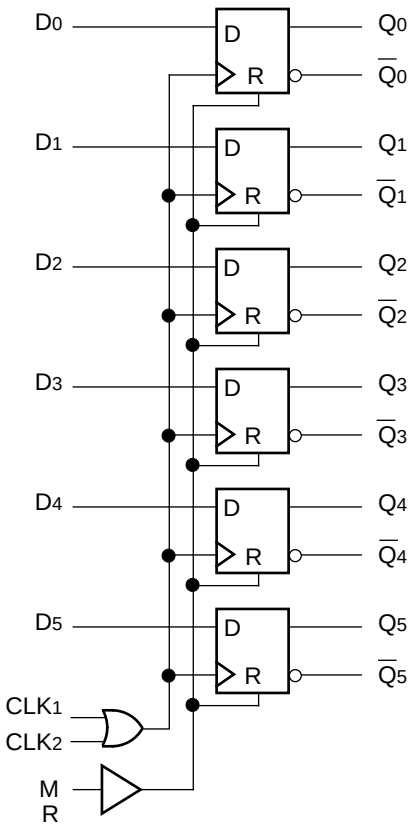
- **1100MHz toggle frequency**
- **Extended 100E VEE range of –4.2V to –5.46V**
- **Differential outputs**
- **Asynchronous Master Reset**
- **Dual clocks**
- **Fully compatible with industry standard 10KH, 100K ECL levels**
- **Internal 75KΩ input pulldown resistors**
- **Fully compatible with Motorola MC10E/100E151**
- **Available in 28-pin PLCC package**

DESCRIPTION

The SY10/100E151 offer 6 edge-triggered, high-speed, master-slave D-type flip-flops with differential outputs, designed for use in new, high-performance ECL systems. The two external clock signals (CLK_1, CLK_2) are gated through a logical OR operation before use as clocking control for the flip-flops. Data is clocked into the flip-flops on the rising edge of either CLK_1 or CLK_2 (or both). When both CLK_1 and CLK_2 are at a logic LOW, data enters the master and is transferred to the slave when either CLK_1 or CLK_2 (or both) go HIGH.

The MR (Master Reset) signal operates asynchronously to make all Q outputs go to a logic LOW.

BLOCK DIAGRAM

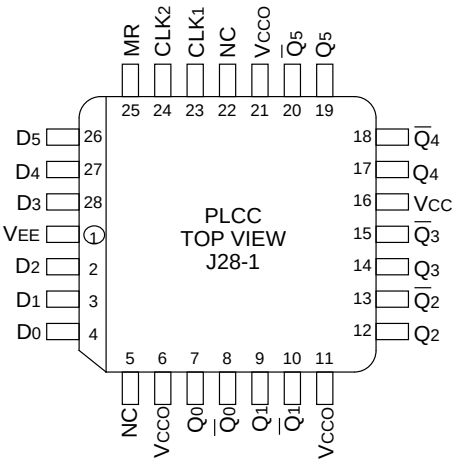


PIN NAMES

Pin	Function
D_0–D_5	Data Inputs
CLK_1, CLK_2	Clock Inputs
MR	Master Reset
Q_0–Q_5	True Outputs
$\overline{Q}_0$–$\overline{Q}_5$	Inverting Outputs
V_{CCO}	V_{CC} to Output

SY10E151JY Microchip Technology IC FF D-TYPE SNGL 6BIT 28PLCC

PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

Ordering Information[1]

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY10E151JI	J28-1	Industrial	SY10E151JI	Sn-Pb
SY10E151JITR[2]	J28-1	Industrial	SY10E151JI	Sn-Pb
SY10E151JC	J28-1	Commercial	SY10E151JC	Sn-Pb
SY10E151JCTR[2]	J28-1	Commercial	SY10E151JC	Sn-Pb
SY100E151JC	J28-1	Commercial	SY100E151JC	Sn-Pb
SY100E151JCTR[2]	J28-1	Commercial	SY100E151JC	Sn-Pb
SY10E151JY[3]	J28-1	Industrial	SY10E151JY with Pb-Free bar-line indicator	Matte-Sn
SY10E151JYTR[2, 3]	J28-1	Industrial	SY10E151JY with Pb-Free bar-line indicator	Matte-Sn
SY100E151JZ[3]	J28-1	Commercial	SY100E151JZ with Pb-Free bar-line indicator	Matte-Sn
SY100E151JZTR[2, 3]	J28-1	Commercial	SY100E151JZ with Pb-Free bar-line indicator	Matte-Sn

Notes:

1. Contact factory for die availability. Dice are guaranteed at T_A = 25°C, DC Electricals only.
2. Tape and Reel.
3. Pb-Free package is recommended for new designs.

SY10E151JY Microchip Technology IC FF D-TYPE SNGL 6BIT 28PLCC

TRUTH TABLES[1]

Asynchronous Operation

Inputs				Output
D_n	CLK_1	CLK_2	MR	$Q_n(t+1)$
X	X	X	H	L

Synchronous Operation

Inputs				Output
D_n	CLK_1	CLK_2	MR	$Q_n(t+1)$
L	u	L	L	L
H	u	L	L	H
L	L	u	L	L
H	L	u	L	H
X	H	u	L	$Q_n(t)$
X	u	H	L	$Q_n(t)$
X	L	L	L	$Q_n(t)$

Note:

1. H = HIGH Voltage Level
   L = LOW Voltage Level
   X = Don't Care
   t = Time before positive CLK transition
   t+1 = Time after positive CLK transition
   u = LOW-to-HIGH transition

DC ELECTRICAL CHARACTERISTICS[1]

V_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T_A = 0°C			T_A = +25°C			T_A = +85°C			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
I_{IH}	Input HIGH Current	—	—	150	—	—	150	—	—	150	µA	—
I_{EE}	Power Supply Current										mA	—
	10E	—	65	78	—	65	78	—	65	78		
	100E	—	65	78	—	65	78	—	75	90		

Note:

1. Specification for packaged product only.

AC ELECTRICAL CHARACTERISTICS[2]

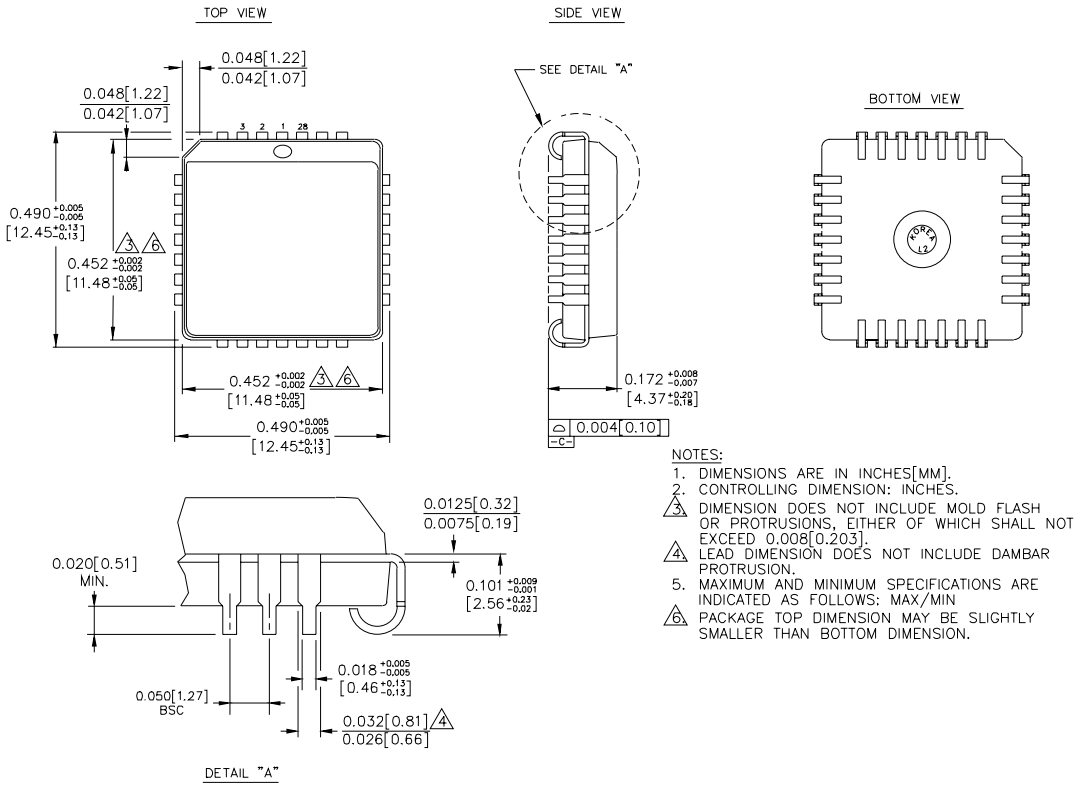
V_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T_A = 0°C			T_A = +25°C			T_A = +85°C			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
f_{MAX}	Max. Toggle Frequency	1100	1400	—	1100	1400	—	1100	1400	—	MHz	—
t_{PD}	Propagation Delay to Output										ps	—
	CLK	475	650	800	475	650	800	475	650	800		
	MR	475	650	850	475	650	850	475	650	850		
t_S	Set-up Time, D	0	–175	—	0	–175	—	0	–175	—	ps	—
t_H	Hold Time, D	350	175	—	350	175	—	350	175	—	ps	—
t_{RR}	Reset Recovery Time	750	550	—	750	550	—	750	550	—	ps	—
t_{PW}	Minimum Pulse Width CLK, MR	400	—	—	400	—	—	400	—	—	ps	—
t_{skew}	Within-Device Skew	—	65	—	—	65	—	—	65	—	ps	1
t_r t_f	Rise/Fall Time 20% to 80%	300	450	700	300	450	700	300	450	700	ps	—

Note:

1. Within-device skew is defined as identical transitions on similar paths through a device.
2. Specification for packaged product only.

28-PIN PLCC (J28-1)



Rev. 03

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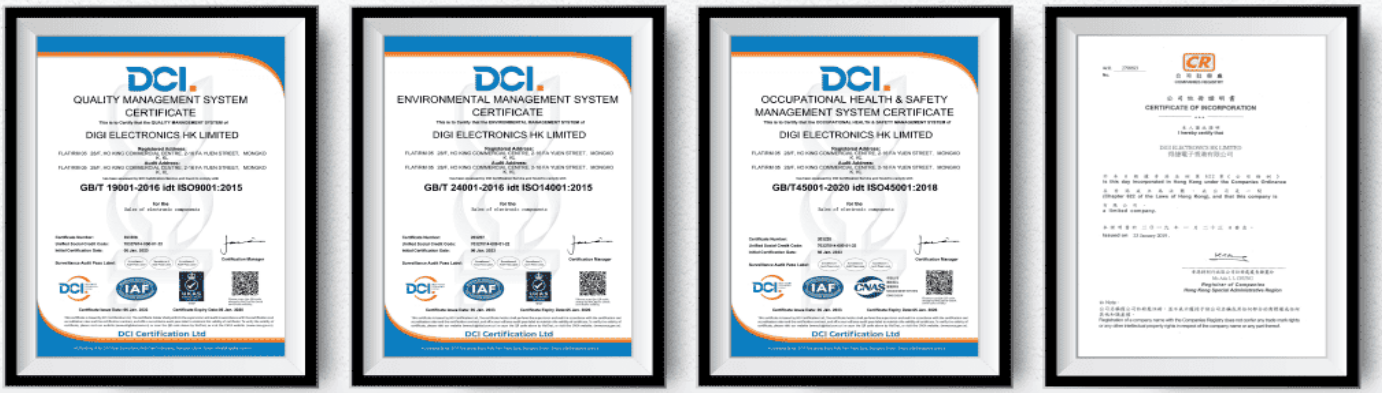

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