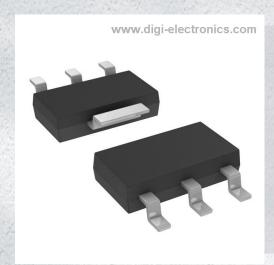


BCP69T1G Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number BCP69T1G-DG

Manufacturer onsemi

Manufacturer Product Number BCP69T1G

Description TRANS PNP 20V 1A SOT223

Detailed Description Bipolar (BJT) Transistor PNP 20 V 1 A 60MHz 1.5 W S

urface Mount SOT-223 (TO-261)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
BCP69T1G	onsemi
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	1 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
20 V	500mV @ 100mA, 1A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
10μA (ICBO)	85 @ 500mA, 1V
Power - Max:	Frequency - Transition:
1.5 W	60MHz
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
TO-261-4, TO-261AA	SOT-223 (TO-261)
Base Product Number:	
PCD60	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0075	



PNP Silicon Epitaxial Transistor

BCP69T1G, NSVBCP69T1G

This PNP Silicon Epitaxial Transistor is designed for use in low voltage, high current applications. The device is housed in the SOT-223 package, which is designed for medium power surface mount applications.

Features

- High Current: $I_C = -1.0 \text{ A}$
- The SOT-223 Package Can Be Soldered Using Wave or Reflow.
- SOT-223 package ensures level mounting, resulting in improved thermal conduction, and allows visual inspection of soldered joints.
 The formed leads absorb thermal stress during soldering, eliminating the possibility of damage to the die.
- NPN Complement is BCP68
- AEC-Q101 Qualified and PPAP Capable
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-20	Vdc
Collector-Base Voltage	V _{CBO}	-25	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current	Ic	-1.0	Adc
Total Power Dissipation @ T_A = 25°C (Note 1) Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-65 to 150	°C

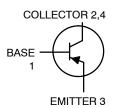
THERMAL CHARACTERISTICS

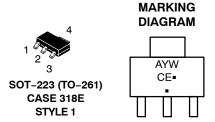
Characteristic	Symbol	Max	Unit
Thermal Resistance – Junction–to–Ambient (Surface Mounted)	$R_{\theta JA}$	83.3	°C/W
Lead Temperature for Soldering, 0.0625 in from case	TL	260	°C
Time in Solder Bath		10	s

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Device mounted on a glass epoxy printed circuit board 1.575 in. x 1.575 in. x 0.059 in.; mounting pad for the collector lead min. 0.93 sq. in.

MEDIUM POWER PNP SILICON HIGH CURRENT TRANSISTOR SURFACE MOUNT





CE = Specific Device Code A = Assembly Location

Y = Year W = Work Week • = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BCP69T1G	SOT-223 (Pb-Free)	1000 / Tape & Reel
NSVBCP69T1G	SOT-223 (Pb-Free)	1000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

BCP69T1G, NSVBCP69T1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristics	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage ($I_C = -100 \mu Adc, I_E = 0$)	V _{(BR)CES}	-25	-	-	Vdc
Collector–Emitter Breakdown Voltage ($I_C = -1.0 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	-20	=	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = -10 \mu Adc, I_C = 0$)	V _{(BR)EBO}	-5.0	=	-	Vdc
Collector-Base Cutoff Current (V _{CB} = -25 Vdc, I _E = 0)	I _{CBO}	-	=	-10	μAdc
Emitter-Base Cutoff Current (V _{EB} = -5.0 Vdc, I _C = 0)	I _{EBO}	-	-	-10	μAdc
ON CHARACTERISTICS					
DC Current Gain ($I_C = -5.0$ mAdc, $V_{CE} = -10$ Vdc) ($I_C = -500$ mAdc, $V_{CE} = -1.0$ Vdc) ($I_C = -1.0$ Adc, $V_{CE} = -1.0$ Vdc)	h _{FE}	50 85 60	- - -	- 375 -	-
Collector-Emitter Saturation Voltage (I _C = -1.0 Adc, I _B = -100 mAdc)	V _{CE(sat)}	-	-	-0.5	Vdc
Base–Emitter On Voltage (I _C = -1.0 Adc, V _{CE} = -1.0 Vdc)	V _{BE(on)}	-	-	-1.0	Vdc
DYNAMIC CHARACTERISTICS					
Current–Gain – Bandwidth Product ($I_C = -10 \text{ mAdc}$, $V_{CE} = -5.0 \text{ Vdc}$)	f _T	_	60	_	MHz

TYPICAL ELECTRICAL CHARACTERISTICS

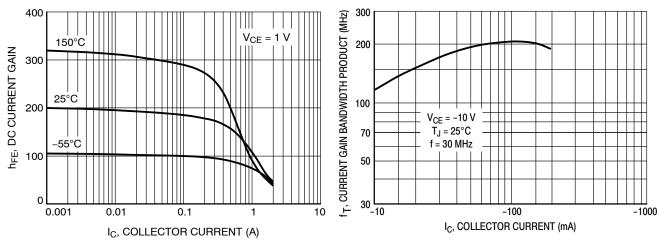


Figure 1. DC Current Gain

Figure 2. Current Gain Bandwidth Product

BCP69T1G, NSVBCP69T1G

TYPICAL ELECTRICAL CHARACTERISTICS

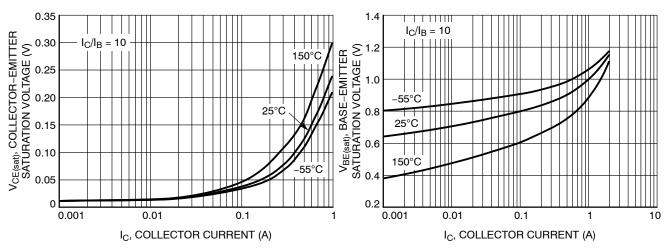


Figure 3. Collector Emitter Saturation Voltage vs. Collector Current

Figure 4. Base Emitter Saturation Voltage vs.
Collector Current

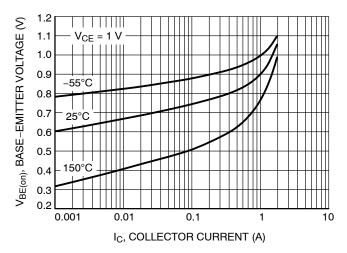


Figure 5. Base Emitter Voltage vs. Collector Current

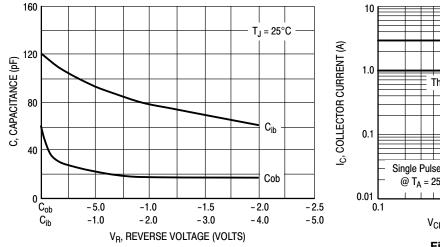


Figure 6. Capacitances

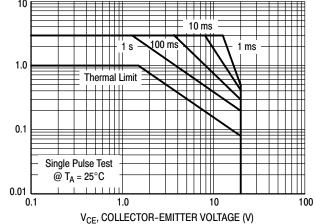


Figure 7. Safe Operating Area

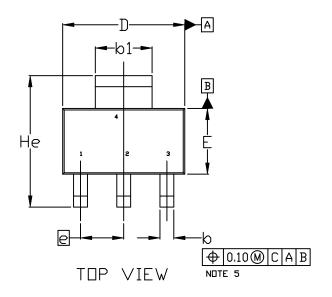


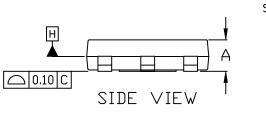
MECHANICAL CASE OUTLINE

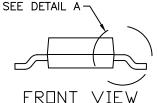
PACKAGE DIMENSIONS

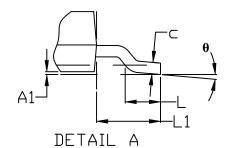


DATE 02 OCT 2018





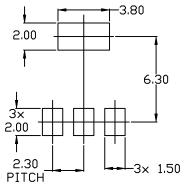




NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
- 4. DATUMS A AND B ARE DETERMINED AT DATUM H.
- A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
- POSITIONAL TOLERANCE APPLIES TO DIMENSIONS b AND b1.

	MILLIMETERS			
DIM	MIN.	N□M.	MAX.	
Α	1.50	1.63	1.75	
A1	0.02	0.06	0.10	
b	0.60	0.75	0.89	
b1	2.90	3.06	3.20	
c	0.24	0.29	0.35	
D	6.30	6.50	6.70	
E	3.30	3.50	3.70	
е		5'30 B2C	;	
L	0.20			
L1	1.50	1.75	2.00	
He	6.70	7.00	7.30	
θ	0°		10*	



RECOMMENDED	MOUNTING
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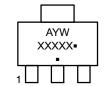
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SOT-223 (TO-261) CASE 318E-04 ISSUE R

DATE 02 OCT 2018

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. ANODE 2. CATHODE 3. NC 4. CATHODE	STYLE 3: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN	STYLE 4: PIN 1. SOURCE 2. DRAIN 3. GATE 4. DRAIN	STYLE 5: PIN 1. DRAIN 2. GATE 3. SOURCE 4. GATE
STYLE 6: PIN 1. RETURN 2. INPUT 3. OUTPUT 4. INPUT	STYLE 7: PIN 1. ANODE 1 2. CATHODE 3. ANODE 2 4. CATHODE	STYLE 8: CANCELLED	STYLE 9: PIN 1. INPUT 2. GROUND 3. LOGIC 4. GROUND	STYLE 10: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE
STYLE 11: PIN 1. MT 1 2. MT 2 3. GATE 4. MT 2	STYLE 12: PIN 1. INPUT 2. OUTPUT 3. NC 4. OUTPUT	STYLE 13: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR		

GENERIC MARKING DIAGRAM*



A = Assembly Location

Y = Year W = Work Week

XXXXX = Specific Device Code

= Pb-Free Package

(Note: Microdot may be in either location)
*This information is generic. Please refer to
device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "•", may
or may not be present. Some products may
not follow the Generic Marking.

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