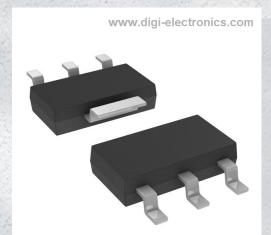


BSP16T1 Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number BSP16T1-DG

Manufacturer onsemi

Manufacturer Product Number BSP16T1

Description TRANS PNP 300V 0.1A SOT223

Detailed Description Bipolar (BJT) Transistor PNP 300 V 100 mA 15MHz 1

.5 W Surface Mount SOT-223 (TO-261)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
BSP16T1	onsemi
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
PNP	100 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
300 V	2V @ 5mA, 50mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
50μΑ	30 @ 50mA, 10V
Power - Max:	Frequency - Transition:
1.5 W	15MHz
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
TO-261-4, TO-261AA	SOT-223 (TO-261)
Base Product Number:	
RSD16	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
RoHS non-compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	



High Voltage Transistors

PNP Silicon

BSP16T1G

Features

 These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	-300	Vdc
Collector-Base Voltage	V_{CBO}	-350	Vdc
Emitter-Base Voltage	V_{EBO}	-6.0	Vdc
Collector Current	I _C	-100	mAdc
Total Device Dissipation @ T _A = 25°C (Note 1)	P _D	1.5	W
Storage Temperature Range	P _D	-65 to +150	°C
Junction Temperature	TJ	150	°C

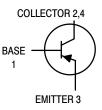
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	83.3	°C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Device mounted on a glass epoxy printed circuit board 1.575 in \times 1.575 in \times 0.059 in; mounting pad for the collector lead min. 0.93 sq. in.

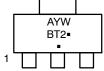
PNP SILICON HIGH VOLTAGE TRANSISTOR SURFACE MOUNT







SOT-223 CASE 318E STYLE 1



A = Assembly Location

Y = Year W = Work Week BT2 =Device Code ■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BSP16T1G	SOT-223 (Pb-Free)	1000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BSP16T1G

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (I _C = –50 mAdc, I _B = 0, L = 25 mH)	V _(BR) CEO	-300	-	Vdc
Collector – Base Breakdown Voltage ($I_C = -100 \mu Adc, I_E = 0$)	V _(BR) CBO	-300	-	Vdc
Collector-Emitter Cutoff Current (V _{CE} = -250 Vdc, I _B = 0)	I _{CES}	-	-50	μAdc
Collector-Base Cutoff Current (V _{CB} = -280 Vdc, I _E = 0)	I _{CBO}	-	-1.0	μAdc
Emitter–Base Cutoff Current $(V_{EB} = -6.0 \text{ Vdc}, I_C = 0)$	I _{EBO}	_	-20	μAdo
ON CHARACTERISTICS			-	•
DC Current Gain (V _{CE} = -10 Vdc, I _C = -50 mAdc)	h _{FE}	30	120	-
Collector-Emitter Saturation Voltage (I _C = -50 mAdc, I _B = -5.0 mAdc)	V _{CE(sat)}	-	-2.0	Vdc
DYNAMIC CHARACTERISTICS				
Current Gain – Bandwidth Product (V _{CE} = -10 Vdc, I _C = -10 mAdc, f = 30 MHz)	f _T	15	-	MHz
Collector–Base Capacitance (V _{CB} = -10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	_	15	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



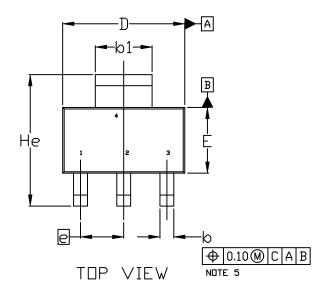
MECHANICAL CASE OUTLINE

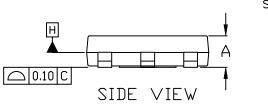
PACKAGE DIMENSIONS

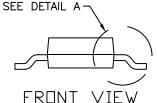


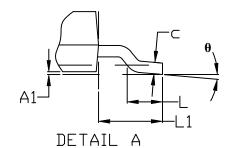
SOT-223 (TO-261) CASE 318E-04 ISSUE R

DATE 02 OCT 2018





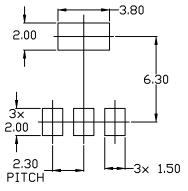




NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
- 4. DATUMS A AND B ARE DETERMINED AT DATUM H.
- A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
- POSITIONAL TOLERANCE APPLIES TO DIMENSIONS to AND to1.

	MILLIMETERS				
DIM	MIN. NDM. MAX.				
Α	1.50	1.63	1.75		
A1	0.02	0.06	0.10		
Ø	0.60	0.75	0.89		
b1	2.90	3.06	3.20		
U	0.24	0.29	0.35		
D	6.30	6.50	6.70		
E	3.30	3.50	3.70		
е		2,30 BSC	,		
L	0.20				
L1	1.50	1.75	2.00		
He	6.70	7.00	7.30		
θ	0*		10°		



RECOMMENDED	MOUNTING
FOOTPRINT	

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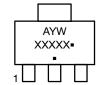
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SOT-223 (TO-261) CASE 318E-04 ISSUE R

DATE 02 OCT 2018

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. ANODE 2. CATHODE 3. NC 4. CATHODE	STYLE 3: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN	STYLE 4: PIN 1. SOURCE 2. DRAIN 3. GATE 4. DRAIN	STYLE 5: PIN 1. DRAIN 2. GATE 3. SOURCE 4. GATE
STYLE 6: PIN 1. RETURN 2. INPUT 3. OUTPUT 4. INPUT	STYLE 7: PIN 1. ANODE 1 2. CATHODE 3. ANODE 2 4. CATHODE	STYLE 8: CANCELLED	STYLE 9: PIN 1. INPUT 2. GROUND 3. LOGIC 4. GROUND	STYLE 10: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE
STYLE 11: PIN 1. MT 1 2. MT 2 3. GATE 4. MT 2	STYLE 12: PIN 1. INPUT 2. OUTPUT 3. NC 4. OUTPUT	STYLE 13: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR		

GENERIC MARKING DIAGRAM*



A = Assembly Location

Y = Year W = Work Week

XXXXX = Specific Device Code • Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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