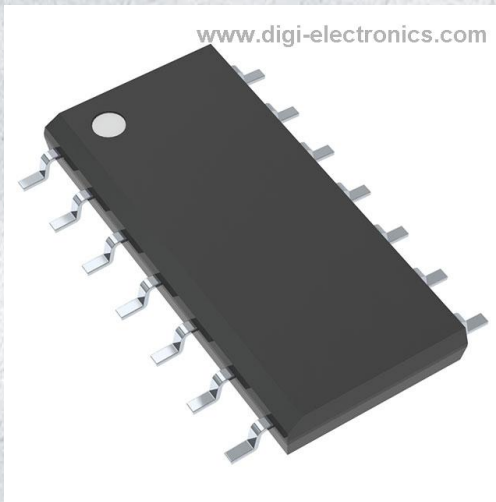


DM74AS30M Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	DM74AS30M-DG
Manufacturer	onsemi
Manufacturer Product Number	DM74AS30M
Description	IC GATE NAND 1CH 8-INP 14SOIC
Detailed Description	NAND Gate IC 1 Channel 14-SOIC



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RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

DM74AS30M

Series:

74AS

Logic Type:

NAND Gate

Number of Inputs:

8

Voltage - Supply:

4.5V ~ 5.5V

Input Logic Level - Low:

0.8V

Max Propagation Delay @ V, Max CL:

5ns @ 5V, 50pF

Mounting Type:

Surface Mount

Package / Case:

14-SOIC (0.154", 3.90mm Width)

Manufacturer:

onsemi

Product Status:

Obsolete

Number of Circuits:

1

Features:

-

Current - Output High, Low:

2mA, 20mA

Input Logic Level - High:

2V

Operating Temperature:

0°C ~ 70°C

Supplier Device Package:

14-SOIC

Base Product Number:

74AS30

Environmental & Export classification

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

REACH Status:

REACH Unaffected

HTSUS:

8542.39.0001



April 1984
Revised March 2000

DM74AS30 8 Input NAND Gate

General Description

This device contains a single gate which performs the logic NAND function.

Features

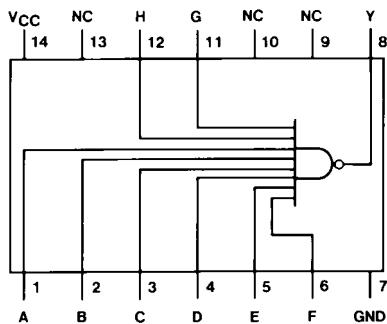
- Switching specifications at 50 pF
- Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky, low power Schottky, and advanced low power Schottky TTL counterpart
- Improved AC performance over Schottky, low power Schottky, and advanced low power Schottky counterparts

Ordering Code:

Order Number	Package Number	Package Description
DM74AS30M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74AS30N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

$$Y = \overline{ABCDEFGH}$$

Inputs	Output
A thru H	Y
All inputs H	L
One or More Inputs L	H

H = HIGH Logic Level
L = LOW Logic Level

DM74AS30 8 Input NAND Gate

DM74AS30

Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	0°C to +70°C
Storage Temperature Range	-65°C to +150°C
Typical θ_{JA}	
N Package	84.0°C/W
M Package	114.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V_{CC}	Supply Voltage	4.5	5	5.5	V
V_{IH}	HIGH Level Input Voltage	2			V
V_{IL}	LOW Level Input Voltage			0.8	V
I_{OH}	HIGH Level Output Current			-2	mA
I_{OL}	LOW Level Output Current			20	mA
T_A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^\circ C$.

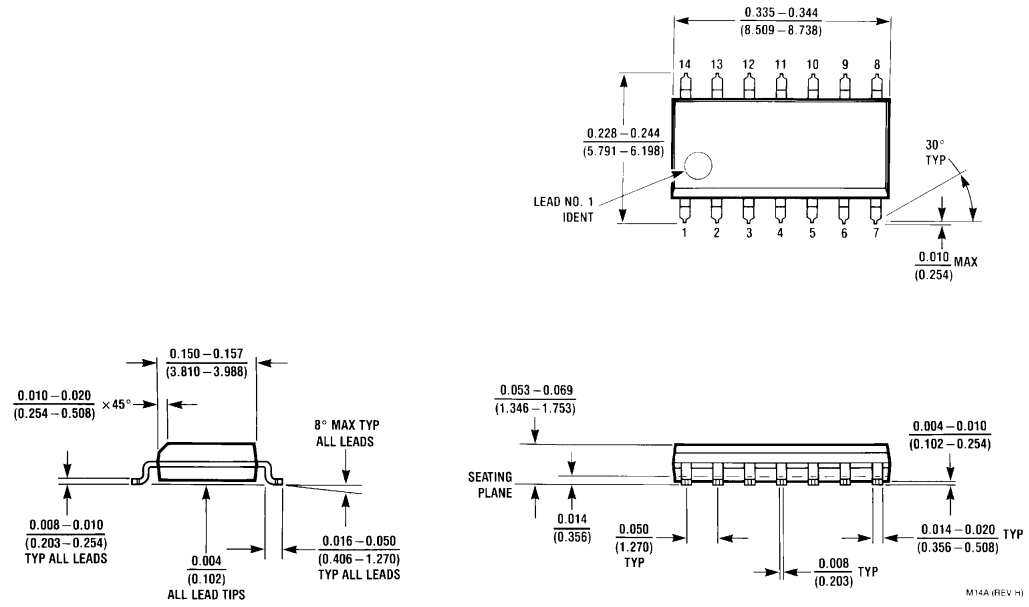
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{IK}	Input Clamp Voltage	$V_{CC} = 4.5V$, $I_I = -18\text{ mA}$			-1.2	V
V_{OH}	HIGH Level Output Voltage	$I_{OH} = -2\text{ mA}$ $V_{CC} = 4.5V\text{ to }5.5V$	$V_{CC} - 2$			V
V_{OL}	LOW Level Output Voltage	$V_{CC} = 4.5V$ $I_{OL} = 20\text{ mA}$		0.35	0.5	V
I_I	Input Current at Max Input Voltage	$V_{CC} = 5.5V$, $V_{IH} = 7V$			0.1	mA
I_{IH}	HIGH Level Input Current	$V_{CC} = 5.5V$, $V_{IH} = 2.7V$			20	μA
I_{IL}	LOW Level Input Current	$V_{CC} = 5.5V$, $V_{IL} = 0.4V$			-0.5	mA
I_O	Output Drive Current	$V_{CC} = 5.5V$, $V_O = 2.25V$	-30		-112	mA
I_{CC}	Supply Current	$V_{CC} = 5.5V$	Outputs HIGH	1	1.5	mA
			Outputs LOW	3.4	4.9	mA

Switching Characteristics

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t_{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	$V_{CC} = 4.5V\text{ to }5.5V$ $R_L = 500\Omega$	1	5	ns
t_{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	$C_L = 50\text{ pF}$	1	4.5	ns

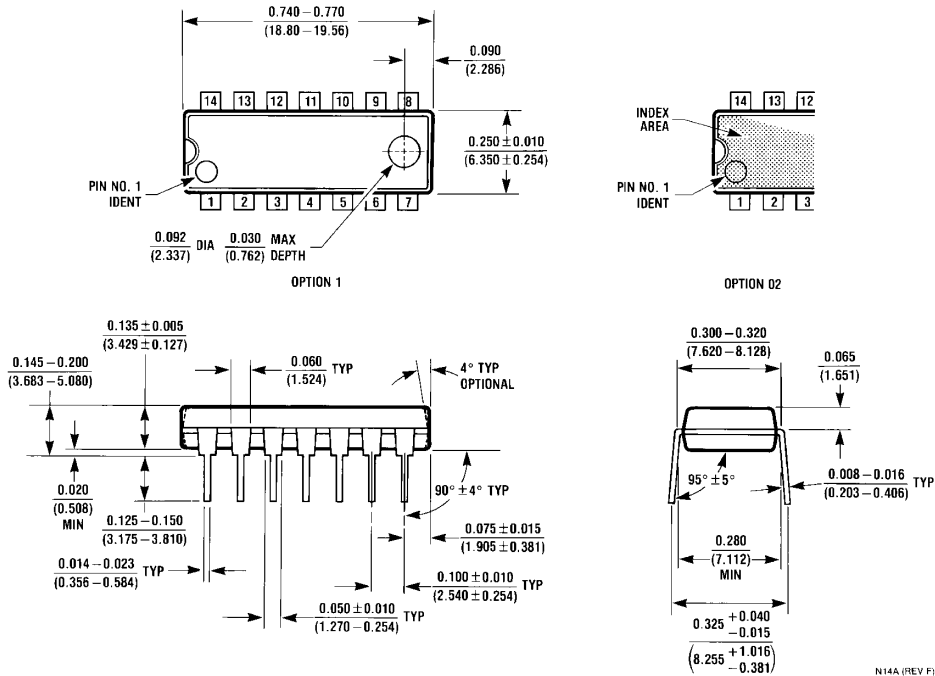
Physical Dimensions inches (millimeters) unless otherwise noted



**14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
Package Number M14A**

DM74AS30 8 Input NAND Gate

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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