

## **EMX1DXV6T1** Datasheet



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DiGi Electronics Part Number EMX1DXV6T1-DG

Manufacturer onsemi

Manufacturer Product Number EMX1DXV6T1

Description TRANS 2NPN 50V 0.1A SOT563

Detailed Description Bipolar (BJT) Transistor Array 2 NPN (Dual) 50V 100

mA 180MHz 500mW Surface Mount SOT-563



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## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
EMX1DXV6T1	onsemi
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
2 NPN (Dual)	100mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, Ic:
50V	400mV @ 5mA, 50mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
500nA (ICBO)	120 @ 1mA, 6V
Power - Max:	Frequency - Transition:
500mW	180MHz
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
SOT-563, SOT-666	SOT-563
Base Product Number:	
EMX1	

## **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
RoHS non-compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



# **Dual NPN General Purpose Amplifier Transistor**

## EMX1DXV6T1G, EMX1DXV6T5G

This NPN transistor is designed for general purpose amplifier applications. This device is housed in the SOT-563 package which is designed for low power surface mount applications, where board space is at a premium.

#### **Features**

- Reduces Board Space
- High h<sub>FE</sub>, 210-460 (Typical)
- Low V<sub>CE(sat)</sub>, < 0.5 V
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Base Voltage	V <sub>(BR)CBO</sub>	60	Vdc
Collector-Emitter Voltage	0 (511)020		Vdc
Emitter-Base Voltage			Vdc
Collector Current - Continuous	I <sub>C</sub>	100	mAdc

#### THERMAL CHARACTERISTICS

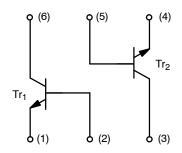
Characteristic (One Junction Heated)	Symbol	Max	Unit	
Total Device Dissipation  T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	357 (Note 1) 2.9 (Note 1)	mW mW/°C	
Thermal Resistance – Junction-to-Ambient	$R_{ heta JA}$	350 (Note 1)	°C/W	
Characteristic (Both Junctions Heated)	Symbol	Max	Unit	
Total Device Dissipation  T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	500 (Note 1) 4.0 (Note 1)	mW mW/°C	
	_	050 (NI-1-4)	°C ///	
Thermal Resistance – Junction-to-Ambient	$R_{ heta JA}$	250 (Note 1)	°C/W	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

1. FR-4 @ Minimum Pad

## DUAL NPN GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT





SOT-563 CASE 463A STYLE 1

#### **MARKING DIAGRAM**



3X = Specific Device Code

M = Month Code

■ = Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

#### EMX1DXV6T1G, EMX1DXV6T5G

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C)

Characteristic	Symbol	Min	Тур	Max	Unit
Collector-Base Breakdown Voltage ( $I_C = 50 \mu Adc, I_E = 0$ )	V <sub>(BR)CBO</sub>	60	-	-	Vdc
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	50	-	-	Vdc
Emitter-Base Breakdown Voltage ( $I_E = 50 \mu Adc, I_E = 0$ )	V <sub>(BR)EBO</sub>	7.0	-	-	Vdc
Collector-Base Cutoff Current $(V_{CB} = 60 \text{ Vdc}, I_E = 0)$	I <sub>CBO</sub>	-	-	0.5	μА
Emitter-Base Cutoff Current (V <sub>EB</sub> = 7.0 Vdc, I <sub>B</sub> = 0)	I <sub>EBO</sub>	-	-	0.5	μА
Collector-Emitter Saturation Voltage (Note 2) (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sat)</sub>	-	-	0.4	Vdc
DC Current Gain (Note 3) (V <sub>CE</sub> = 6.0 Vdc, I <sub>C</sub> = 1.0 mAdc)	h <sub>FE</sub>	120	-	560	-
Transition Frequency ( $V_{CE} = 12 \text{ Vdc}$ , $I_{C} = 2.0 \text{ mAdc}$ , $f = 30 \text{ MHz}$ )	fT	-	180	-	MHz
Output Capacitance (V <sub>CB</sub> = 12 Vdc, I <sub>C</sub> = 0 Adc, f = 1 MHz)	C <sub>OB</sub>	-	2.0	-	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.

#### **ORDERING INFORMATION**

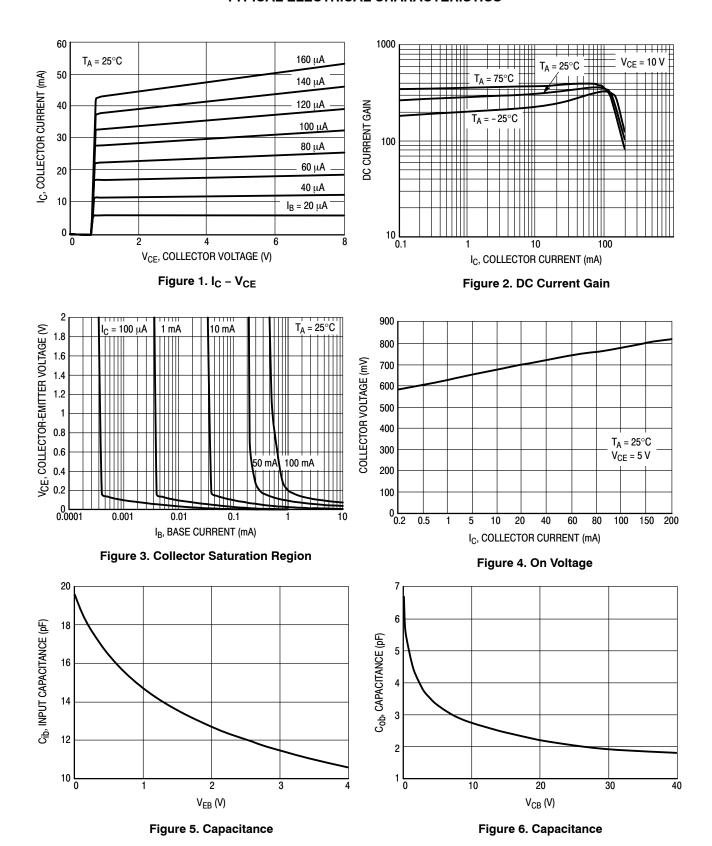
Device	Package	Shipping <sup>†</sup>
EMX1DXV6T1G, NSVEMX1DXV6T1G	SOT-563 (Pb-Free)	4000 Units / Tape & Reel
EMX1DXV6T5G	SOT-563 (Pb-Free)	8000 Units / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>3.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, D.C.  $\leq$  2%.

#### EMX1DXV6T1G, EMX1DXV6T5G

#### TYPICAL ELECTRICAL CHARACTERISTICS





## MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS



STYLE 4

STYLE 10:

PIN 1. CATHODE 1

2. N/C 3. CATHODE 2

4. ANDDE 2

PIN 1. COLLECTOR 2. COLLECTOR

3. BASE

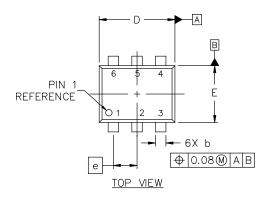
4. EMITTER
5. COLLECTOR
6. COLLECTOR

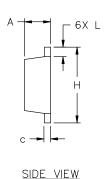
#### SOT-563-6 1.60x1.20x0.55, 0.50P CASE 463A ISSUE J

**DATE 15 FEB 2024** 

#### NOTES:

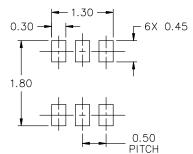
- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- 2. ALL DIMENSION ARE IN MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.





DIM NDM. MIN MAX. 0.50 0.55 0.60 Α 0.17 0.22 0.27  $\subset$ 0.08 0.13 0.18 D 1.50 1.60 1.70 Ε 1.10 1.20 1.30 9 0.50 BSC Н 1.50 1.60 1.70 0.20 0.30 L 0.10

MILLIMETERS



STYLE 6: PIN 1. CATHODE 2. ANODE

CATHODE

CATHODE

4. CATHODE 5. CATHODE

RECOMMENDED MOUNTING FOOTPRIN	*
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\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

E 1 RCE 2 RCE 2
E 2 IN 1

PIN 1. EMITTER 2

2. BASE 2 3. COLLECTOR 1

4. EMITTER 1

STYLE 11:

STYLE 5:

3. ANDDE

PIN 1. CATHODE 2. CATHODE

> 4. ANDDE 5. CATHODE

6. CATHODE

## GENERIC MARKING DIAGRAM\*



XX = Specific Device Code M = Month Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DESCRIPTION: SOT-563-6 1.60x1.20x0.55, 0.50P		PAGE 1 OF 1	
DOCUMENT NUMBER:	MENT NUMBER: 98AON11126D Electronic versions are uncontrolled except when accessed directly from the Document Reposit Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
	5. BASE 1 6. COLLECTOR 2	or may not be present. Some products may not follow the Generic Marking.	

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