

# **FDMS7658AS Datasheet**

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DiGi Electronics Part Number	FDMS7658AS-DG
Didi Liectionics Part Number	20-54050 (51410-1
Manufacturer	onsemi
Manufacturer Product Number	FDMS7658AS
Description	MOSFET N-CH 30V 29A/70A 8PQFN
Detailed Description	N-Channel 30 V 29A (Ta), 70A (Tc) 2.5W (Ta), 89W ( Tc) Surface Mount 8-PQFN (5x6)

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## Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
FDMS7658AS	onsemi
Series:	Product Status:
PowerTrench <sup>®</sup> , SyncFET™	Obsolete
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (ld) @ 25°C:
30 V	29A (Ta), 70A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ ld, Vgs:
4.5V, 10V	1.9mOhm @ 28A, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
3V @ 1mA	109 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	7350 pF @ 15 V
FET Feature:	Power Dissipation (Max):
	2.5W (Ta), 89W (Tc)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
8-PQFN (5x6)	8-PowerTDFN
Base Product Number:	
FDMS7658	

## **Environmental & Export classification**

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	



**ON Semiconductor®** 

## FDMS7658AS N-Channel PowerTrench<sup>®</sup> SyncFET<sup>TM</sup> **30 V, 176 A, 1.9 m**Ω

#### **Features**

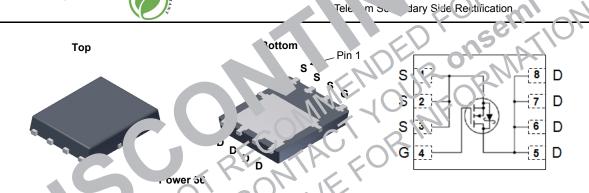
- Max r<sub>DS(on)</sub> = 1.9 mΩ at V<sub>GS</sub> = 10 V, I<sub>D</sub> = 28 A
- Max r<sub>DS(on)</sub> = 2.2 mΩ at V<sub>GS</sub> = 7 V, I<sub>D</sub> = 26 A
- Advanced Package and Silicon Combination for Low r<sub>DS(on)</sub> and High Efficiency
- SyncFET<sup>TM</sup> Schottky Body Diode
- MSL1 Robust Package Design
- 100% UIL Tested
- RoHS Compliant

## **General Description**

The FDMS7658AS has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been comto offer the lowest r<sub>DS(on)</sub> while maintaining excellent ching rformance. This device has the added benefit of in e ient mu plithic Schottky body diode.

#### Applications

- Synchronour Rectific for L 'D' Jonverters
- Notebo、 Vcc 'GPU w Side Switch.
- ad Lov Side Switch rkins 'oin



#### Im Ratings TA = 25 °C unless otherwise noted. <u>'E'</u> MC 12 -

Symbo	Parameter		Ratings	Units	
V <sub>DL</sub>	Drein to Source Voltage			30	V
V <sub>GS</sub>	Cate to Sourca Voltage		(Note 4)	±20	V
	Drain Current -Continuous	T <sub>C</sub> = 25 °C	(Note 5)	176	
	Co. undous	T <sub>C</sub> = 100 °C	(Note 5)	112	Α
	Continuous	T <sub>A</sub> = 25 °C	(Note 1a)	29	A
5	-Pulsed		(Note 6)	670	
dv/dt	MOSFE1 dv/dt			1.5	V/ns
E <sub>AS</sub>	Single Pulse Avalanche Energy		(Note 3)	162	mJ
D	Power Dissipation	T <sub>C</sub> = 25 °C		89	w
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> = 25 °C	(Note 1a)	2.5	vv
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range			-55 to +150	°C

#### **Thermal Characteristics**

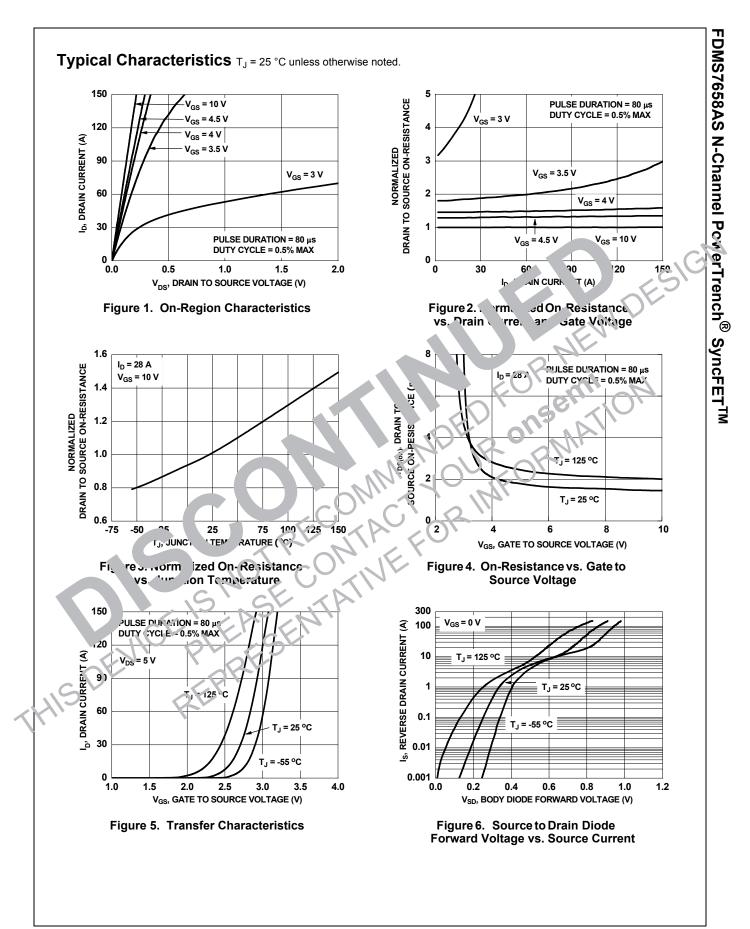
June-2024, Rev.3

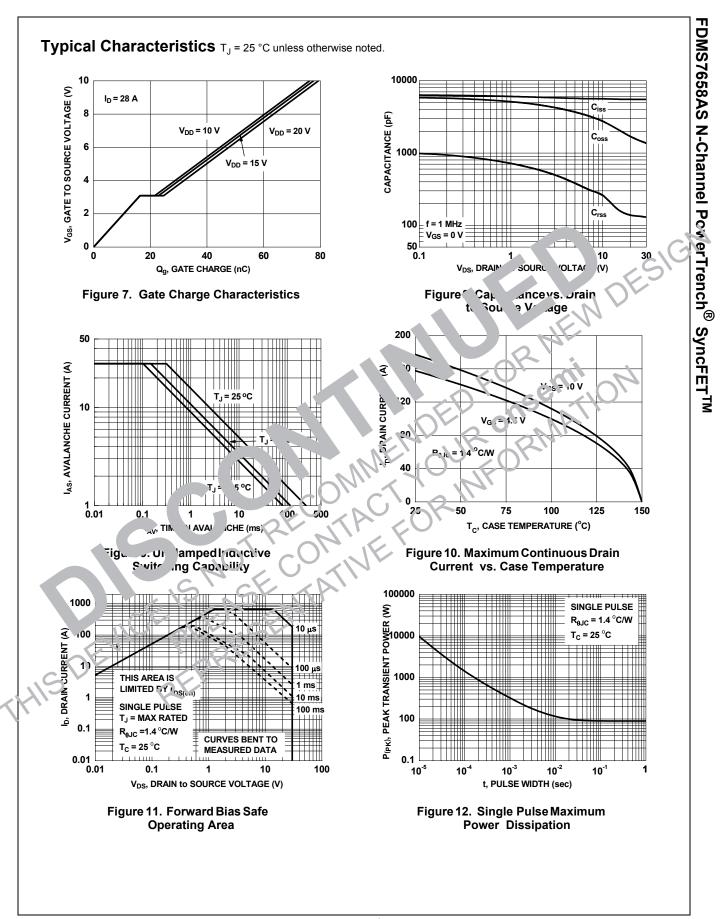
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	1.4	°C 1.1
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a	) 50	°C/W

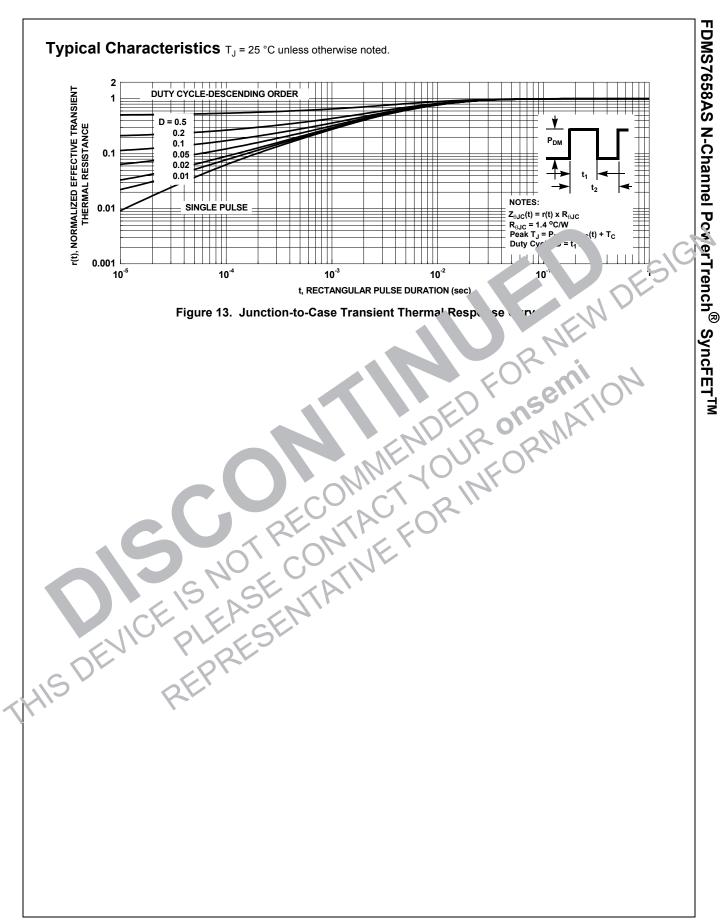
#### Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS7658AS	FDMS7658AS	Power 56	13 "	12 mm	3000 units

Symbol	Parameter	Test Conditio	ns M	in. Ty	o. Max.	Units
Off Chara	cteristics					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V	3	30		V
ABV <sub>DSS</sub>	Breakdown Voltage Temperature			-		
$\Delta T_{J}$	Coefficient	$I_D = 10 \text{ mA}, \text{ referenced}$	to 25 °C	23	3	mV/°C
DSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V			500	μA
GSS	Gate to Source Leakage Current, Forward	$V_{GS}$ = 20 V, $V_{DS}$ = 0 V			100	nA
)n Chara	cteristics (Note 2)					
V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 1 mA	1	.2 1.7	3.0	V
$\Delta V_{GS(th)}$	Gate to Source Threshold Voltage					
$\Delta T_{J}$	Temperature Coefficient	I <sub>D</sub> = 10 mA, referenced	to 25 °C	-5		mV/°C
•		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 28 A		1.5	1.9	
	Statia Drain to Source On Desistance	V <sub>GS</sub> = 7 V, I <sub>D</sub> = 26 A		7	.2	
DS(on)	Static Drain to Source On Resistance	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 23 A		1.	2.4	- msz
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 28 A, <sup>-</sup>	Г <sub>Ј</sub> = 12( С	2.0	2.6	
JFS	Forward Transconductance	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 28 A		18		S
Dvnamic	Characteristics				F	
C <sub>iss</sub>	Input Capacitance		— —	552	5 7350	pF
C <sub>OSS</sub>	Output Capacitance	$V_{DS} = 15$ $V_G = 0$		202		pF
Srss	Reverse Transfer Capacitance	f = 1 i - i z				pF
₹ <sub>g</sub>	Gate Resistance		0.			Ω
y						
Switching	Characteristics		12	201	1	
d(on)	Turn-On Delay Time		$\sim$	20		ns
ſ	Rise Time	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 28 Λ,		8	17	ns
d(off)	Turn-Off Delay Time	$V_{CS} = 10 \text{ V}, \text{ R}_{GEN} = 3 \Omega$		43		ns
f	Fall Time		2	5	10	ns
Qg	Total Gate C rge	V <sub>GS</sub> = 0 <sup>∨</sup> 10 V		78		nC
Qg	To' Gale Ch.	$V_{CS} = 0 V \text{ to } 4.5 V V_{DE}$	) = 15 V,	35		nC
Q <sub>gs</sub>	G 'e Gaue Charge		28 A	16.		nC
Q <sub>gd</sub>	Sate to Dra 'Miller' Charge	1, 1		6.6		nC
ain-S	rc. Diode Characteristics	DI.				
		V <sub>GS</sub> = 0 V, I <sub>S</sub> = 2 A	(Note 2)	0.38	3 0.9	
V <sub>SD</sub>	Source to Drain Dione Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 28 A	(Note 2)	0.74	1.3	- V
in .	Reverse Recovery Time		1 -	46	75	ns
2 <sub>rr</sub>	Reverse Recovery Charge	$-I_{\rm F}$ = 28 A, di/dt = 300 A	/μs	73	117	nC
lotes:	ined with the device nounted on a 1in <sup>2</sup> pad 2 oz copper pac rd design.	d on a 1.5 x 1.5 in. board of FR-4 r	naterial. R <sub>θJC</sub> is guara	nteed by desig	n while R <sub>0CA</sub> is d	letermined by
	a. 50 °C/W when 1 in <sup>2</sup> pad of 2				hen mounted on ad of 2 oz coppe	
	00000		00000			





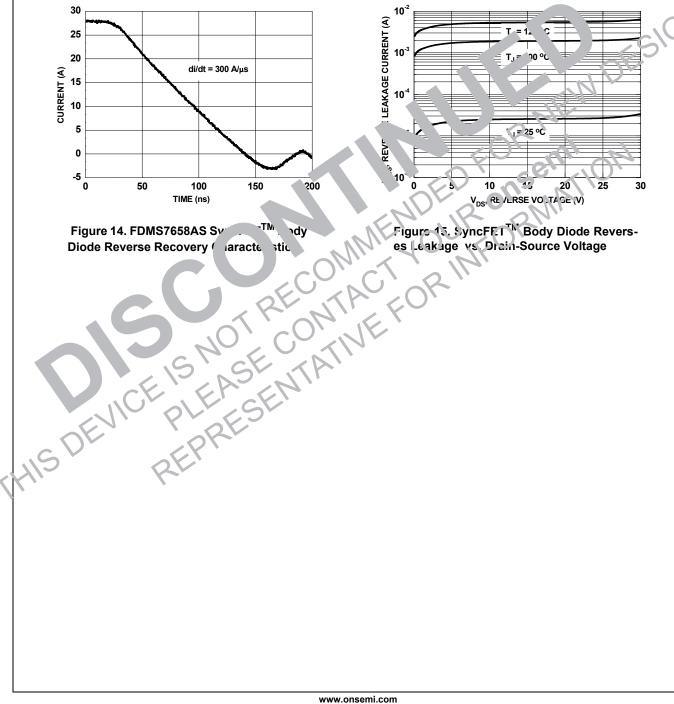


### Typical Characteristics (continued)

### SyncFET<sup>™</sup> Schottky body diode Characteristics

ON Semiconductor's SyncFET<sup>TM</sup> process embeds a Schottky diode in parallel with PowerTrench MoSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverses recovery characteristic of the FDMS7658AS.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.



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