FDPF15N65YDTU Datasheet

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DiGi Electronics Part Number	FDPF15N65YDTU-DG
Manufacturer	onsemi
Manufacturer Product Number	FDPF15N65YDTU
Description	MOSFET N-CH 650V 15A TO220F-3
Detailed Description	N-Channel 650 V 15A (Tc) 38.5W (Tc) Through Hole TO-220F-3 (Y-Forming)

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
FDPF15N65YDTU	onsemi
Series:	Product Status:
UniFET™	Obsolete
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
650 V	15A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
10V	440mOhm @ 7.5A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
5V @ 250µA	63 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±30V	3095 pF @ 25 V
FET Feature:	Power Dissipation (Max):
-	38.5W (Tc)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Through Hole
Supplier Device Package:	Package / Case:
TO-220F-3 (Y-Forming)	TO-220-3 Full Pack, Formed Leads
Base Product Number:	
FDPF1	

Environmental & Export classification

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
EAR99	8541.29.0095

FAIRCHILD SEMICONDUCTOR®

November 2013

FDPF15N65

N-Channel UniFET™ MOSFET

650 V, 15 A, 440 mΩ

Features

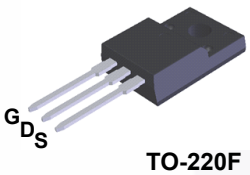
- $R_{DS(on)}$ = 360 mΩ (Typ.) @ V_{GS} = 10 V, I_D = 7.5 A
- Low Gate Charge (Typ. 48.5 nC)
- Low C_{rss} (Typ. 23.6 pF)
- 100% Avalanche Tested

Description

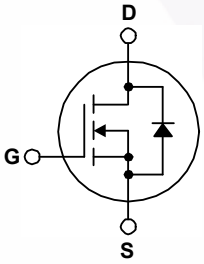
UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

Applications

- LCD/LED/PDP TV and Monitor
- Uninterruptible Power Supply



TO-220F



Absolute Maximum Ratings

T_C = 25°C unless otherwise noted.

Symbol	Parameter		FDPF15N65	Unit
V_{DSS}	Drain-Source Voltage		650	V
I_D	Drain Current	- Continuous (T_C = 25°C) - Continuous (T_C = 100°C)	15* 9.5*	A A
I_{DM}	Drain Current	- Pulsed (Note 1)	60*	A
V_{GSS}	Gate-Source voltage		± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		637	mJ
I_{AR}	Avalanche Current (Note 1)		15	A
E_{AR}	Repetitive Avalanche Energy (Note 1)		25.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P_D	Power Dissipation	(T_C = 25°C) - Derate Above 25°C	38.5 0.3	W W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

* Drain current limited by maximum junction termperature.

Thermal Characteristics

Symbol	Parameter	FDPF15N65	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	3.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDPF15N65	FDPF15N65	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 μA, T_J = 25°C	650	--	--	V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	--	0.65	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 650 V, V_{GS} = 0 V V_{DS} = 520 V, T_C = 125°C	-- --	-- --	1 10	μA μA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30 V, V_{DS} = 0 V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -30 V, V_{DS} = 0 V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 μA	3.0	--	5.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10 V, I_D = 7.5 A	--	0.36	0.44	Ω
g_{FS}	Forward Transconductance	V_{DS} = 40 V, I_D = 7.5 A	--	19.2	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	--	2380	3095	pF
C_{oss}	Output Capacitance		--	295	385	pF
C_{rss}	Reverse Transfer Capacitance		--	23.6	35.5	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 325 V, I_D = 15 A, V_{GS} = 10 V, R_G = 21.7 Ω	--	65	140	ns
t_r	Turn-On Rise Time		--	125	260	ns
$t_{d(off)}$	Turn-Off Delay Time		--	105	220	ns
t_f	Turn-Off Fall Time	(Note 4)	--	65	140	ns
Q_g	Total Gate Charge	V_{DS} = 520 V, I_D = 15 A, V_{GS} = 10 V	--	48.5	63.0	nC
Q_{gs}	Gate-Source Charge		--	14.0	--	nC
Q_{gd}	Gate-Drain Charge	(Note 4)	--	21.2	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	15*	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	60	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0 V, I_S = 15 A	--	--	1.4	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_S = 15 A, dI_F/dt =100 A/μs	--	496	--	ns
Q_{rr}	Reverse Recovery Charge		--	5.69	--	μC

Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 5.23 mH, I_{AS} = 15 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 15 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

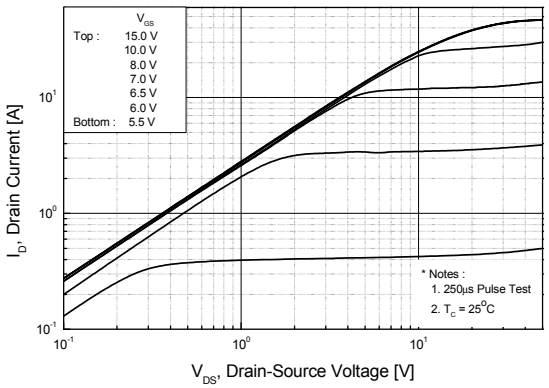


Figure 2. Transfer Characteristics

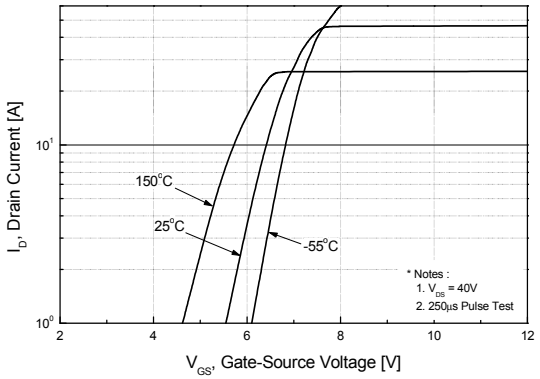


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

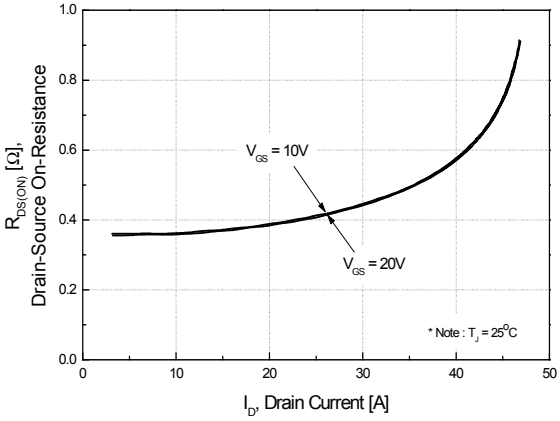


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

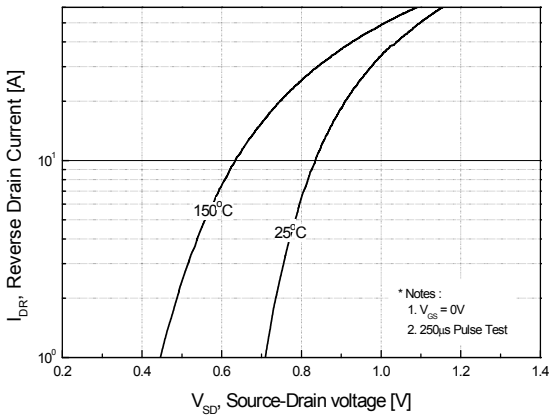


Figure 5. Capacitance Characteristics

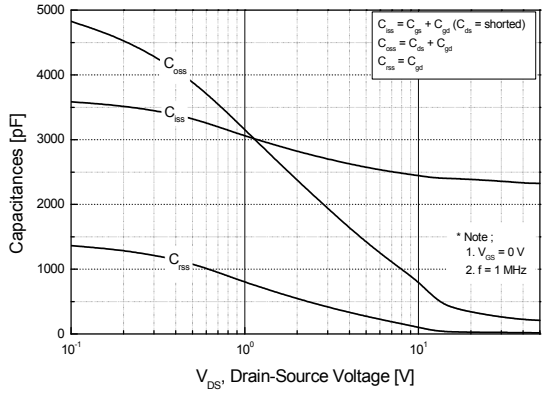
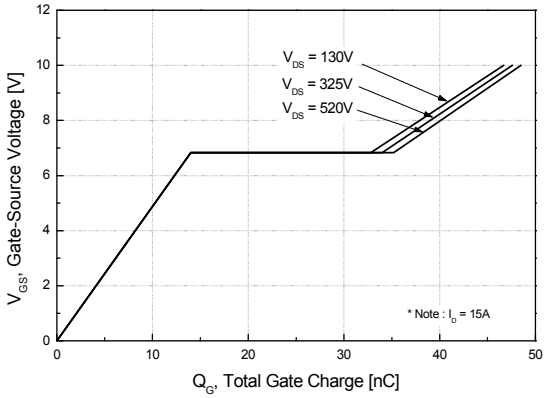


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

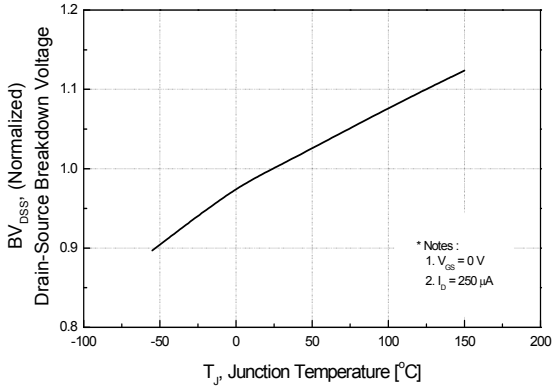


Figure 8. On-Resistance Variation vs. Temperature

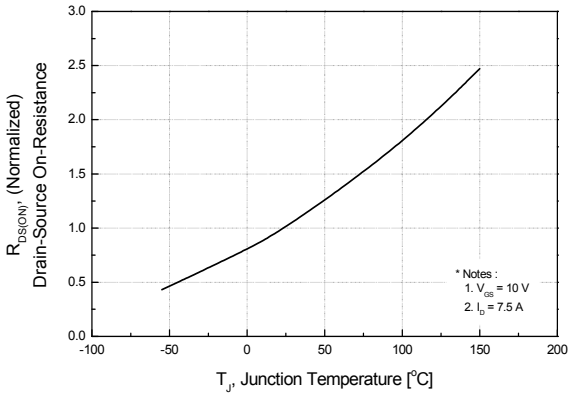


Figure 9. Safe Operating Area

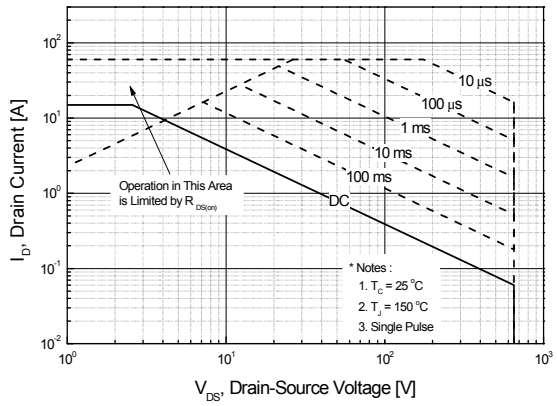


Figure 10. Maximum Drain Current vs. Case Temperature

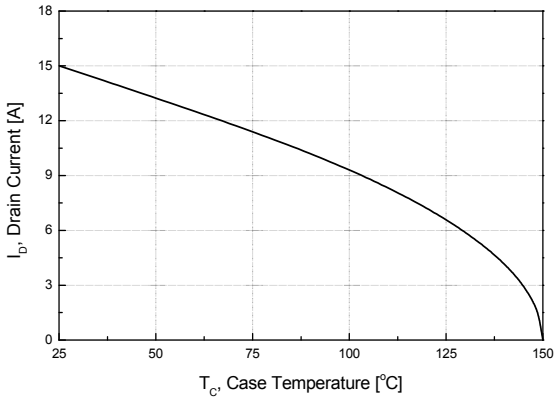
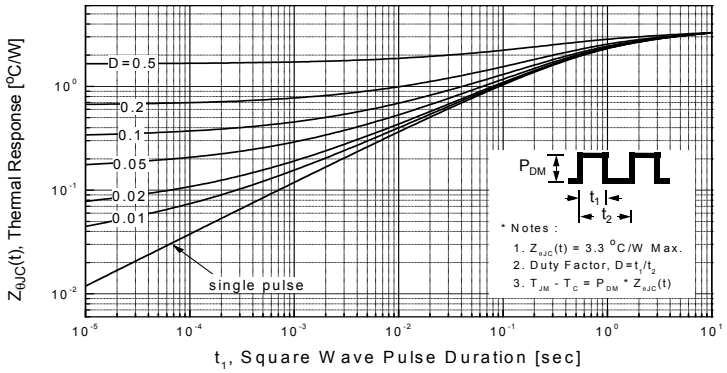


Figure 11. Transient Thermal Response Curve



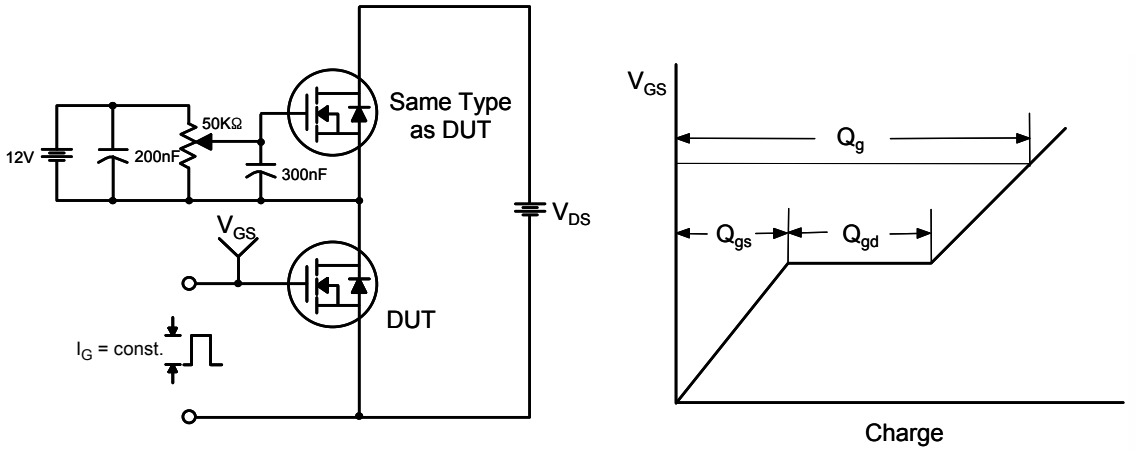


Figure 12. Gate Charge Test Circuit & Waveform

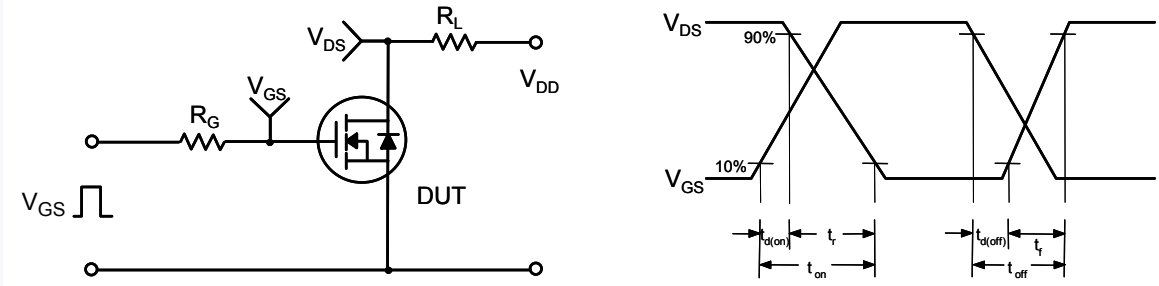


Figure 13. Resistive Switching Test Circuit & Waveforms

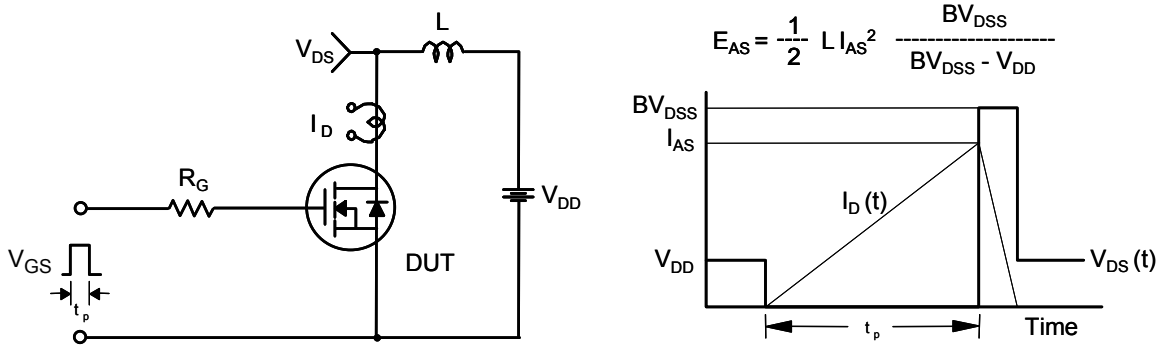


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

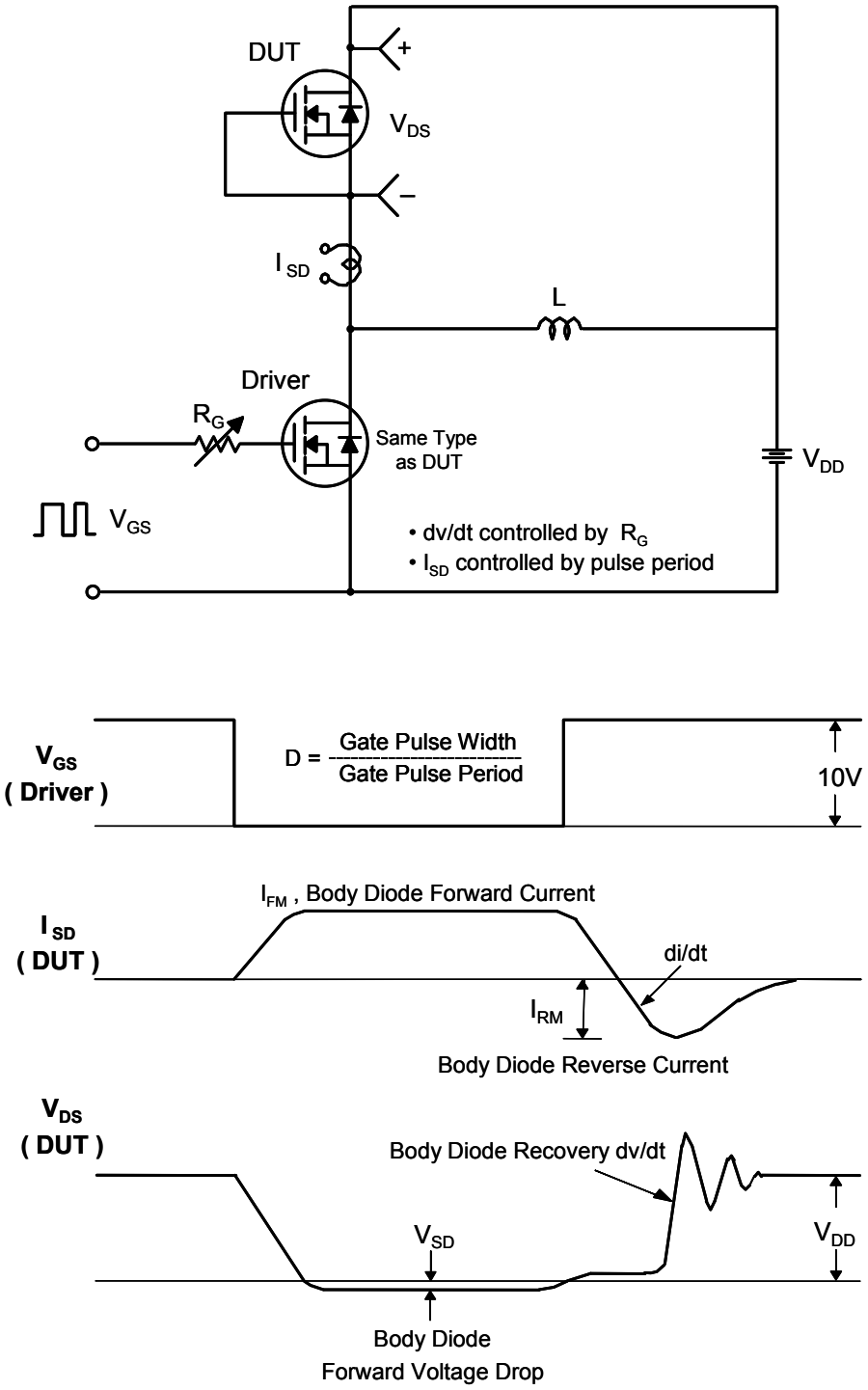


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

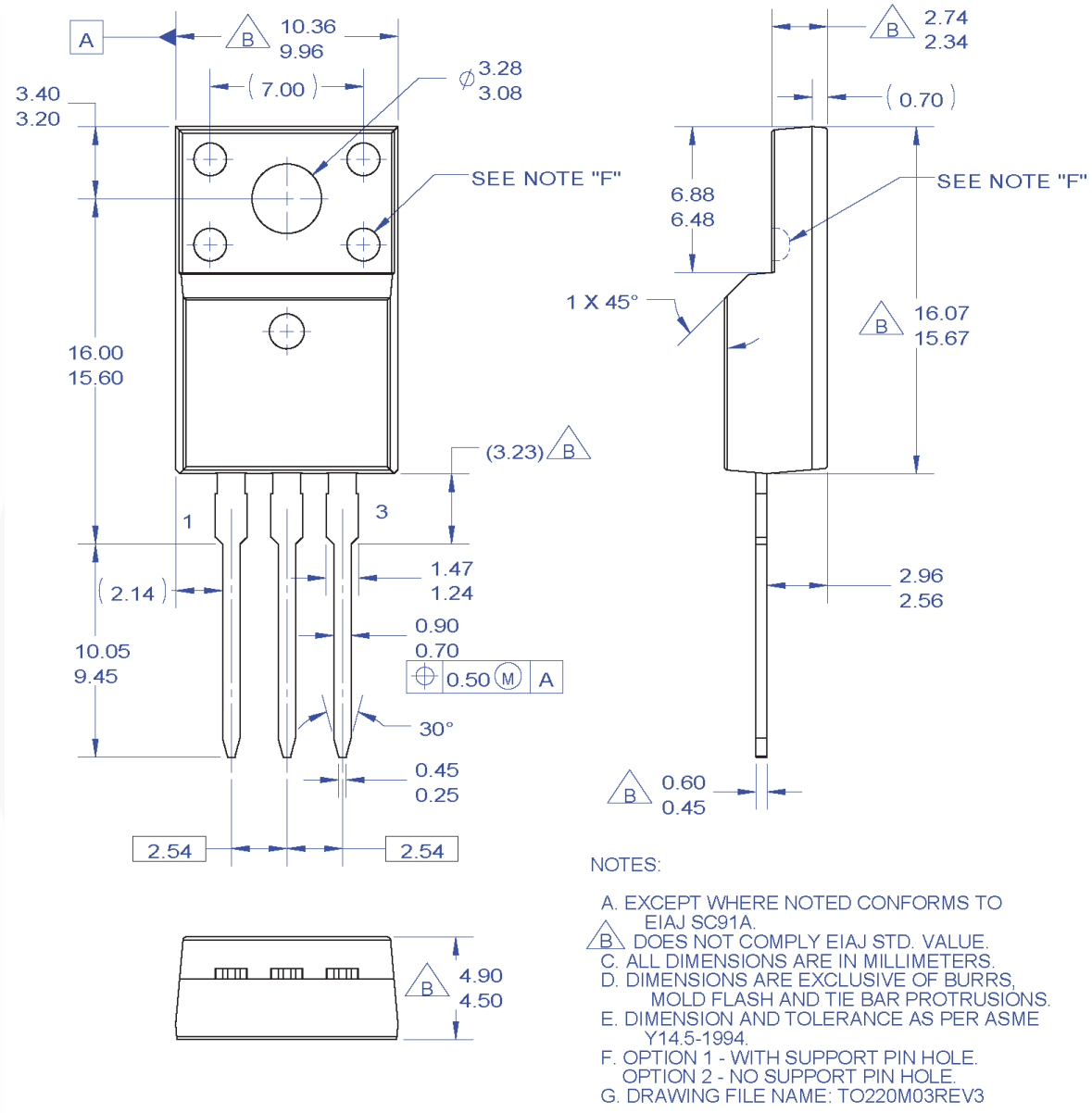


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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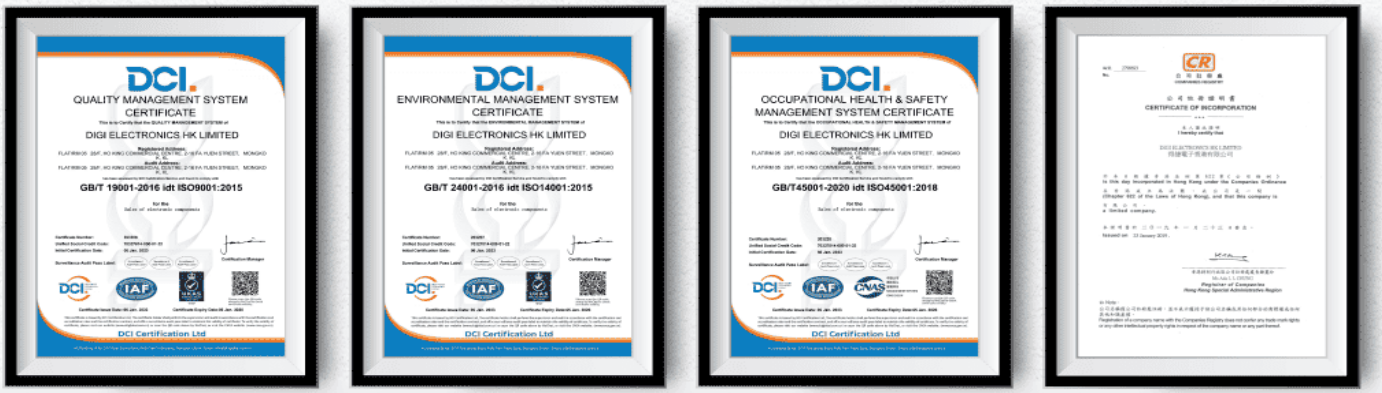

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