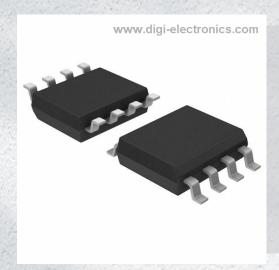


# FDS4072N7 Datasheet



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DiGi Electronics Part Number FDS4072N7-DG

Manufacturer onsemi

Manufacturer Product Number FDS4072N7

Description MOSFET N-CH 40V 12.4A 8SO

Detailed Description N-Channel 40 V 12.4A (Ta) 3W (Ta) Surface Mount 8

-SO FLMP



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## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
FDS4072N7	onsemi
Series:	Product Status:
PowerTrench®	Obsolete
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
40 V	12.4A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
4.5V, 10V	9mOhm @ 13.7A, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
3V @ 250μA	46 nC @ 4.5 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±12V	4299 pF @ 20 V
FET Feature:	Power Dissipation (Max):
	3W (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
8-SO FLMP	8-SOIC (0.154", 3.90mm Width) Exposed Pad
Base Product Number:	
FDS40	

## **Environmental & Export classification**

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
EADOO	9541 20 0005



February 2004

## FDS4072N7

## 40V N-Channel PowerTrench® MOSFET

### **General Description**

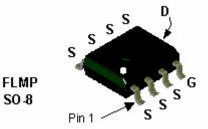
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for "low side" synchronous rectifier operation, providing an extremely low  $R_{\text{DS(ON)}}$  in a small package.

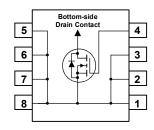
### **Applications**

- · Synchronous rectifier
- DC/DC converter

### **Features**

- 12.4 A, 40 V  $R_{DS(ON)} = 11 \text{ m}\Omega$  @  $V_{GS} = 4.5 \text{ V}$  $R_{DS(ON)} = 9 \text{ m}\Omega$  @  $V_{GS} = 10 \text{ V}$
- High performance trench technology for extremely low  $R_{\mathsf{DS}(\mathsf{ON})}$
- High power and current handling capability
- · Fast switching
- FLMP SO-8 package: Enhanced thermal performance in industry-standard package size





Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		40	V
$V_{GSS}$	Gate-Source Voltage		± 12	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	12.4	A
	- Pulsed		60	
P <sub>D</sub>	Power Dissipation	(Note 1a)	3.0	W
		(Note 1b)	1.5	
$T_J$ , $T_{STG}$	Operating and Storage Junction Temperature Range		-55 to +150	°C

### **Thermal Characteristics**

R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	(Note 1a)	40	°C/W
ReJC	Thermal Resistance, Junction-to-Case		0.5	°C/W

Package Marking and Ordering Information

		<u> </u>	<u> </u>		
Device Ma	rking	Device	Reel Size	Tape width	Quantity
FDS4072	2N7	FDS4072N7	13"	12mm	2500 units

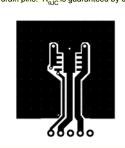
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Sc	ource Avalanche Ratings (No	te 2)	I		I	I
E <sub>AS</sub>	Drain-Source Avalanche Energy	Single Pulse, $V_{DD}$ = 20V, $I_D$ =12.4 A			200	mJ
I <sub>AS</sub>	Drain-Source Avalanche Current				12.4	Α
Off Char	acteristics			•		
BV <sub>DSS</sub>	Drain–Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \qquad I_{D} = 250 \mu\text{A}$	40			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		38		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 32 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			1	μА
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	V <sub>GS</sub> = 12 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -12 \text{ V}$ , $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	1	1.3	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		-4.5		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 12.4 A V <sub>GS</sub> = 10 V, I <sub>D</sub> = 13.7 A V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 12.4 A,T <sub>J</sub> = 125°C		9 8 14	11 9 18	mΩ
<b>g</b> <sub>FS</sub>	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 12.4 \text{ A}$		84		S
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 20 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		4299		pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		351		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			149		pF
Switchin	g Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 20 \text{ V}, \qquad I_{D} = 1 \text{ A}, \\ V_{GS} = 4.5 \text{ V}, \qquad R_{GEN} = 6 \Omega$		20	36	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 4.5 \text{ V}, \qquad R_{GEN} = 6 \Omega$		12	22	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			52	83	ns
t <sub>f</sub>	Turn–Off Fall Time			18	32	ns
Qg	Total Gate Charge	$V_{DS} = 20 \text{ V}, \qquad I_{D} = 12.4 \text{ A},$		33	46	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 4.5 V		7.8		nC
$Q_{gd}$	Gate-Drain Charge			8.1		nC
Drain-So	ource Diode Characteristics	and Maximum Ratings				
I <sub>s</sub>	Maximum Continuous Drain-Source				2.5	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = 2.5 \text{ A}  \text{(Note 2)}$		0.7	1.2	V
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> = 12.4 A,		30		nS
Q <sub>rr</sub>	Diode Reverse Recovery Charge	$d_{iF}/d_{t} = 100 \text{ A/}\mu\text{s}$		90		nC

## **Electrical Characteristics**

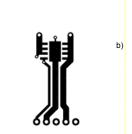
T<sub>A</sub> = 25°C unless otherwise noted

#### Notes

 R<sub>aJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>aJC</sub> is guaranteed by design while R<sub>aCA</sub> is determined by the user's board design.



a) 40°C/W when mounted on a 1in² pad of 2 oz copper



85°C/W when mounted on a minimum pad of 2 oz copper

Scale 1: 1 on letter size paper

**2.** Pulse Test: Pulse Width <  $300\mu$ s, Duty Cycle < 2.0%

## **Typical Characteristics**

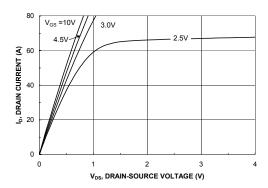


Figure 1. On-Region Characteristics.

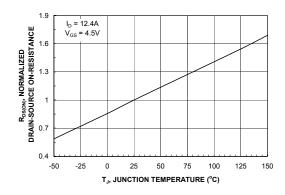


Figure 3. On-Resistance Variation withTemperature.

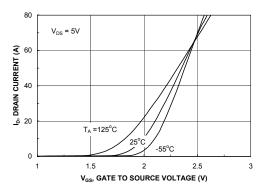


Figure 5. Transfer Characteristics.

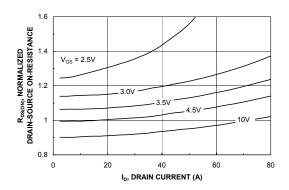


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

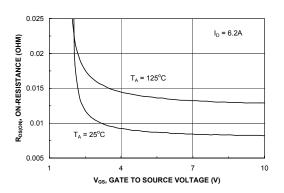


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

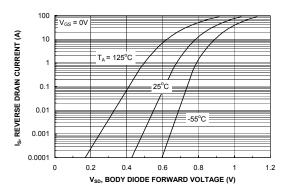
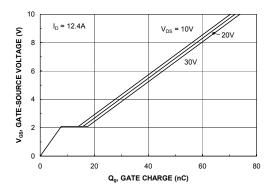


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## **Typical Characteristics**



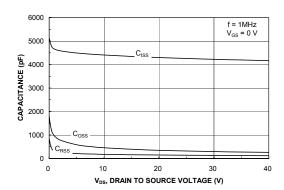


Figure 7. Gate Charge Characteristics.

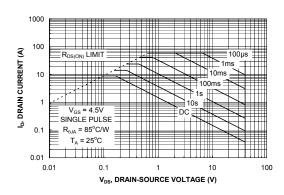


Figure 8. Capacitance Characteristics.

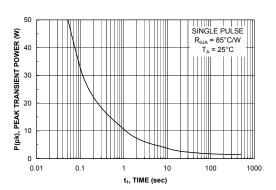


Figure 9. Maximum Safe Operating Area.



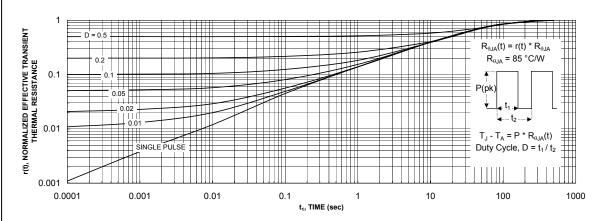


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1b. Transient thermal response will change depending on the circuit board design.

## **Dimensional Outline and Pad Layout** -(0.65) (2.36)DRAIN TERMINAL 0.75 MIN (0.67) (2.36)2.80 MIN DRAIN 2.80 MIN TERMINAL 0.70 BOTTOM VIEW 1.50 MIN 4 $4.90\pm0.10$ 1.40 1.27 3.81 3.81 В LAND PATTERN RECOMMENDATION 3.90±0.10 SEE DETAIL A (0.34)1.27 6.00±0.20 NOTES: UNLESS OTHERWISE SPECIFIED △ 0.1 C ALL DIMENSIONS ARE IN MILLIMETERS. STANDARD LEAD FINISH: 20-80 MICROINCHES NICKEL/ 6 MICROINCHES MAX. PALLADIUM AND GOLD FLASH. NO JEDEC REGISTERED REFERENCE AS OF MARCH 2, 2000. 0.50 X 45° GAGE PLANE 1.60 MAX 0.90 0.50 SEATING PLANE (1.04) DETAIL A SCALE: 24:1

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