

# FJB3307DTM Datasheet



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DiGi Electronics Part Number FJE

FJB3307DTM-DG

Manufacturer

onsemi

Manufacturer Product Number

FJB3307DTM

Description

TRANS NPN 400V 8A D2PAK

**Detailed Description** 

Bipolar (BJT) Transistor NPN 400 V 8 A 1.72 W Surfa

ce Mount TO-263 (D2PAK)



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### **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
FJB3307DTM	onsemi
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
NPN	8 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, Ic:
400 V	3V @ 2A, 8A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
	5 @ 5A, 5V
Power - Max:	Frequency - Transition:
1.72 W	
Operating Temperature:	Mounting Type:
150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
TO-263-3, D2PAK (2 Leads + Tab), TO-263AB	TO-263 (D2PAK)
Base Product Number:	
FJB3307	

### **Environmental & Export classification**

Moisture Sensitivity Level (MSL):	REACH Status:		
1 (Unlimited)	REACH Unaffected		
ECCN:	HTSUS:		
FAR99	8541 29 0095		



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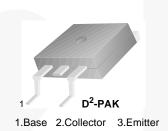
July 2013

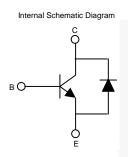
### **FJB3307D**

# **High-Voltage Fast-Switching NPN Power Transistor**

#### **Features**

- Built-in Diode between Collector and Emitter
- Suitable for Electronic Ballast and Switch-Mode Power Supplies





### **Ordering Information**

Part Number	Number Marking Package		Packing Method	
FJB3307DTM	J3307D	D <sup>2</sup> -PAK	Tape and Reel	

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	700	V
V <sub>CEO</sub>	Collector-Emitter Voltage	400	V
$V_{EBO}$	Emitter-Base Voltage	9	V
I <sub>C</sub>	Collector Current (DC)	8	Α
I <sub>CP</sub> <sup>(1)</sup>	Collector Current (Pulse)	16	Α
I <sub>B</sub>	Base Current (DC)	4	Α
I <sub>BP</sub> <sup>(1)</sup>	Base Current (Pulse)	8	Α
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C

1

#### Note:

1. Pulse test: pulse width =  $300\mu s$ , duty cycle = 2% pulsed.

### **Thermal Characteristics**

Symbol	Parameter		Value	Units
P <sub>D</sub>	Total Device Dissipation	T <sub>A</sub> = 25°C	1.72	W
PD Total Device Dissipation	Total Device Dissipation	$T_C = 25^{\circ}C$	80	W
$R_{\theta ja}$	Thermal Resistance, Junction to Ambient		72.5	°C/W
$R_{\theta jc}$	Thermal Resistance, Junction to Case 1.56 °C		°C/W	

### **Electrical Characteristics**(2)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 500 \mu A, I_E = 0$	700			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 5 \text{ mA}, I_B = 0$	400			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 500  \mu A,  I_C = 0$	9			V
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 9 \text{ V, } I_{C} = 0$			1	mA
h <sub>FE1</sub>	DC Current Gain	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 2 A	8		40	
h <sub>FE2</sub>	DC Current Gain	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 5 A	5		30	
		$I_C = 2 \text{ A}, I_B = 0.4 \text{ A}$			1	V
V	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5 A, I <sub>B</sub> = 1 A			2	V
V <sub>CE(sat)</sub> Co		I <sub>C</sub> = 5 A, I <sub>B</sub> = 1 A, T <sub>A</sub> = 100°C			3	V
		I <sub>C</sub> = 8 A, I <sub>B</sub> = 2 A			3	V
		$I_C = 2 \text{ A}, I_B = 0.4 \text{ A}$			1.2	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5 A, I <sub>B</sub> = 1 A			1.6	V
,		I <sub>C</sub> = 5 A, I <sub>B</sub> = 1 A, T <sub>A</sub> = 100°C			2.0	V
V <sub>F</sub>	Diode Forward Voltage	I <sub>C</sub> = 3 A			2.5	V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz		60		pF
t <sub>STG</sub>	Storage Time	V <sub>CC</sub> = 125 V, I <sub>C</sub> = 5 A,			3.0	μs
t <sub>F</sub>	Fall Time	$I_{B1} = -I_{B2} = 1 \text{ A}, R_L = 50 \Omega$			0.7	μs
t <sub>STG</sub>	Storage Time	$V_{CC}$ = 30 V, $I_{C}$ = 5 A, L=200 $\mu$ H $I_{B1}$ =1 A, $R_{BB}$ = 0 $\Omega$ ,			2.3	μs
t <sub>F</sub>	Fall Time	$V_{BE(OFF)} = -5 \text{ V},$ $V_{CLAMP} = 250 \text{ V}$			150	ns

#### Note:

2. Pulse test:  $pw = 300 \mu s$ , duty cycle = 2%.

### h<sub>FE</sub> Classification

Classification	H1	H2
h <sub>FE1</sub>	15 ~ 28	26 ~ 39

### **Typical Performance Characteristics**

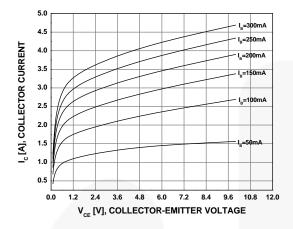


Figure 1. Static Characteristic

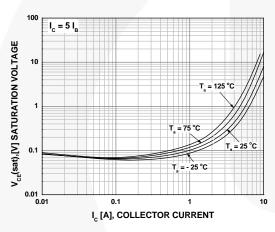


Figure 3. Collector-Emitter Saturation Voltage

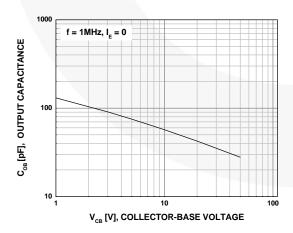


Figure 5. Collector Output Capacitance

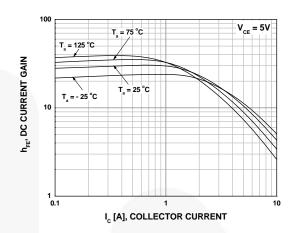


Figure 2. DC Current Gain

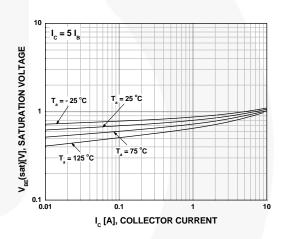


Figure 4. Base-Emitter Saturation Voltage

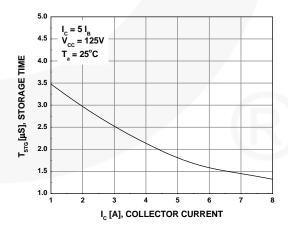


Figure 6. Storage Time (Resistive Load)

3.00

### **Typical Performance Characteristics** (Continued)

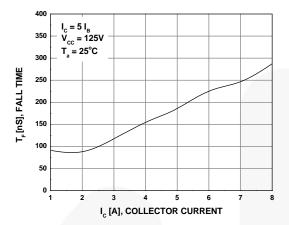


Figure 7. Fall Time (Resistive Load)

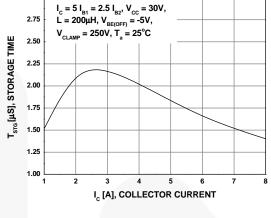


Figure 8. Storage Time (Inductive Load)

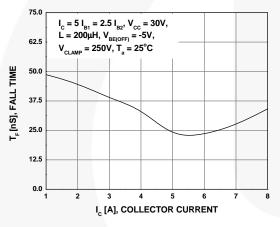


Figure 9. Fall Time (Inductive Load)

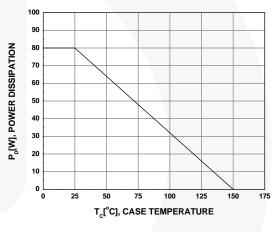


Figure 10. Power Derating

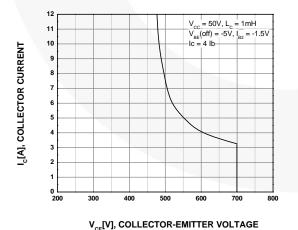
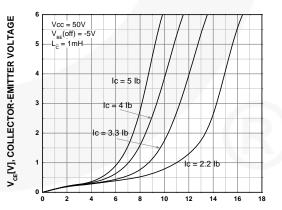


Figure 11. Reverse Bias Safe Operating Area



I<sub>CE</sub>[A], COLLECTOR CURRENT Figure 12. RBSOA Saturation

### **Typical Performance Characteristics** (Continued)

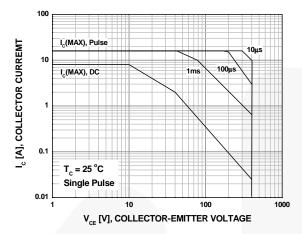


Figure 13. Forward Biased Safe Operating Area

### **Physical Dimensions**

## D<sup>2</sup>-PAK

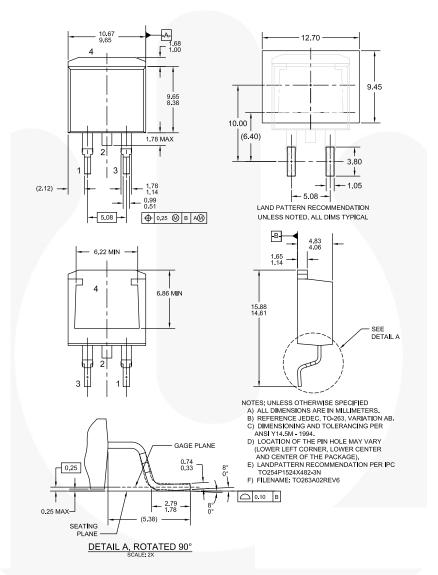


Figure 14. 2-LEAD, JEDEC TO263, VARIATION AB, SURFACE MOUNT (Active)

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