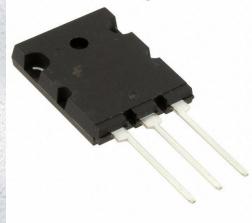


FJL6920TU Datasheet

Manu

www.digi-electronics.com



DiGi Electronics Part Number	FJL6920TU-DG
Manufacturer	onsemi
1anufacturer Product Number	FJL6920TU
Description	TRANS NPN 800V 20A TO264-3
Detailed Description	Bipolar (BJT) Transistor NPN 800 V 20 A 200 W Thro ugh Hole TO-264-3

https://www.DiGi-Electronics.com



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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
FJL6920TU	onsemi
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN	20 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
800 V	3V @ 2.75A, 11A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
1mA	5.5 @ 11A, 5V
Power - Max:	Frequency - Transition:
200 W	
Operating Temperature:	Mounting Type:
150°C (TJ)	Through Hole
Package / Case:	Supplier Device Package:
TO-264-3, TO-264AA	TO-264-3
Base Product Number:	
FJL6920	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	Not Applicable
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	

onsemi

NPN Triple Diffused Planar Silicon Transistor

FJL6920

Features

- High Collector–Base Breakdown Voltage: $BV_{CBO} = 1700 V$
- Low Saturation Voltage: $V_{CE}(sat) = 3 V (Max.)$
- For Color Monitor
- These Devices are Pb-Free, Halide Free and are RoHS Compliant

Applications

• High Voltage Color Display Horizontal Deflection Output

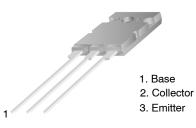
()				
Parameter	Symbol	Rating	Units	
Collector-Base Voltage	V _{CBO}	1700	V	
Collector-Emitter Voltage	V _{CEO}	800	V	
Emitter-Base Voltage	V _{EBO}	6	V	
Collector Current (DC)	۱ _C	20	А	
Collector Current (Pulse) *	I _{CP}	30	А	
Collector Dissipation	P _C	200	W	
Junction Temperature	TJ	150	°C	
Storage Temperature	T _J , T _{STG}	-55 ~ 150	°C	

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. *Pulse Test: PW = $300 \ \mu$ s, Duty Cycle = 2% Pulsed

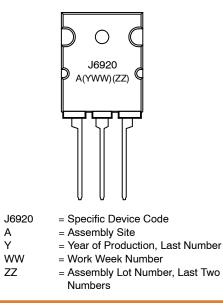
THERMAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Parameter	Symbol	Max.	Units
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.625	°C/W



TO-264-3LD CASE 340CA

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping
FJL6920TU	TO-264-3LD	375 Units / Tube

FJL6920

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _{CES}	Collector Cut-Off Current	V _{CB} = 1400 V, R _{BE} = 0			1	mA
I _{CBO}	Collector Cut-Off Current	V _{CB} = 800 V, I _E = 0			10	μA
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = 4 \text{ V}, \text{ I}_{C} = 0$			1	mA
BV_{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 500 \ \mu A, \ I_{E} = 0$	1700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 5 mA, I _B = 0	800			V
BV_{EBO}	Emitter-Base Breakdown Voltage	I _E = 500 μA, I _C = 0	6			V
h _{FE1}	DC Current Gain	V _{CE} = 5 V, I _C = 1 A	8			
h _{FE2}	DC Current Gain	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 11 \text{ A}$	5.5		8.5	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 11 A, I _B = 2.75 A			3	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 11 A, I _B = 2.75 A			1.5	V
t _{STG}	Storage Time (Note 1)				3	μs
t _F	Fall Time (Note 1)			0.15	0.2	μs

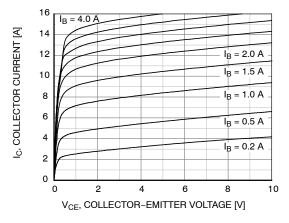
ELECTRICAL CHARACTERISTICS (Note 1) ($T_C = 25^{\circ}C$ unless otherwise noted)

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: PW = 20 μ s, Duty Cycle = 1% Pulsed

FJL6920

TYPICAL CHARACTERISTICS





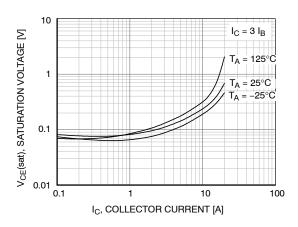


Figure 3. Collector-Emitter Saturation Voltage

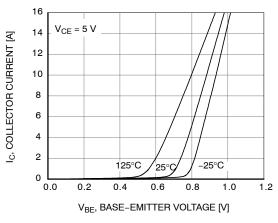


Figure 5. Base-Emitter On Voltage

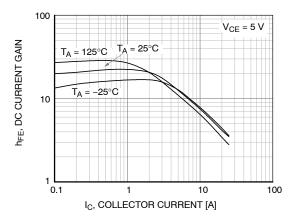
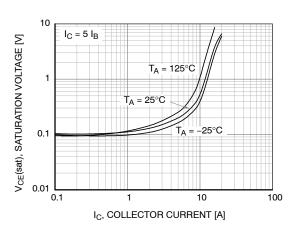


Figure 2. DC Current Gain





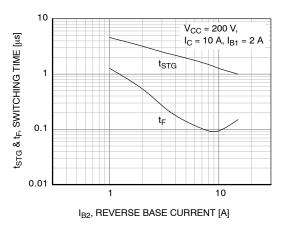


Figure 6. Resistive Load Switching Time

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TYPICAL CHARACTERISTICS (Continued)

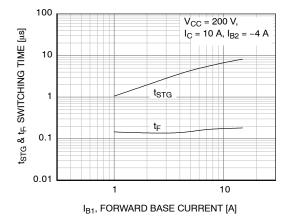


Figure 7. Resistive Load Switching Time

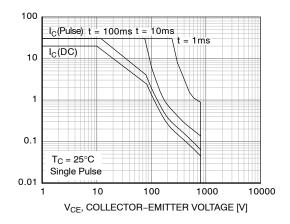


Figure 9. Forward Bias Safe Operating Area

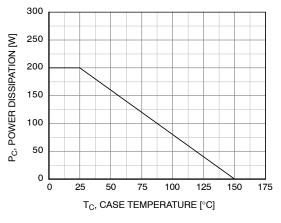
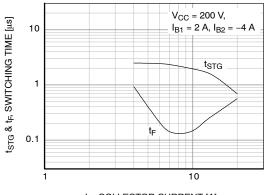


Figure 11. Power Derating



I_C, COLLECTOR CURRENT [A]

Figure 8. Resistive Load Switching Time

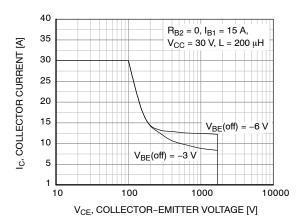


Figure 10. Reverse Bias Safe Operating Area

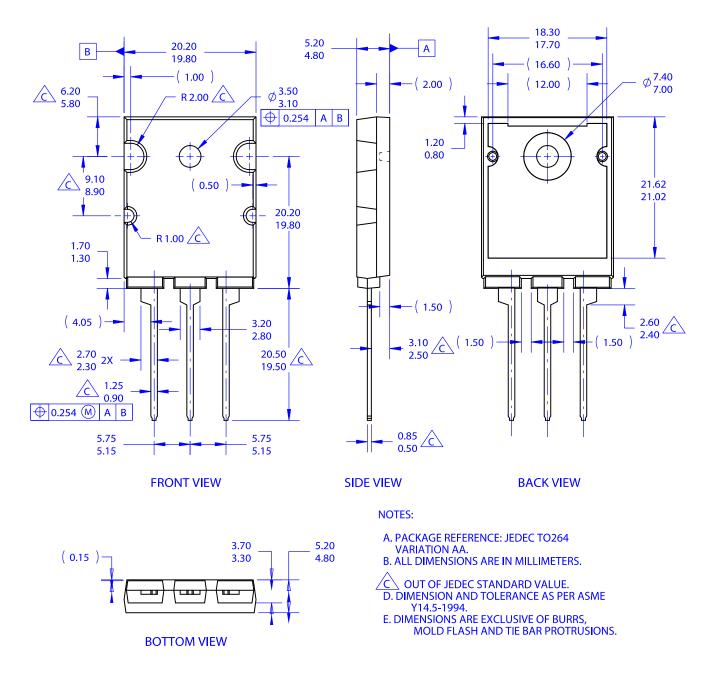
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MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

TO-264-3LD CASE 340CA ISSUE O

DATE 31 OCT 2016



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DESCRIPTION:	TO-264-3LD		PAGE 1 OF 1

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