

FQD6P25TF Datasheet



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DiGi Electronics Part Number	FQD6P25TF-DG
Manufacturer	onsemi
Manufacturer Product Number	FQD6P25TF
Description	MOSFET P-CH 250V 4.7A DPAK
Detailed Description	P-Channel 250 V 4.7A (Tc) 2.5W (Ta), 55W (Tc) Surface Mount TO-252AA



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Purchase and inquiry

Manufacturer Product Number:

FQD6P25TF

Series:

QFET®

FET Type:

P-Channel

Drain to Source Voltage (Vdss):

250 V

Drive Voltage (Max Rds On, Min Rds On):

10V

Vgs(th) (Max) @ Id:

5V @ 250µA

Vgs (Max):

±30V

FET Feature:

-

Operating Temperature:

-55°C ~ 150°C (Tj)

Supplier Device Package:

TO-252AA

Base Product Number:

FQD6

Manufacturer:

onsemi

Product Status:

Obsolete

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

4.7A (Tc)

Rds On (Max) @ Id, Vgs:

1.10hm @ 2.35A, 10V

Gate Charge (Qg) (Max) @ Vgs:

27 nC @ 10 V

Input Capacitance (Ciss) (Max) @ Vds:

780 pF @ 25 V

Power Dissipation (Max):

2.5W (Ta), 55W (Tc)

Mounting Type:

Surface Mount

Package / Case:

TO-252-3, DPAK (2 Leads + Tab), SC-63

Environmental & Export classification

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

REACH Status:

REACH Unaffected

HTSUS:

8541.29.0095



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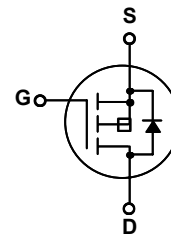
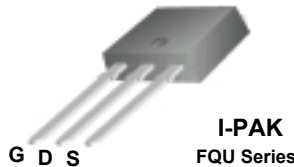
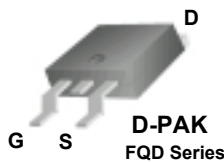
250V P-Channel MOSFET

General Description

These P-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters.

Features

- -4.7A, -250V, $R_{DS(on)} = 1.1\Omega @ V_{GS} = -10V$
- Low gate charge (typical 21 nC)
- Low Crss (typical 20 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS Compliant



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	FQD6P25 / FQU6P25	Units
V _{DSS}	Drain-Source Voltage	-250	V
I _D	Drain Current - Continuous (T _C = 25°C)	-4.7	A
	- Continuous (T _C = 100°C)	-3.0	A
I _{DM}	Drain Current - Pulsed (Note 1)	-18.8	A
V _{GSS}	Gate-Source Voltage	± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	540	mJ
I _{AR}	Avalanche Current (Note 1)	-4.7	A
E _{AR}	Repetitive Avalanche Energy (Note 1)	5.5	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	-5.5	V/ns
P _D	Power Dissipation (T _A = 25°C) *	2.5	W
	Power Dissipation (T _C = 25°C)	55	W
	- Derate above 25°C	0.44	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	°C

Thermal Characteristics

Symbol	Parameter	Typ	Max	Units
R _{θJC}	Thermal Resistance, Junction-to-Case	--	2.27	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient *	--	50	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	--	110	°C/W

* When mounted on the minimum pad size recommended (PCB Mount)

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = -250\ \mu\text{A}$	-250	--	--	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = -250\ \mu\text{A}$, Referenced to 25°C	--	-0.1	--	$\text{V}/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -250\text{ V}, V_{GS} = 0\text{ V}$	--	--	-1	μA
		$V_{DS} = -200\text{ V}, T_C = 125^\circ\text{C}$	--	--	-10	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\ \mu\text{A}$	-3.0	--	-5.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = -10\text{ V}, I_D = -2.35\text{ A}$	--	0.82	1.1	Ω
g_{FS}	Forward Transconductance	$V_{DS} = -40\text{ V}, I_D = -2.35\text{ A}$ (Note 4)	--	3.0	--	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = -25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	600	780	pF
C_{oss}	Output Capacitance		--	115	150	pF
C_{rss}	Reverse Transfer Capacitance		--	20	25	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = -125\text{ V}, I_D = -6.0\text{ A},$ $R_G = 25\ \Omega$ (Note 4, 5)	--	13	35	ns
t_r	Turn-On Rise Time		--	75	160	ns
$t_{d(off)}$	Turn-Off Delay Time		--	40	90	ns
t_f	Turn-Off Fall Time		--	50	110	ns
Q_g	Total Gate Charge	$V_{DS} = -200\text{ V}, I_D = -6.0\text{ A},$ $V_{GS} = -10\text{ V}$ (Note 4, 5)	--	21	27	nC
Q_{gs}	Gate-Source Charge		--	4.7	--	nC
Q_{gd}	Gate-Drain Charge		--	10.7	--	nC

Drain-Source Diode Characteristics and Maximum Ratings

I_S	Maximum Continuous Drain-Source Diode Forward Current	--	--	-4.7	A	
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current	--	--	-18.8	A	
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = -4.7\text{ A}$	--	--	-5.0	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_S = -6.0\text{ A},$ $dI_F / dt = 100\text{ A}/\mu\text{s}$ (Note 4)	--	170	--	ns
Q_{rr}	Reverse Recovery Charge		--	1.1	--	μC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. $L = 39\text{ mH}, I_{AS} = -4.7\text{ A}, V_{DD} = -50\text{ V}, R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq -6.0\text{ A}, di/dt \leq 300\text{ A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width $\leq 300\ \mu\text{s}$, Duty cycle $\leq 2\%$
5. Essentially independent of operating temperature

Typical Characteristics

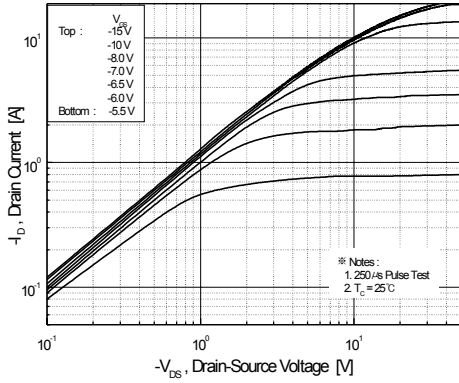


Figure 1. On-Region Characteristics

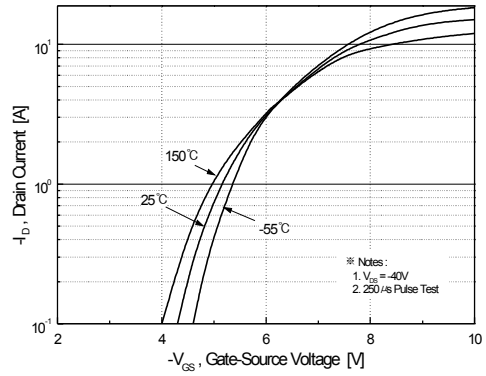


Figure 2. Transfer Characteristics

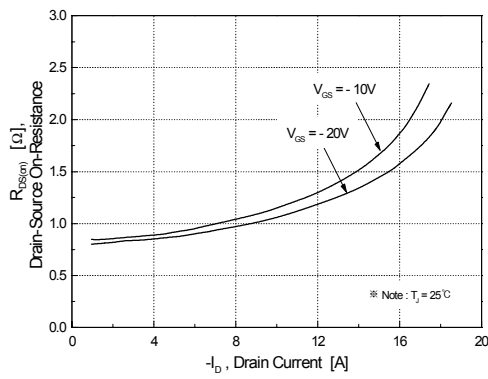


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

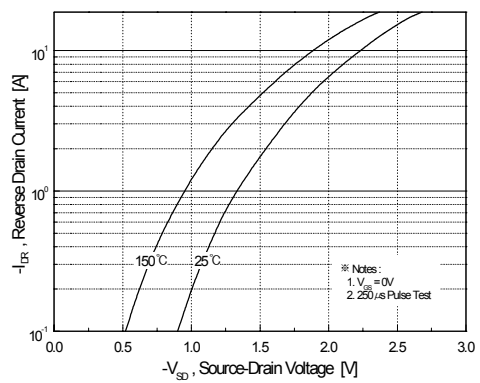


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

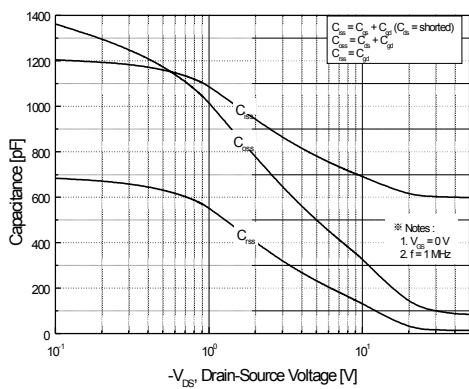


Figure 5. Capacitance Characteristics

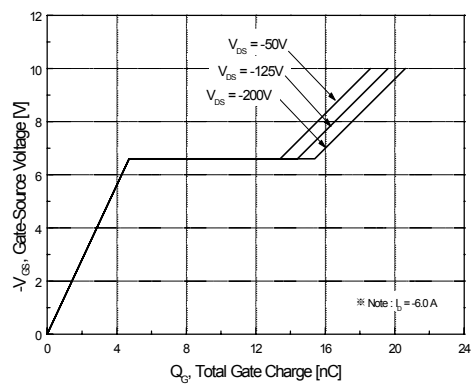


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

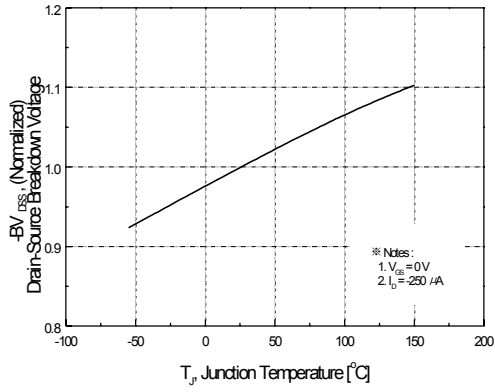


Figure 7. Breakdown Voltage Variation vs. Temperature

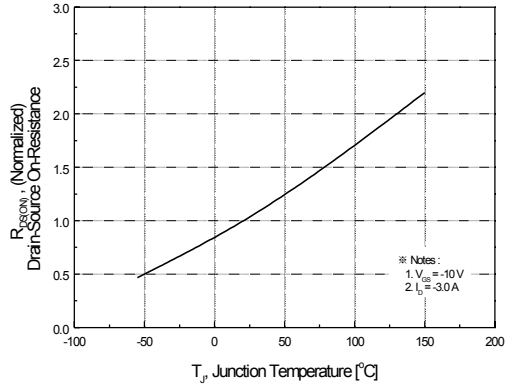


Figure 8. On-Resistance Variation vs. Temperature

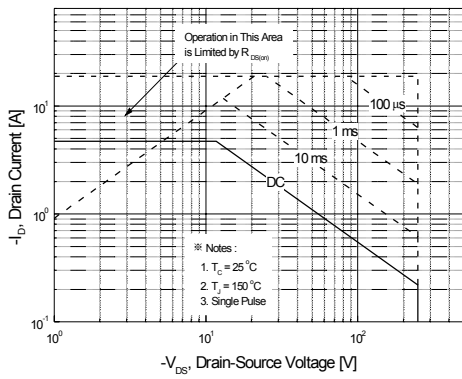


Figure 9. Maximum Safe Operating Area

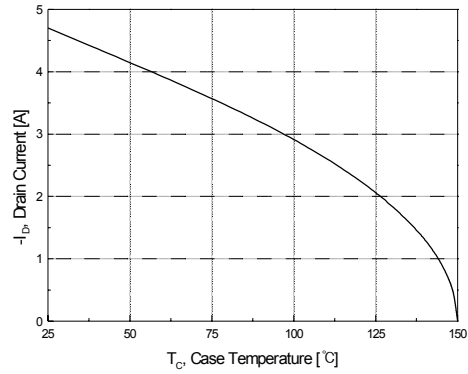


Figure 10. Maximum Drain Current vs. Case Temperature

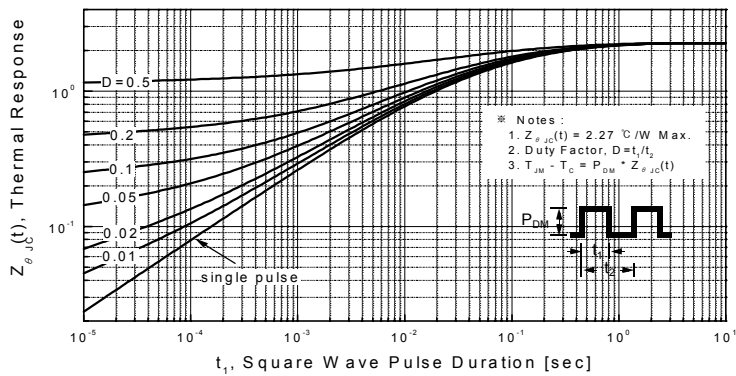
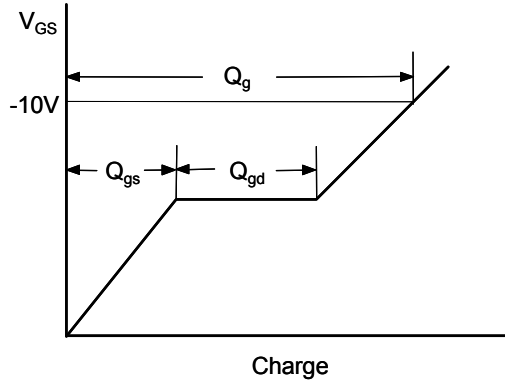
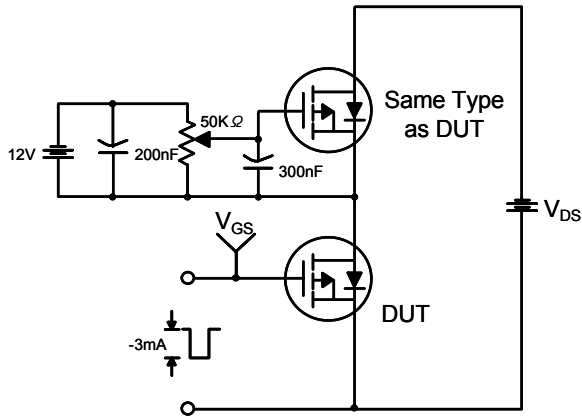


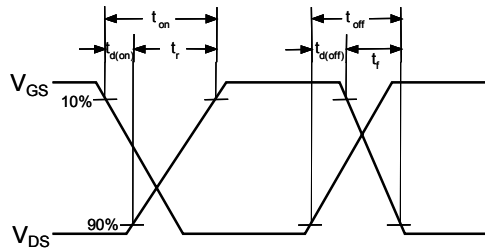
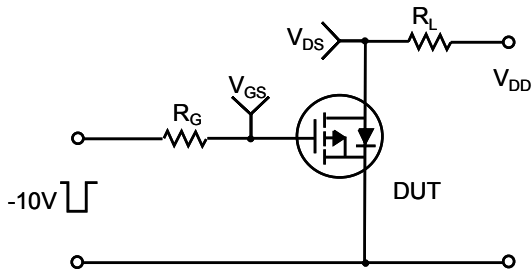
Figure 11. Transient Thermal Response Curve

FQD6P25 / FQU6P25

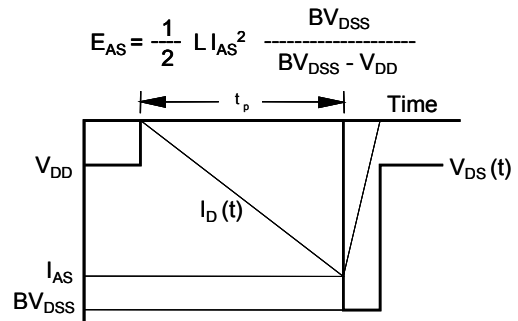
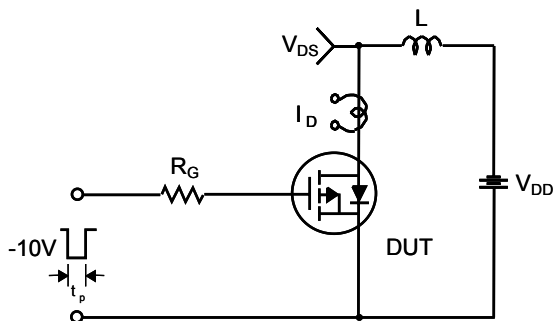
Gate Charge Test Circuit & Waveform



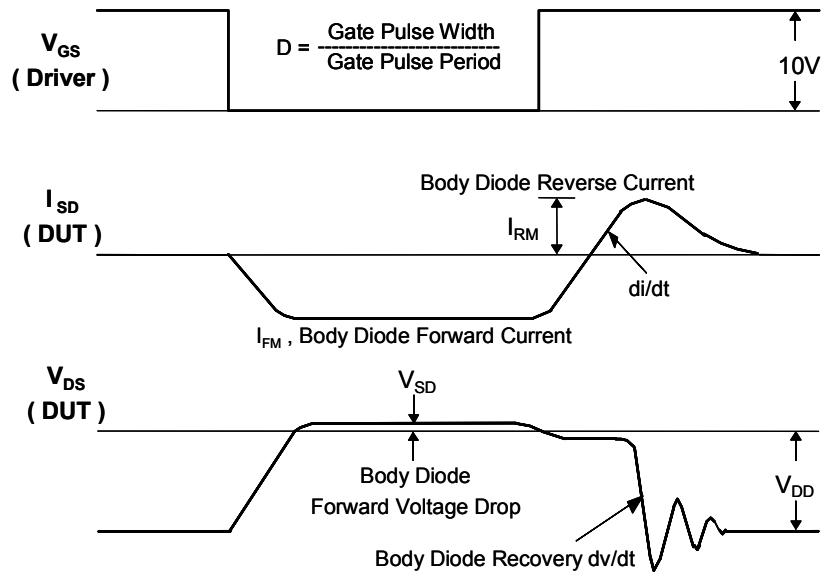
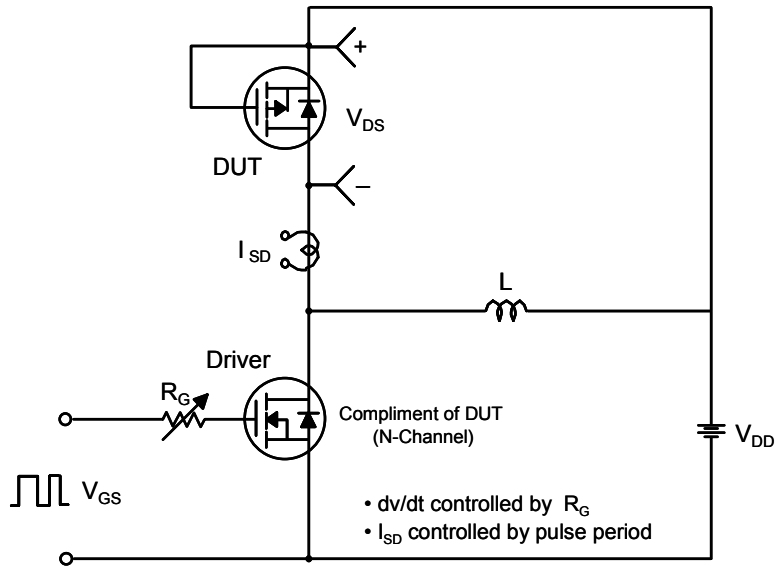
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



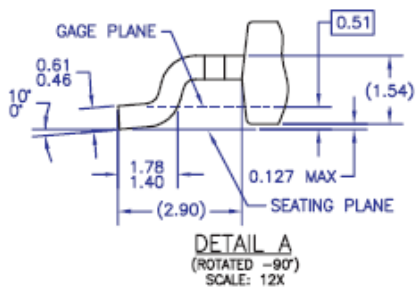
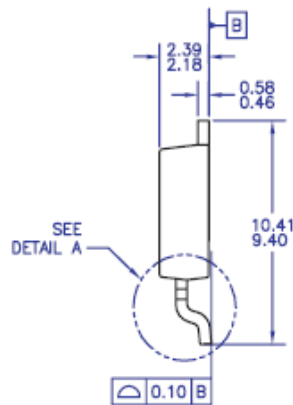
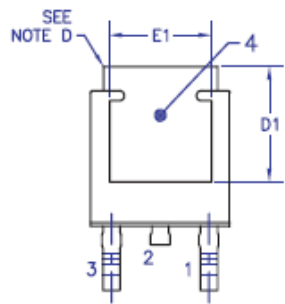
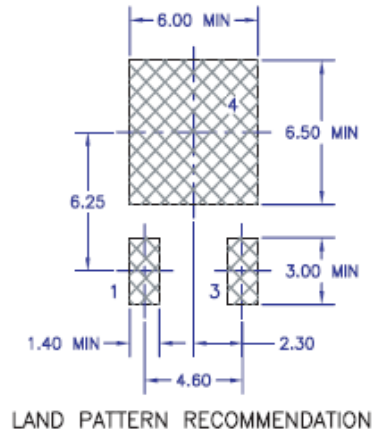
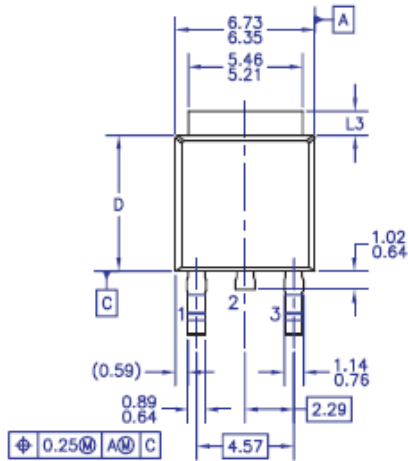
Peak Diode Recovery dv/dt Test Circuit & Waveforms



FQD6P25 / FQU6P25

Mechanical Dimensions

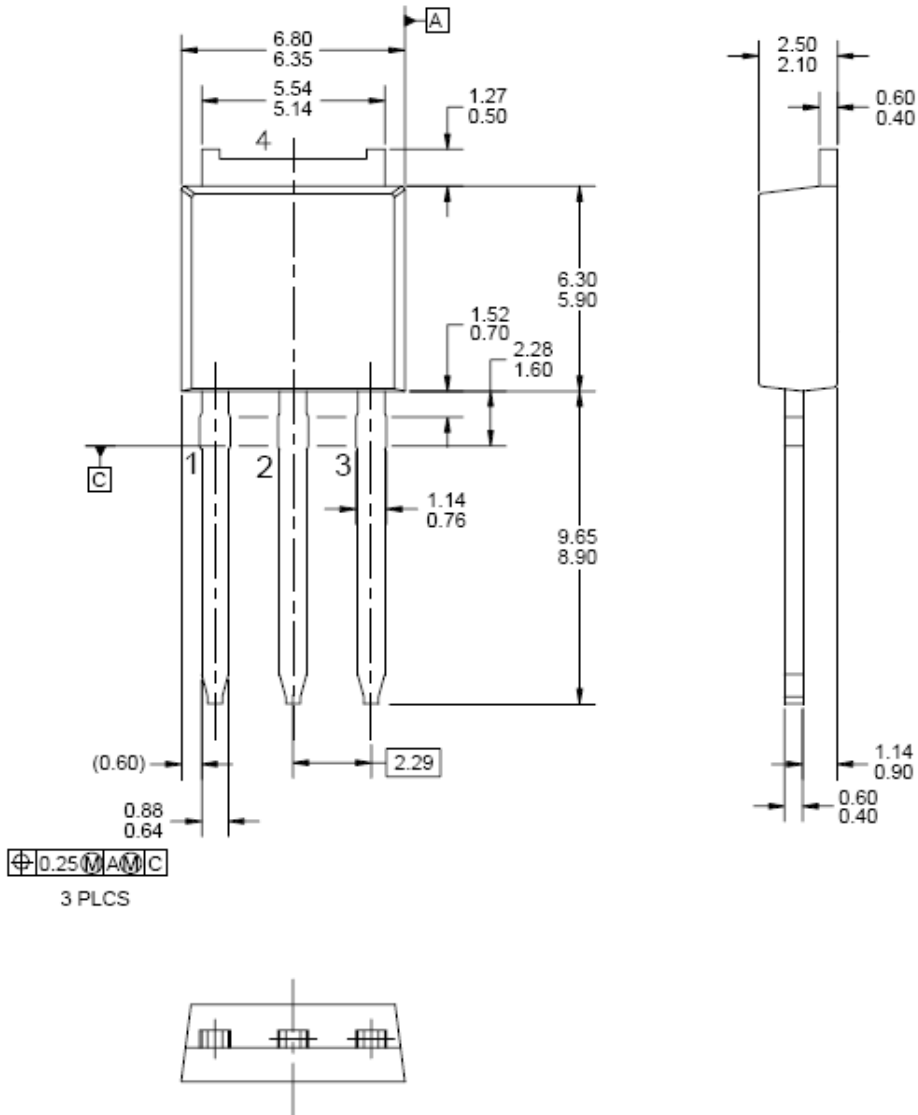
D - PAK



Dimensions in Millimeters

Mechanical Dimensions

I - PAK






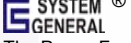


Dimensions in Millimeters



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