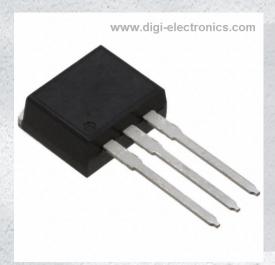


FQI8N60CTU Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number FQI8N60CTU-DG

Manufacturer onsemi

Manufacturer Product Number FQI8N60CTU

Description MOSFET N-CH 600V 7.5A I2PAK

Detailed Description N-Channel 600 V 7.5A (Tc) 3.13W (Ta), 147W (Tc) Th

rough Hole TO-262 (I2PAK)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
FQI8N60CTU	onsemi
Series:	Product Status:
QFET®	Obsolete
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
600 V	7.5A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
10V	1.20hm @ 3.75A, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
4V @ 250μA	36 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±30V	1255 pF @ 25 V
FET Feature:	Power Dissipation (Max):
	3.13W (Ta), 147W (Tc)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Through Hole
Supplier Device Package:	Package / Case:
TO-262 (I2PAK)	TO-262-3 Long Leads, I2PAK, TO-262AA
Base Product Number:	
FOI8N60	

Environmental & Export classification

8541.29.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	Not Applicable
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

ON Semiconductor

Is Now



To learn more about onsemi[™], please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,



ON Semiconductor®

FQB8N60C / FQI8N60C

N-Channel QFET® MOSFET

600 V, 7.5 A, 1.2 Ω

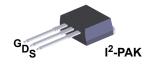
Description

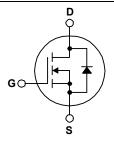
This N-Channel enhancement mode power MOSFET is • Low Gate Charge (Typ. 28 nC) produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state • 100% Avalanche Tested resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 7.5 A, 600 V, $R_{DS(on)}$ = 1.2 Ω (Max.) @ V_{GS} = 10 V, $I_D = 3.75 A$
- Low Crss (Typ. 12 pF)
- · RoHS Compliant







Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQB8N60CTM / FQI8N60CTU	Unit
V _{DSS}	Drain-Source Voltage		600	V
I _D	Drain Current - Continuous (T _C = 25°C)		7.5	Α
	- Continuous (T _C = 100°C)		4.6	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	30	Α
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	230	mJ
I _{AR}	Avalanche Current	(Note 1)	7.5	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	14.7	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
	Power Dissipation (T _A = 25°C)*		3.13	W
P_D	Power Dissipation (T _C = 25°C)		147	W
	- Derate above 25°C		1.18	W/°C
T_J , T_{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds.		300	°C

Thermal Characteristics

Symbol	Parameter	FQB8N60CTM / FQI8N60CTU	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.85	
В	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	40	1

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQB8N60CTM	FQB8N60C	D ² -PAK	Tape and Reel	330 mm	24 mm	800 units
FQI8N60CTU	FQI8N60C	I ² -PAK	Tube	N/A	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	rest Conditions	WIII.	ıyp.	wax.	Unit
Off Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	600			V
ΔBV_{DSS} / ΔT_{J}	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.7		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V			1	μΑ
	Zero Gate voltage Drain Current	V _{DS} = 480 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 3.75 \text{ A}$		1.0	1.2	Ω
9FS	Forward Transconductance	$V_{DS} = 40 \text{ V}, I_{D} = 3.75 \text{ A}$		8.7		S

Dynamic Characteristics

C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	 965	1255	pF
Coss	Output Capacitance	f = 1.0 MHz	 105	135	pF
C _{rss}	Reverse Transfer Capacitance		 12	16	pF

Switching Characteristics

t _{d(on)}	Turn-On Delay Time	V _{DD} = 300 V, I _D = 7.5A,	 16.5	45	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$	 60.5	130	ns
t _{d(off)}	Turn-Off Delay Time		 81	170	ns
t _f	Turn-Off Fall Time	(Note 4)	 64.5	140	ns
Q_g	Total Gate Charge	V _{DS} = 480 V, I _D = 7.5A,	 28	36	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V	 4.5		nC
Q _{gd}	Gate-Drain Charge	(Note 4)	 12		nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current		 	7.5	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		 	30	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 7.5 \text{ A}$	 	1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{S} = 7.5 \text{ A},$	 365	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$	 3.4		μС

Notes: 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2.L = 7.3 mH, I_{AS} = 7.5 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C. 3. $I_{SD} \le 7.5$ A, di/dt ≤ 200 A/ μ s , $V_{DD} \le BV_{DSS}$, starting T_{J} = 25°C. 4. Essentially independent of operating temperature.

Typical Characteristics

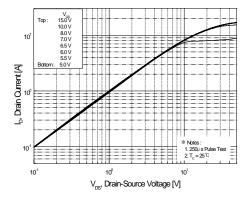


Figure 1. On-Region Characteristics

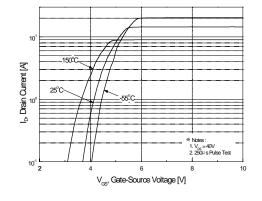


Figure 2. Transfer Characteristics

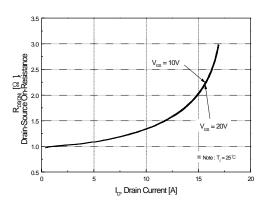


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

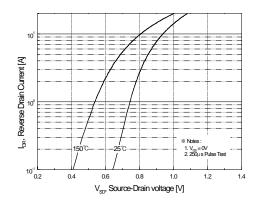


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

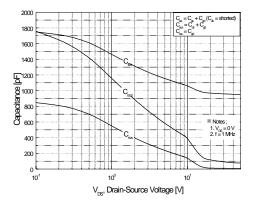


Figure 5. Capacitance Characteristics

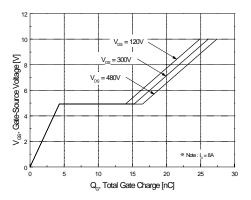
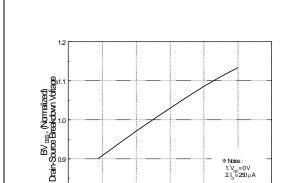


Figure 6. Gate Charge Characteristics



Typical Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs Temperature

T_,, Junction Temperature [°C]

150

200

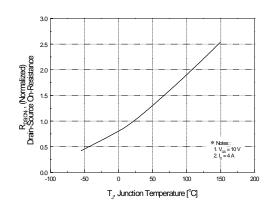


Figure 8. On-Resistance Variation vs Temperature

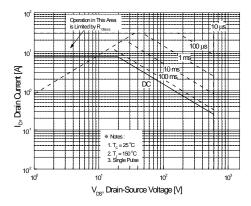


Figure 9. Maximum Safe Operating Area

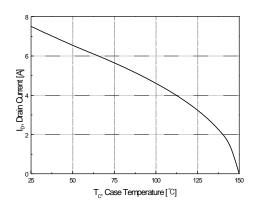


Figure 10. Maximum Drain Current vs Case Temperature

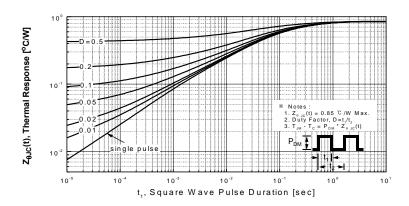


Figure 11. Transient Thermal Response Curve

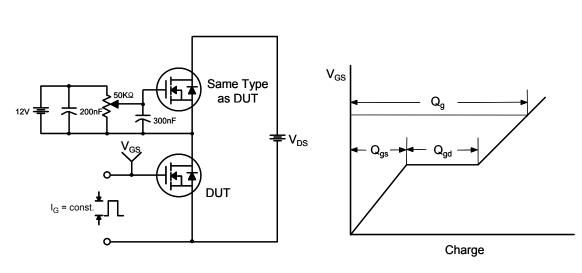


Figure 12. Gate Charge Test Circuit & Waveform

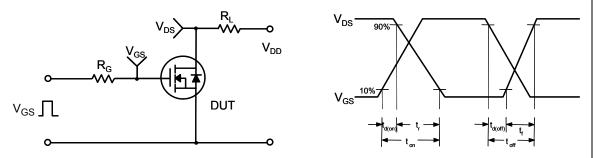


Figure 13. Resistive Switching Test Circuit & Waveforms

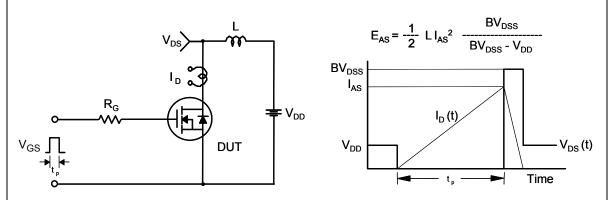
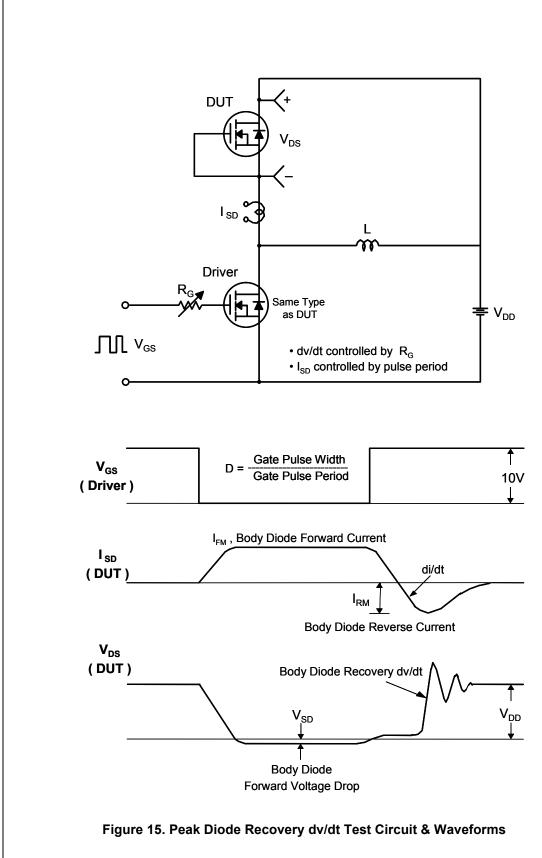


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

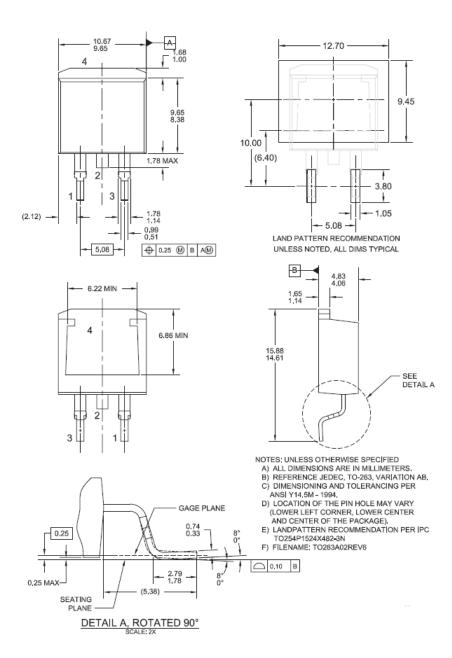
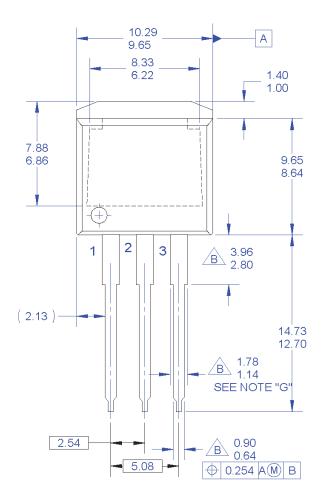
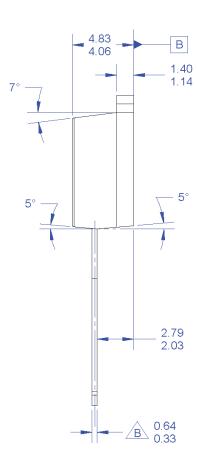


Figure 16. TO263 (D²PAK), Molded, 2-Lead, Surface Mount

Package drawings are provided as a service to customers considering ON Semiconductor components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a ON Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of ON Semiconductor's worldwide terms and conditions, specif-ically the warranty therein, which covers ON Semiconductor products.

Mechanical Dimensions





NOTES:

- A. EXCEPT WHERE NOTED CONFORMS TO
 T0262 JEDEC VARIATION AA.
 B DOES NOT COMPLY JEDEC STD. VALUE.
 C. ALL DIMENSIONS ARE IN MILLIMETERS.
 D. DIMENSIONS ARE EXCLUSIVE OF BURRS,
 MOLD FLASH AND THE BAR PROTRUSIONS.
- E. DIMENSION AND TOLERANCE AS PER ANSI
- F. LOCATION OF PIN HOLE MAY VARY
 (LOWER LEFT CORNER, LOWER CENTER
 AND CENTER OF PACKAGE)
 G. MAXIMUM WIDTH FOR F102 DEVICE = 1.35 MAX.
 H. DRAWING FILE NAME: TO262A03REV5

Figure 17. TO262 (I²PAK), Molded, 3-Lead, Jedec Variation AA

Package drawings are provided as a service to customers considering ON Semiconductor components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a ON Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of ON Semiconductor's worldwide terms and conditions, specif-ically the warranty therein, which covers ON Semiconductor products.

ON Semiconductor and III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages.

Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative



OUR CERTIFICATE

DiGi provide top-quality products and perfect service for customer worldwide through standardization, technological innovation and continuous improvement. DiGi through third-party certification, we striciy control the quality of products and services. Welcome your RFQ to Email: Info@DiGi-Electronics.com

















Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com