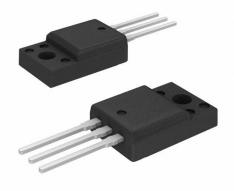


# **FQPF10N60CT Datasheet**

www.digi-electronics.com



DiGi Electronics Part Number

Manufacturer

Manufacturer Product Number

Carl State Contraction Contraction

Description MO

**Detailed Description** 

FQPF10N60CT-DG

onsemi

FQPF10N60CT

MOSFET N-CH 600V 9.5A TO220F

N-Channel 600 V 9.5A (Tc) 50W (Tc) Through Hole T O-220F-3

https://www.DiGi-Electronics.com



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



## Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
FQPF10N60CT	onsemi
Series:	Product Status:
QFET <sup>®</sup>	Obsolete
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (ld) @ 25°C:
600 V	9.5A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ ld, Vgs:
10V	730mOhm @ 4.75A, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
4V @ 250μΑ	57 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±30V	2040 pF @ 25 V
FET Feature:	Power Dissipation (Max):
	50W (Tc)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Through Hole
Supplier Device Package:	Package / Case:
TO-220F-3	TO-220-3 Full Pack
Base Product Number:	
FQPF1	

## **Environmental & Export classification**

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
EAR99	8541.29.0095



Is Now Part of



## **ON Semiconductor**®

# To learn more about ON Semiconductor, please visit our website at <u>www.onsemi.com</u>

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="https://www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to <a href="https://www.onsemi.com">Fairchild\_questions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized applications, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an equif prese



## FQP10N60C / FQPF10N60C N-Channel QFET<sup>®</sup> MOSFET

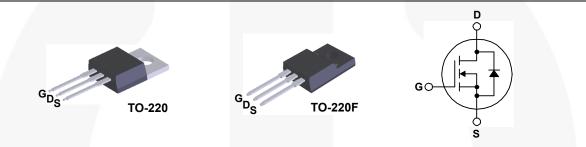
600 V, 9.5 A, 730 mΩ

### Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology. This advanced technology has been especially tailored to mini-mize on-state resistance, provide superior switching perfor-mance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

#### Features

- 9.5 A, 600 V,  $R_{DS(on)}$  = 730 m $\Omega$  (Max.) @  $V_{GS}$  = 10 V,  $I_{D}$  = 4.75 A
- Low Gate Charge (Typ. 44 nC)
- Low Crss (Typ. 18 pF)
- 100% Avalanche Tested



#### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FQP10N60C	FQPF10N60C	Unit
V <sub>DSS</sub>	Drain-Source Voltage		6	600	
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =	= 25°C)	9.5	9.5 *	А
	- Continuous (T <sub>C</sub> =	= 100°C)	5.7	5.7 *	А
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	38	38 *	А
V <sub>GSS</sub>	Gate-Source Voltage		± 30		V
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	700		mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	9.5		А
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	15.6		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns
P <sub>D</sub>	Power Dissipation ( $T_C = 25^{\circ}C$ )		156	50	W
- Derate above 25°C		°C	1.25	0.4	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150		°C
Τ <sub>L</sub>	Maximum lead temperature for soldering, 1/8" from case for 5 seconds		3	00	°C

\* Drain current limited by maximum junction temperature.

### **Thermal Characteristics**

Symbol	Parameter	FQP10N60C	FQPF10N60C	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.8	2.5	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5		°C/W
$R_{\thetaJA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	°C/W

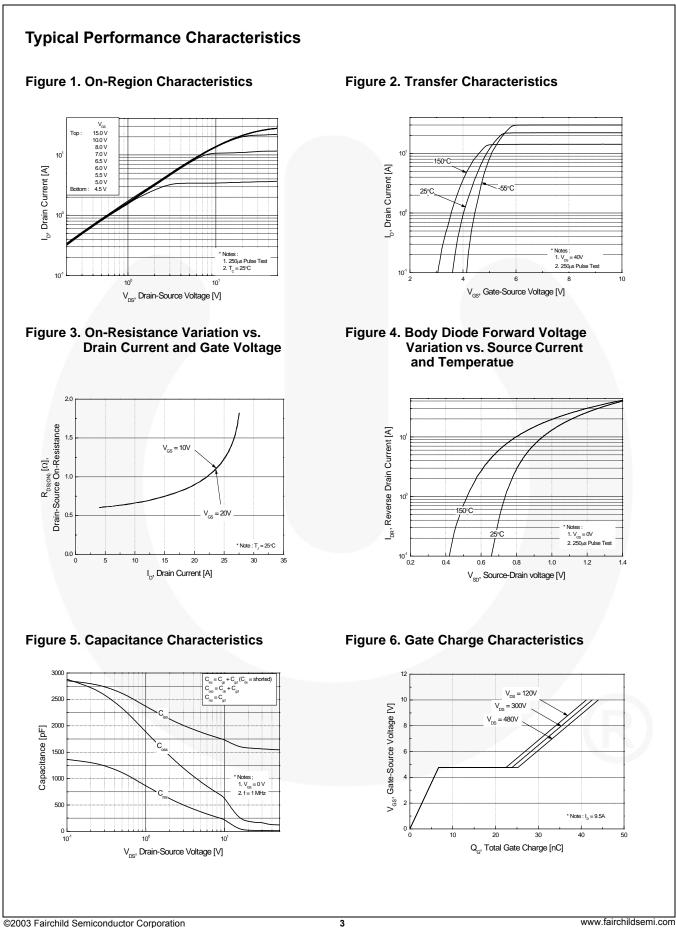
November 2013

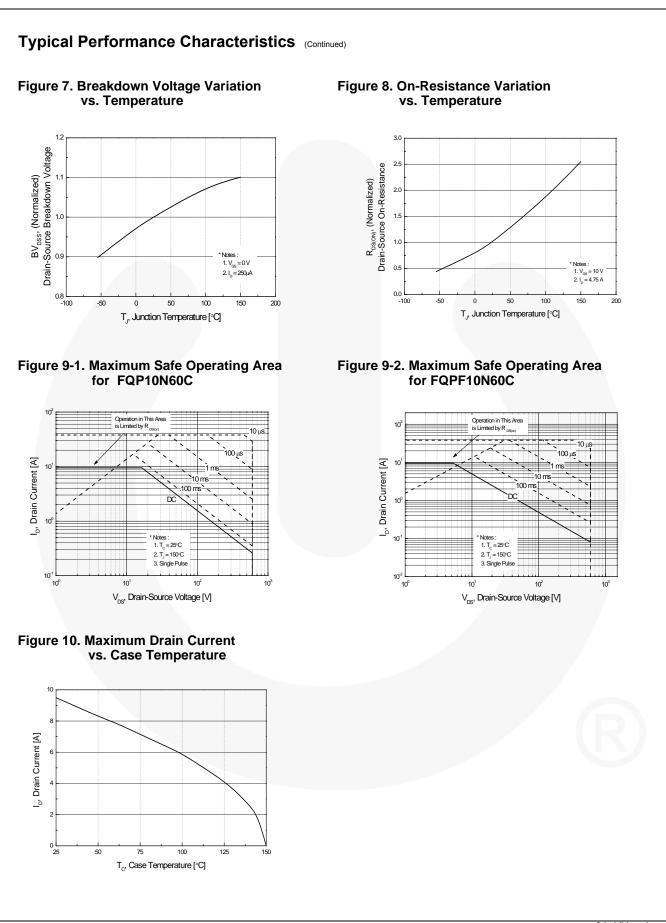
FQP10N60C     FQP10N60C     TC       FQPF10N60C     FQPF10N60C     TO			Package	• •			Tape Wig		Quantity 50 units	
			TO-220	Tube	N/A		N/A			
		TO-220F TO-220F				N/A N/A		50 units 50 units		
FQFF10N60		FQPF10N60CT FQPF10N60C	TO-220F				N/A N/A		50 units	
						<u> </u>				
Symbol	Cilara	Cteristics T <sub>C</sub> = 25°C	unless otherwi	Test Conditions	5	Min	Тур	Мах	Uni	
-							,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
Off Characte			V	- 0 )/   - 250 (		600			V	
BV <sub>DSS</sub>		urce Breakdown Voltage		<sub>3</sub> = 0 V, I <sub>D</sub> = 250 μA 250 μA, Referenced to	25°C		0.7		V/°C	
ΔΒV <sub>DSS</sub> / ΔΤ <sub>J</sub>	Coefficie	vn Voltage Temperature nt	'D -	250 µA, Relefenced to	25 0		0.7		v/ C	
I <sub>DSS</sub>	Zero Gat	Zero Gate Voltage Drain Current		= 600 V, V <sub>GS</sub> = 0 V				1	μA	
	6		V <sub>DS</sub>	= 480 V, T <sub>C</sub> = 125°C				10	μA	
I <sub>GSSF</sub>	Gate-Boo	ly Leakage Current, Forw	ard V <sub>GS</sub>	s = 30 V, V <sub>DS</sub> = 0 V				100	nA	
I <sub>GSSR</sub>	Gate-Boo	dy Leakage Current, Reve		$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$				-100	nA	
On Characte	ristics									
V <sub>GS(th)</sub>	Gate Threshold Voltage			V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		2.0		4.0	V	
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance			$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 4.75 \text{ A}$			0.6	0.73	Ω	
9 <sub>FS</sub>	Forward Transconductance		V <sub>DS</sub>	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 4.75 A			8.0		S	
Dynamic Cha	ractoristi	<u></u>								
C <sub>iss</sub>	C Characteristics		Vns	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,			1570	2040	pF	
C <sub>oss</sub>		apacitance		f = 1.0 MHz			166	215	pF	
C <sub>rss</sub>		Transfer Capacitance					18	24	pF	
Switching Ch		Delay Time	V	- 300 \/   0.54			23	55	ns	
t <sub>d(on)</sub> t <sub>r</sub>		Rise Time		$V_{DD}$ = 300 V, $I_D$ = 9.5A, R <sub>G</sub> = 25 $\Omega$			69	150	ns	
		Delay Time			F		144	300	ns	
t <sub>d(off)</sub> t <sub>f</sub>		Fall Time			(Note 4)		77	165	ns	
ч Q <sub>g</sub>		e Charge	Vpa	, = 480 V, I <sub>D</sub> = 9.5A,			44	57	nC	
Q <sub>gs</sub>		urce Charge	V <sub>GS</sub>	s = 10 V	-		6.7		nC	
Q <sub>gd</sub>		in Charge			(Note 4)		18.5		nC	
	1	aracteristics and Maxim		-						
I <sub>S</sub>	Maximum Continuous Drain-Source Die							9.5	A	
I <sub>SM</sub>		n Pulsed Drain-Source Die						38	A	
V <sub>SD</sub>		urce Diode Forward Volta	0 00	<sub>s</sub> = 0 V, I <sub>S</sub> = 9.5 A				1.4	V	
t <sub>rr</sub>		Recovery Time		s = 0 V, I <sub>S</sub> = 9.5 A, / dt = 100 A/us	F		420		ns	
Q <sub>rr</sub>	Reverse	Recovery Charge	uF	dl <sub>F</sub> / dt = 100 A/μs			4.2		μC	

2. L = 14.2 mH, I\_{AS} = 9.5 A, V\_DD = 50 V, R\_G = 25  $\Omega,$  starting T\_J = 25°C.

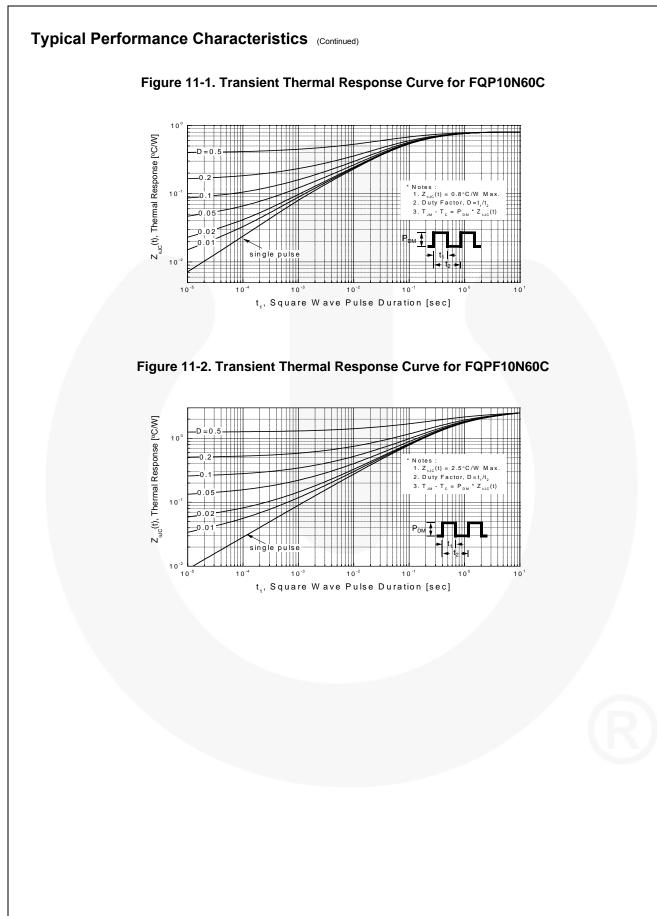
3. I\_{SD} \leq 9.5 A, di/dt  $\leq$  200 A/µs, V\_{DD}  $\leq$  BV\_{DSS}, starting T\_J = 25°C.

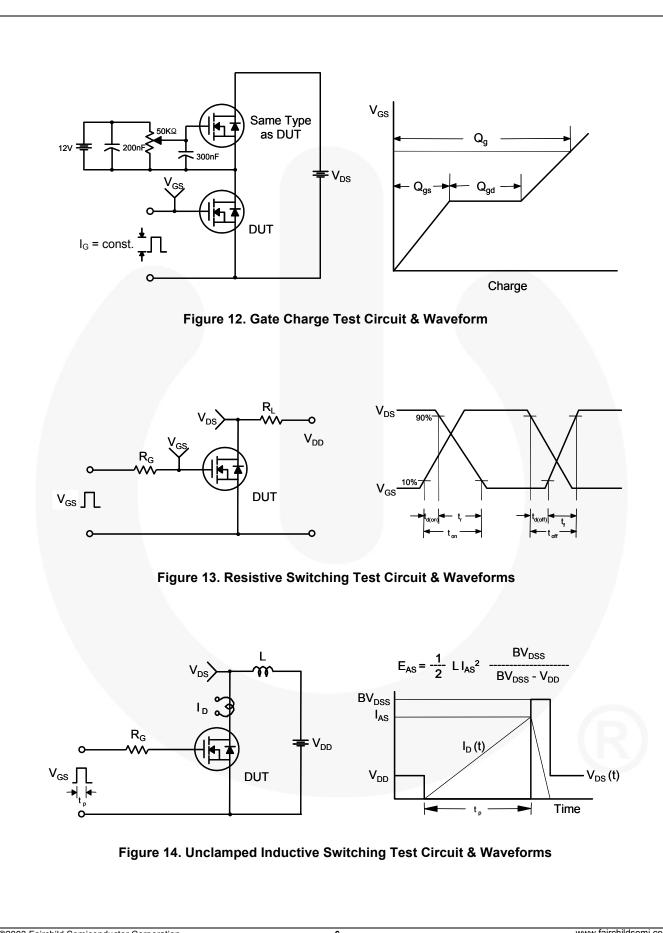
4. Essentially independent of operating temperature typical characteristics.





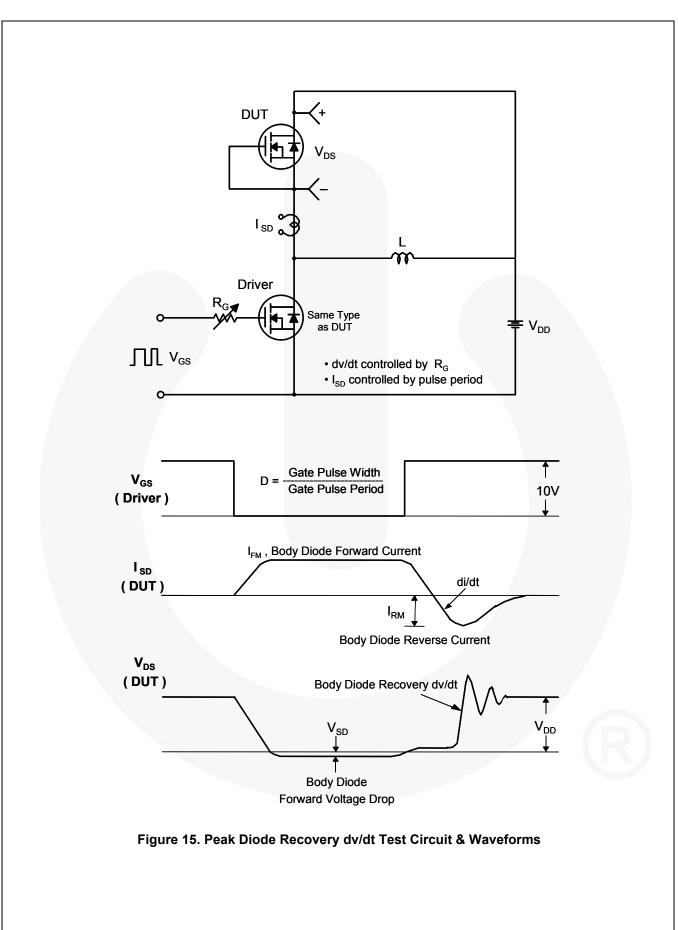
4





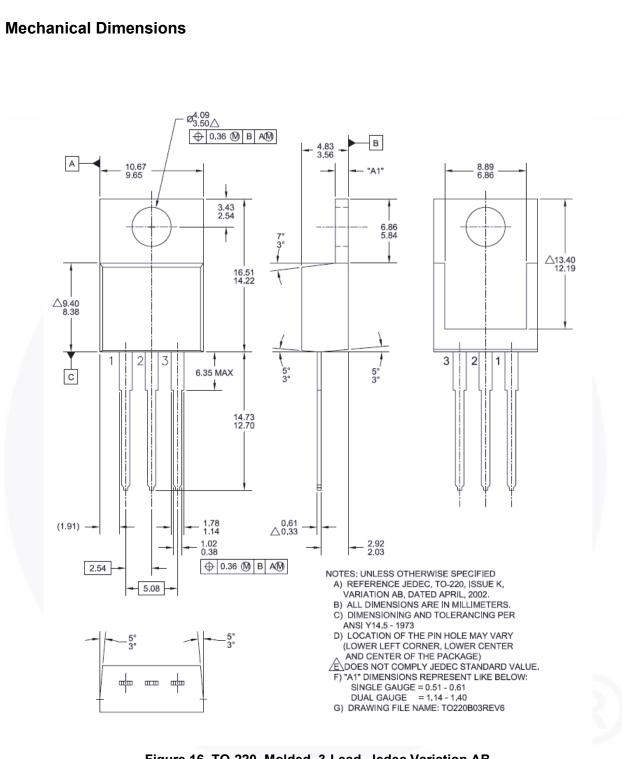
FQP10N60C / FQPF10N60C — N-Channel QFET® MOSFET

6



7

FQP10N60C / FQPF10N60C — N-Channel QFET® MOSFET

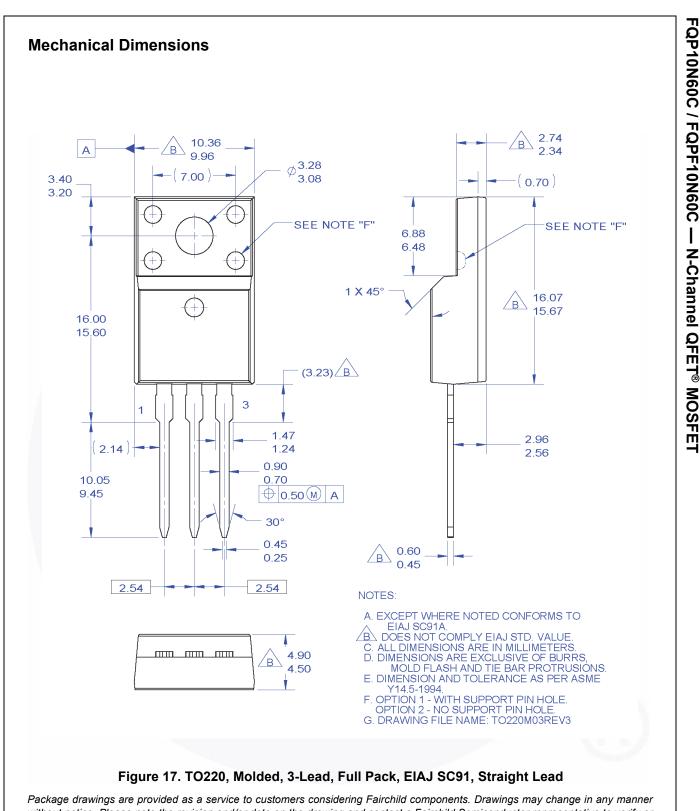


#### Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

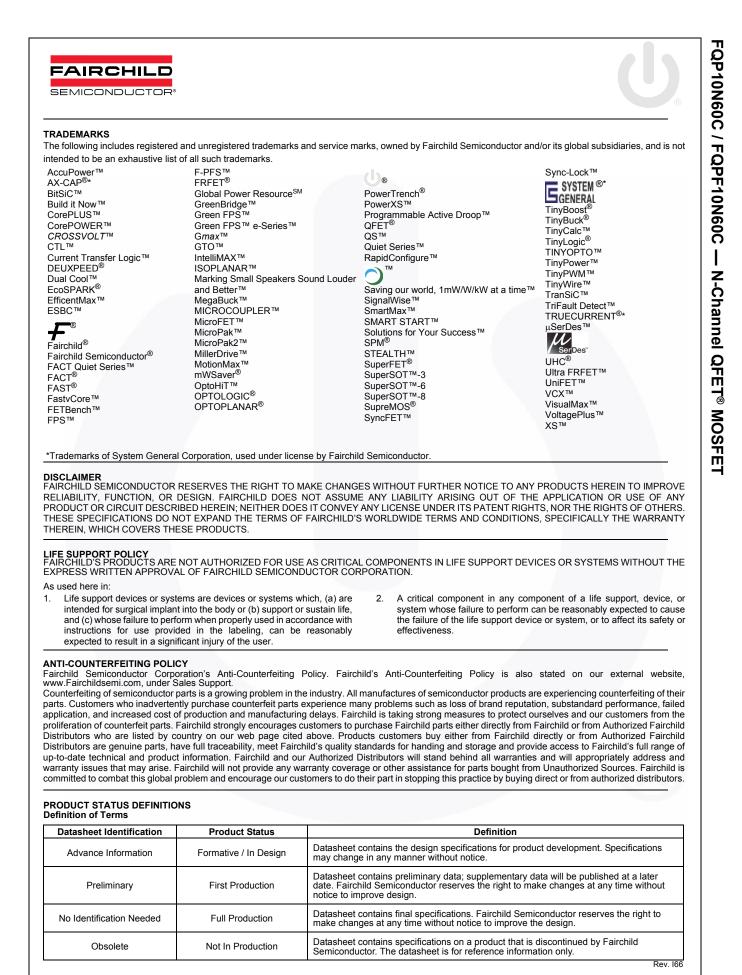
http://www.fairchildsemi.com/package/packageDetails.html?id=PN\_TT220-003



without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN\_TF220-003



ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights or the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor haves have, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application. Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all c

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC



## **OUR CERTIFICATE**

DiGi provide top-quality products and perfect service for customer worldwide through standardization, technological innovation and continuous improvement. DiGi through third-party certification, we striciy control the quality of products and services. Welcome your RFQ to Email: Info@DiGi-Electronics.com

	<section-header></section-header>		
Marginary Marginary   Marginary	Market	Marchine Marchine Image: Control of the sector of the sec	





Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.