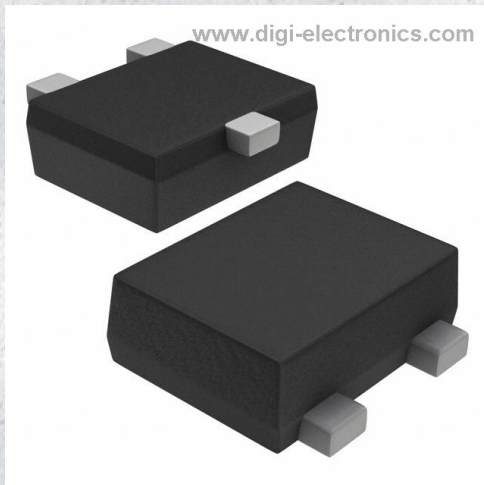


MCH3481-TL-W Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	MCH3481-TL-W-DG
Manufacturer	onsemi
Manufacturer Product Number	MCH3481-TL-W
Description	MOSFET N-CH 20V 2A SC70FL/MCPH3
Detailed Description	N-Channel 20 V 2A (Ta) 800mW (Ta) Surface Mount SC-70FL/MCPH3



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

MCH3481-TL-W

Series:

-

FET Type:

N-Channel

Drain to Source Voltage (Vdss):

20 V

Drive Voltage (Max Rds On, Min Rds On):

1.2V, 4.5V

Vgs(th) (Max) @ Id:

900mV @ 1mA

Vgs (Max):

±9V

FET Feature:

-

Operating Temperature:

150°C (TJ)

Supplier Device Package:

SC-70FL/MCPH3

Base Product Number:

MCH3481

Manufacturer:

onsemi

Product Status:

Obsolete

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

2A (Ta)

Rds On (Max) @ Id, Vgs:

104mOhm @ 1A, 4.5V

Gate Charge (Qg) (Max) @ Vgs:

2.9 nC @ 4.5 V

Input Capacitance (Ciss) (Max) @ Vds:

175 pF @ 10 V

Power Dissipation (Max):

800mW (Ta)

Mounting Type:

Surface Mount

Package / Case:

3-SMD, Flat Lead

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.21.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

MCH3481

Power MOSFET 20V, 104mΩ, 2A, Single N-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

Features

- Low On-Resistance
- 1.2V drive
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

- Load Switch

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V _{DSS}	20	V
Gate to Source Voltage	V _{GSS}	±9	V
Drain Current (DC)	I _D	2	A
Drain Current (Pulse) PW ≤ 10μs, duty cycle ≤ 1%	I _{DP}	8	A
Power Dissipation When mounted on ceramic substrate (900mm ² × 0.8mm)	P _D	0.8	W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm ² × 0.8mm)	R _{θJA}	156.2	°C/W

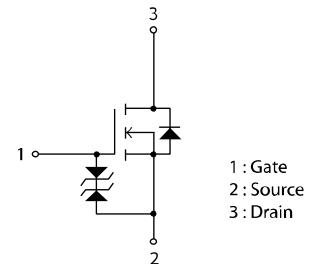


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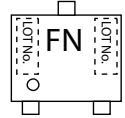
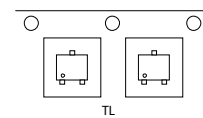
V _{DSS}	R _{DS(on)} Max	I _D Max
20V	104mΩ@ 4.5V	2A
	147mΩ@ 2.5V	
	203mΩ@ 1.8V	
	540mΩ@ 1.2V	

ELECTRICAL CONNECTION N-Channel



PACKING TYPE : TL

MARKING



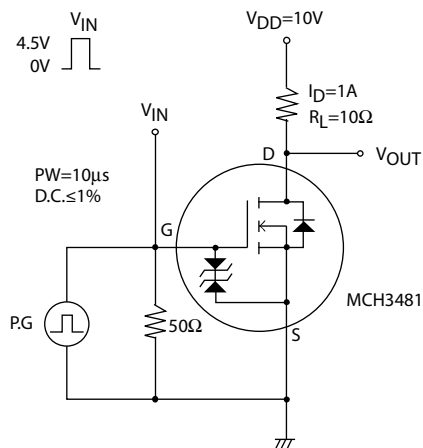
ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

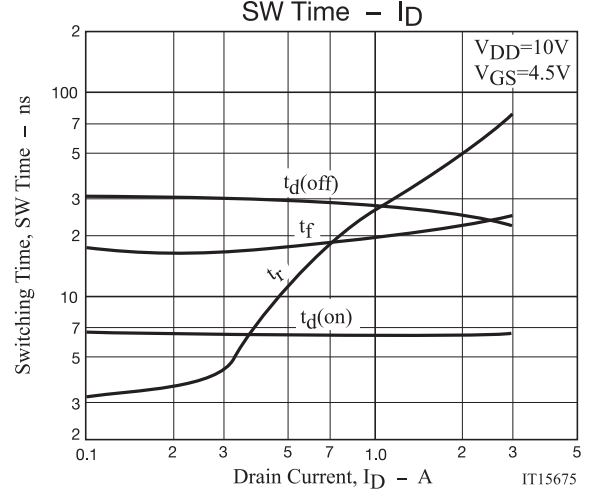
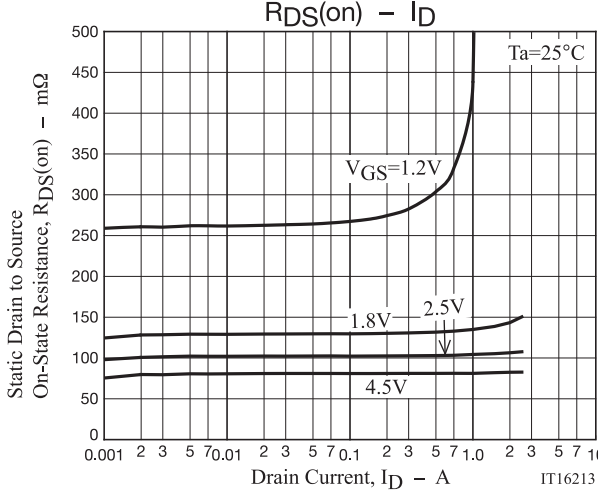
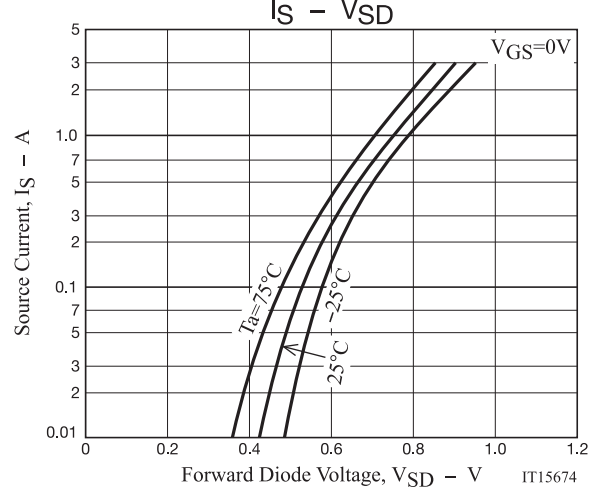
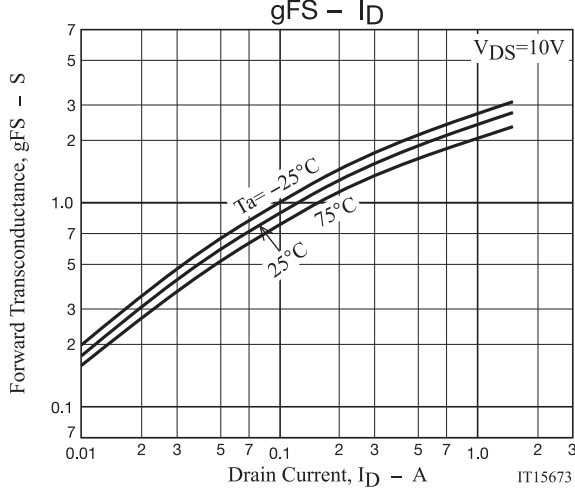
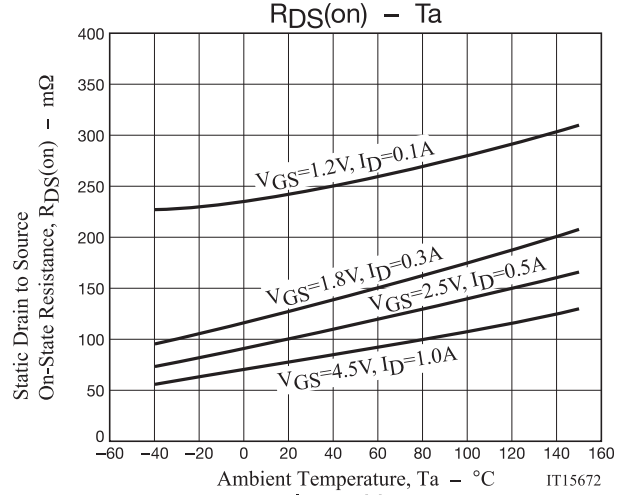
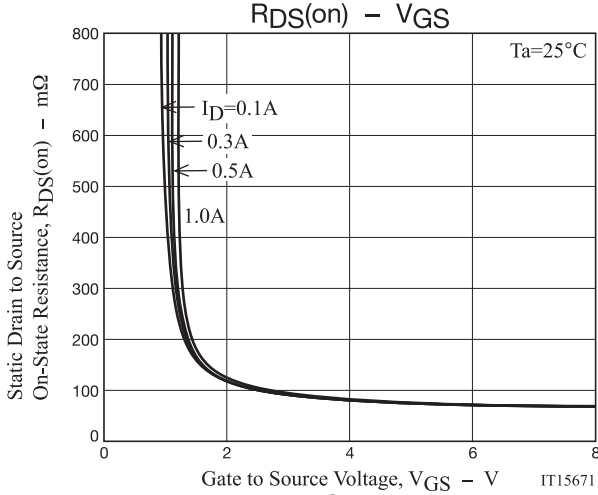
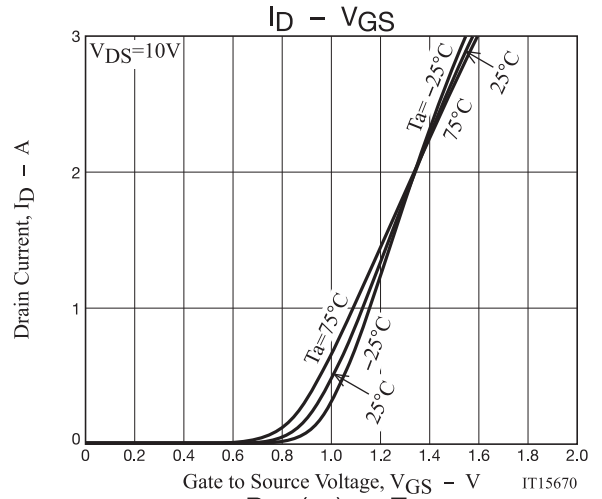
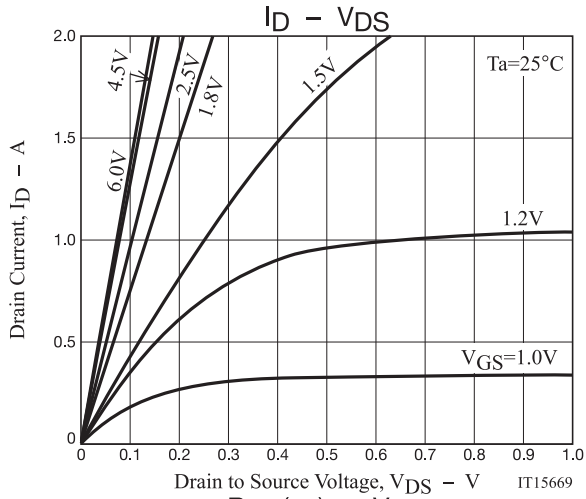
MCH3481**ELECTRICAL CHARACTERISTICS** at $T_a = 25^\circ\text{C}$ (Note 2)

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}, V_{GS}=0\text{V}$	20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$			1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS}=\pm 7.2\text{V}, V_{DS}=0\text{V}$			± 10	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	0.3		0.9	V
Forward Transconductance	g_{FS}	$V_{DS}=10\text{V}, I_D=1\text{A}$		2.4		S
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D=1\text{A}, V_{GS}=4.5\text{V}$		80	104	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=0.5\text{A}, V_{GS}=2.5\text{V}$		105	147	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D=0.3\text{A}, V_{GS}=1.8\text{V}$		135	203	$\text{m}\Omega$
	$R_{DS(on)4}$	$I_D=0.1\text{A}, V_{GS}=1.2\text{V}$		270	540	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=10\text{V}, f=1\text{MHz}$		175		pF
Output Capacitance	C_{oss}			30		pF
Reverse Transfer Capacitance	C_{rss}			25		pF
Turn-ON Delay Time	$t_{d(on)}$		See specified Test Circuit		6.6	
Rise Time	t_r			27		ns
Turn-OFF Delay Time	$t_{d(off)}$			28		ns
Fall Time	t_f			19		ns
Total Gate Charge	Q_g	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=2\text{A}$			2.9	
Gate to Source Charge	Q_{gs}			0.46		nC
Gate to Drain "Miller" Charge	Q_{gd}			0.53		nC
Forward Diode Voltage	V_{SD}	$I_S=2\text{A}, V_{GS}=0\text{V}$		0.85	1.2	V

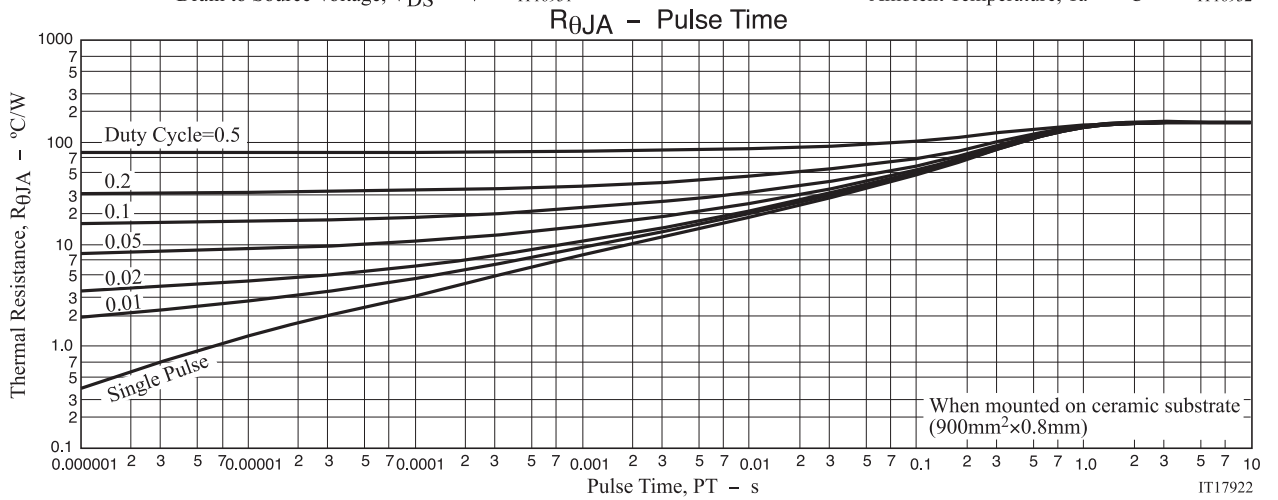
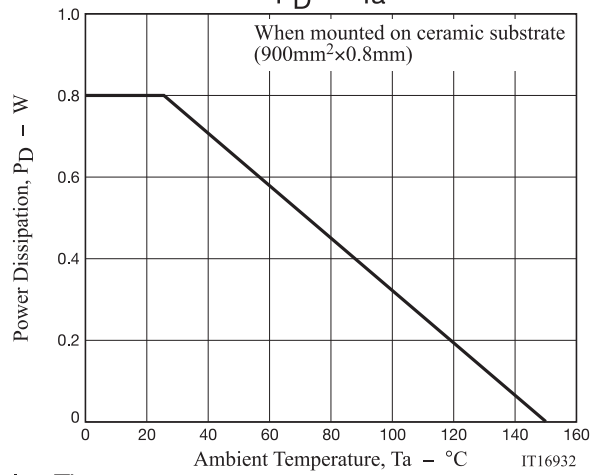
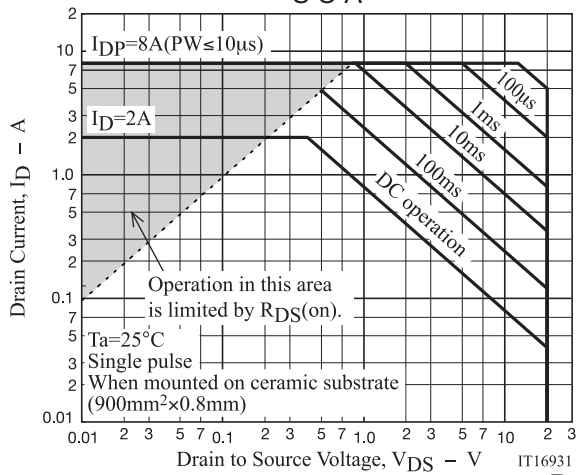
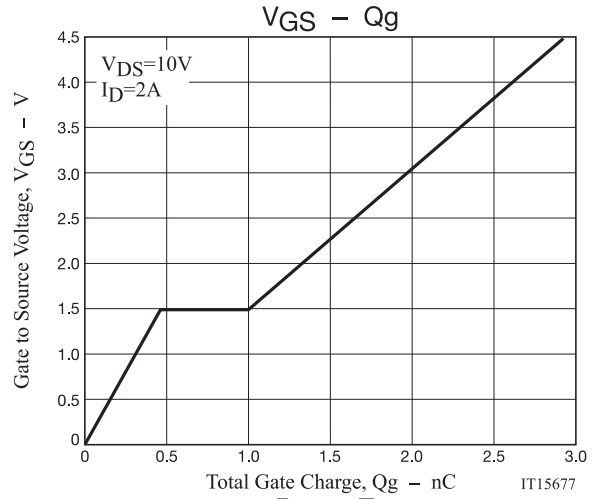
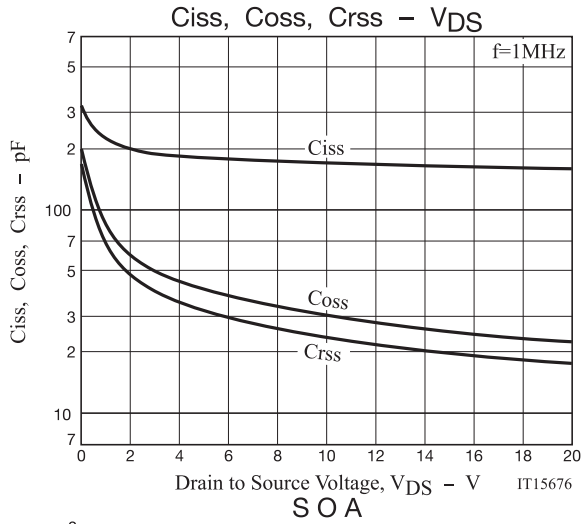
Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit

MCH3481



MCH3481

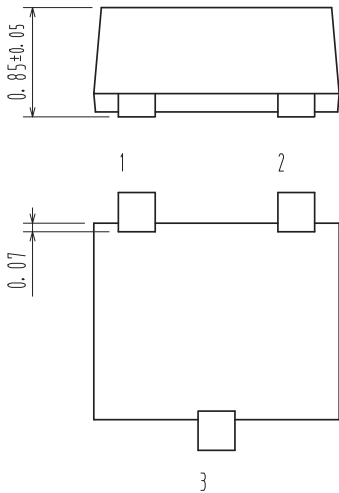
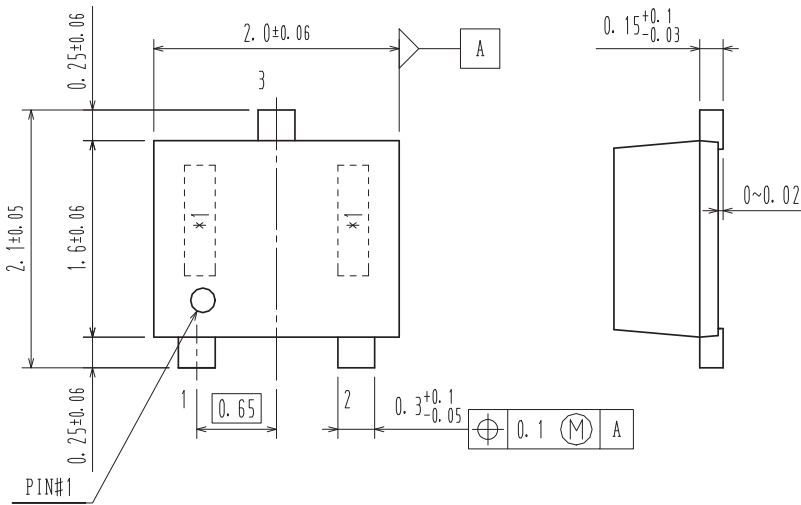


MCH3481

PACKAGE DIMENSIONS

unit : mm

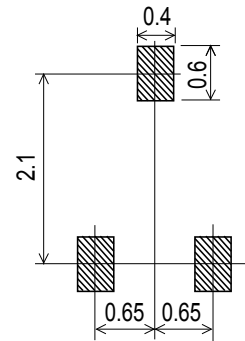
SC-70FL / MCPH3
CASE 419AQ
ISSUE O



*1: Lot indication

- 1 : Gate
- 2 : Source
- 3 : Drain

Recommended Soldering Footprint



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
MCH3481-TL-H	FN	SC-70FL / MCPH3 (Pb-Free / Halogen Free)	3,000 / Tape & Reel
MCH3481-TL-W			

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the MCH3481 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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