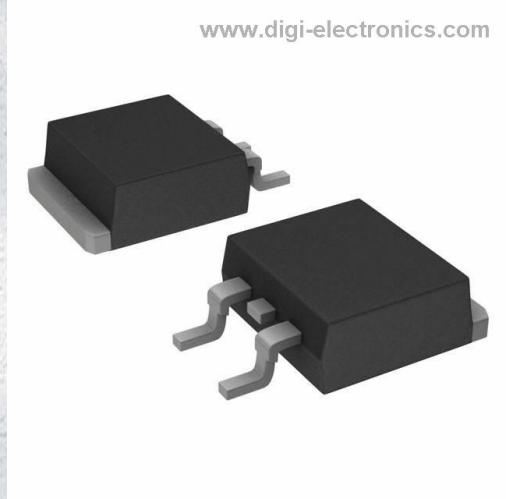
MJB5742T4G Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number	MJB5742T4G-DG
Manufacturer	onsemi
Manufacturer Product Number	MJB5742T4G
Description	TRANS NPN DARL 400V 8A D2PAK
Detailed Description	Bipolar (BJT) Transistor NPN - Darlington 400 V 8 A 100 W Surface Mount D2PAK

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
MJB5742T4G	onsemi
Series:	Product Status:
-	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN - Darlington	8 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ Ib, Ic:
400 V	3V @ 400mA, 8A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
-	200 @ 4A, 5V
Power - Max:	Frequency - Transition:
100 W	-
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
TO-263-3, D2PAK (2 Leads + Tab), TO-263AB	D2PAK
Base Product Number:	
MJB5742	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	

onsemi

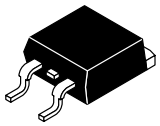
DATA SHEET
www.onsemi.com

NPN Silicon Power Darlington Transistors

MJB5742T4G

POWER DARLINGTON TRANSISTORS
8 AMPERES, 400 VOLTS
100 WATTS

The Darlington transistors are designed for high−voltage power switching in inductive circuits.



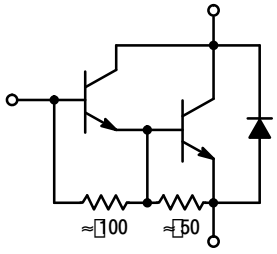
D²PAK
CASE 418B
STYLE 1

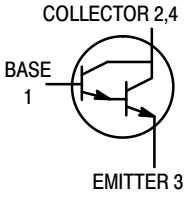
Features

- These Devices are Pb−Free and are RoHS Compliant

Applications

- Small Engine Ignition
- Switching Regulators
- Inverters
- Solenoid and Relay Drivers
- Motor Controls





MAXIMUM RATINGS

Symbol	Rating	Value	Unit
$V_{CEO(sus)}$	Collector−Emitter Voltage	400	Vdc
V_{CEV}	Collector−Emitter Voltage	800	Vdc
V_{EB}	Emitter−Base Voltage	8	Vdc
I_C I_{CM}	Collector Current − Continuous − Peak (Note 1)	8 16	Adc
I_B I_{BM}	Base Current − Continuous − Peak (Note 1)	2.5 5	Adc
P_D	Total Device Dissipation @ T_A = 25°C Derate above 25°C	2 0.016	W W/°C
P_D	Total Device Dissipation @ T_C = 25°C Derate above 25°C	100 0.8	W W/°C
T_J, T_{stg}	Operating and Storage Junction Temperature Range	−65 to +150	°C

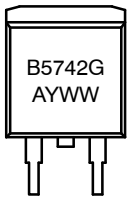
THERMAL CHARACTERISTICS

Symbol	Characteristics	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction−to−Case	1.25	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction−to−Ambient	62.5	°C/W
T_L	Maximum Lead Temperature for Soldering Purposes 1/8″ from Case for 5 Seconds	275	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Pulse Test: Pulse Width = 5 ms, Duty Cycle ≤ 10%.

MARKING DIAGRAM



B5742 = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
G = Pb−Free Package

ORDERING INFORMATION

Device	Package	Shipping†
MJB5742T4G	D²PAK (Pb−Free)	800 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MJB5742T4G

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Characteristic	Min	Typ	Max	Unit
OFF CHARACTERISTICS (Note 2)					
$V_{CEO(sus)}$	Collector−Emitter Sustaining Voltage (I_C = 50 mA, I_B = 0)	400	–	–	Vdc
I_{CEV}	Collector Cutoff Current (V_{CEV} = Rated Value, $V_{BE(off)}$ = 1.5 Vdc) (V_{CEV} = Rated Value, $V_{BE(off)}$ = 1.5 Vdc, T_C = 100°C)	– –	– –	1 5	mAdc
I_{EBO}	Emitter Cutoff Current (V_{EB} = 8 Vdc, I_C = 0)	–	–	75	mAdc
SECOND BREAKDOWN					
$I_{S/b}$	Second Breakdown Collector Current with Base Forward Biased	See Figure 6			
RBSOA	Clamped Inductive SOA with Base Reverse Biased	See Figure 7			
ON CHARACTERISTICS (Note 2)					
h_{FE}	DC Current Gain (I_C = 0.5 Adc, V_{CE} = 5 Vdc) (I_C = 4 Adc, V_{CE} = 5 Vdc)	50 200	100 400	– –	–
$V_{CE(sat)}$	Collector−Emitter Saturation Voltage (I_C = 4 Adc, I_B = 0.2 Adc) (I_C = 8 Adc, I_B = 0.4 Adc) (I_C = 4 Adc, I_B = 0.2 Adc, T_C = 100°C)	– – –	– – –	2 3 2.2	Vdc
$V_{BE(sat)}$	Base−Emitter Saturation Voltage (I_C = 4 Adc, I_B = 0.2 Adc) (I_C = 8 Adc, I_B = 0.4 Adc) (I_C = 4 Adc, I_B = 0.2 Adc, T_C = 100°C)	– – –	– – –	2.5 3.5 2.4	Vdc
V_f	Diode Forward Voltage (Note 3) (I_F = 5 Adc)	–	–	2.5	Vdc
SWITCHING CHARACTERISTICS					
Typical Resistive Load (Table 1)					
t_d	Delay Time	(V_{CC} = 250 Vdc, $I_{C(pk)}$ = 6 A I_{B1} = I_{B2} = 0.25 A, t_p = 25 μs, Duty Cycle ≤ 1%)	–	0.04	–	μs
t_r	Rise Time		–	0.5	–	μs
t_s	Storage Time		–	8	–	μs
t_f	Fall Time		–	2	–	μs
Inductive Load, Clamped (Table 1)						
t_{sv}	Voltage Storage Time	($I_{C(pk)}$ = 6 A, $V_{CE(pk)}$ = 250 Vdc I_{B1} = 0.06 A, $V_{BE(off)}$ = 5 Vdc)	–	4	–	μs
t_c	Crossover Time		–	2	–	μs

2. Pulse Test: Pulse Width 300 μs, Duty Cycle = 2%.
3. The internal Collector−to−Emitter diode can eliminate the need for an external diode to clamp inductive loads. Tests have shown that the Forward Recovery Voltage (V_f) of this diode is comparable to that of typical fast recovery rectifiers.

MJB5742T4G

TYPICAL CHARACTERISTICS

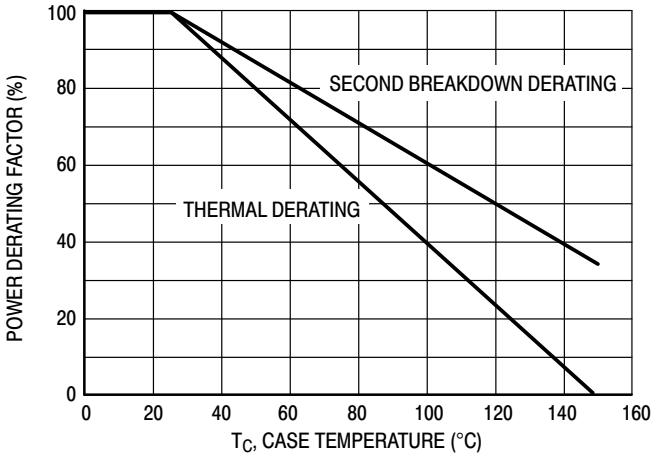


Figure 1. Power Derating

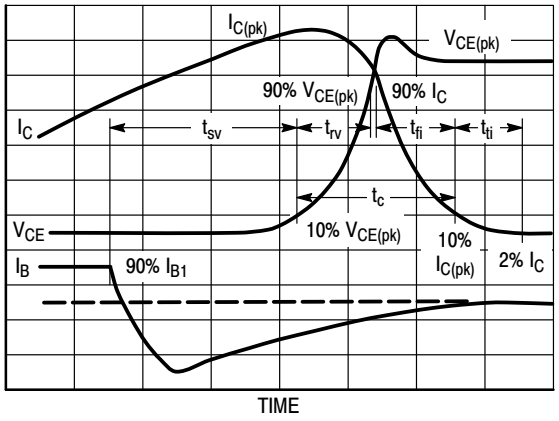


Figure 2. Inductive Switching Measurements

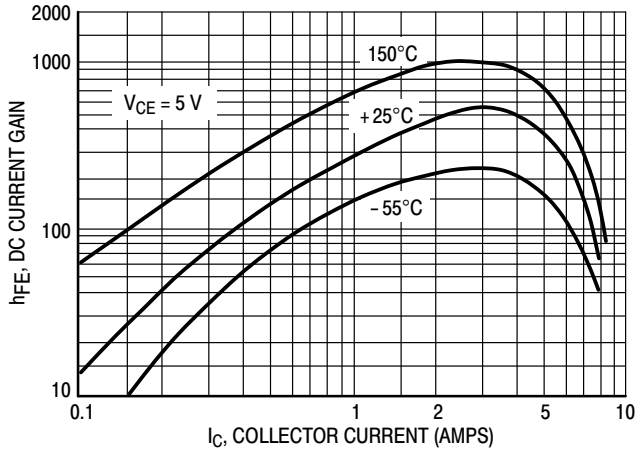


Figure 3. DC Current Gain

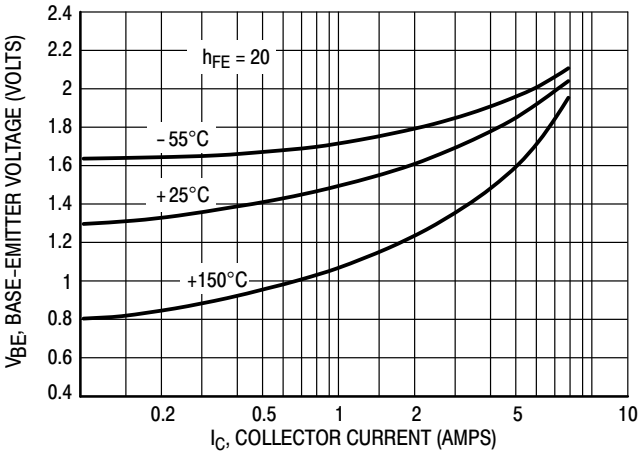


Figure 4. Base−Emitter Voltage

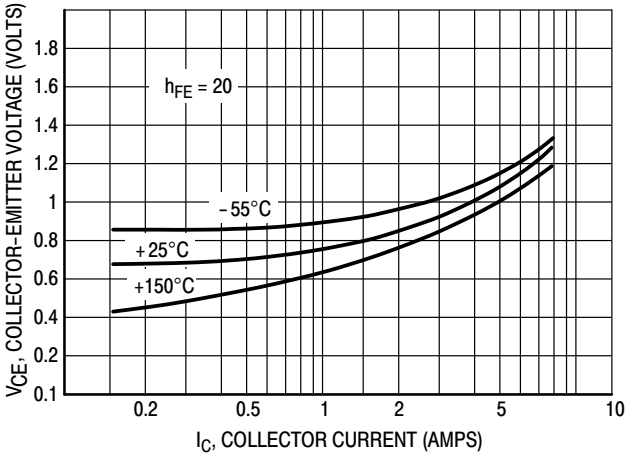
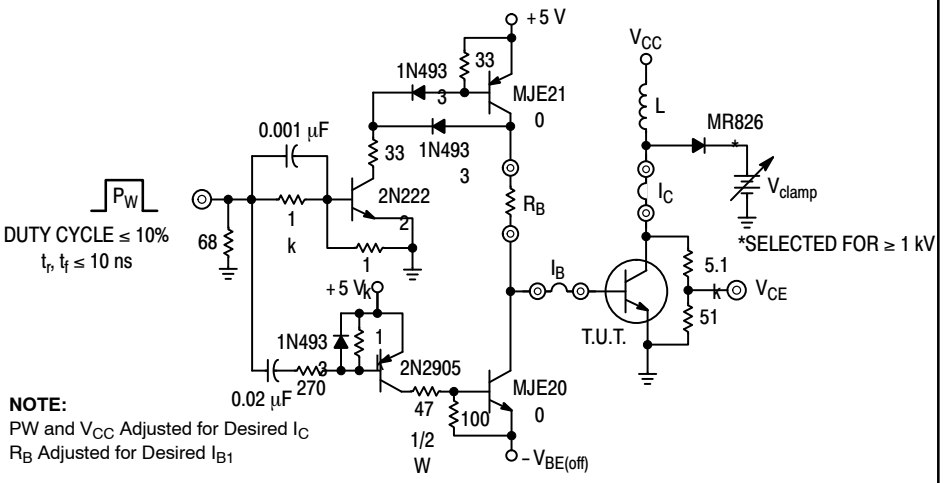
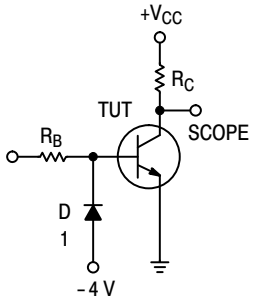
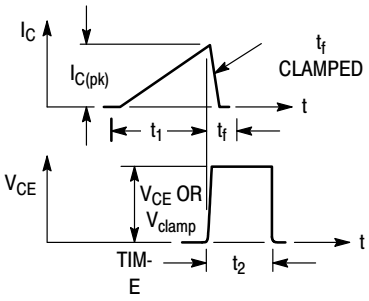
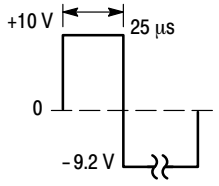


Figure 5. Collector−Emitter Saturation Voltage

MJB5742T4G

Table 1. Test Conditions for Dynamic Performance

	REVERSE BIAS SAFE OPERATING AREA AND INDUCTIVE SWITCHING	RESISTIVE SWITCHING
TEST CIRCUITS		
CIRCUIT VALUES	COIL DATA: FERROXCUBE CORE #6656 FULL BOBBIN (~16 TURNS) #16 GAP FOR 200 μH/20 A L_{coil} = 200 μH V_{CC} = 30 V $V_{CE(pk)}$ = 250 Vdc $I_{C(pk)}$ = 6 A	V_{CC} = 250 V D1 = 1N5820 OR EQUIV.
TEST WAVEFORMS	OUTPUT WAVEFORMS  t_1 ADJUSTED TO OBTAIN I_C $t_1 \approx \frac{L_{coil}(I_{C_{pk}})}{V_{CC}}$ $t_2 \approx \frac{L_{coil}(I_{C_{pk}})}{V_{clamp}}$ TEST EQUIPMENT SCOPE-TEKTRONICS 475 OR EQUIVALENT	 t_r, t_f < 10 ns DUTY CYCLE = 1% R_B AND R_C ADJUSTED FOR DESIRED I_B AND I_C

MJB5742T4G

SAFE OPERATING AREA INFORMATION

FORWARD BIAS

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 6 is based on $T_C = 25°C$; $T_{J(pk)}$ is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when $T_C \geq 25°C$. Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown on Figure 6 may be found at any case temperature by using the appropriate curve on Figure 1.

REVERSE BIAS

For inductive loads, high voltage and high current must be sustained simultaneously during turn−off, in most cases, with the base to emitter junction reverse biased. Under these conditions the collector voltage must be held to a safe level at or below a specific value of collector current. This can be accomplished by several means such as active clamping, RC snubbing, load line shaping, etc. The safe level for these devices is specified as Reverse Bias Safe Operating Area and represents the voltage−current condition allowable during reverse biased turnoff. This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode. Figure 7 gives the complete RBSOA characteristics.

The Safe Operating Area figures shown in Figures 6 and 7 are specified ratings for these devices under the test conditions shown.

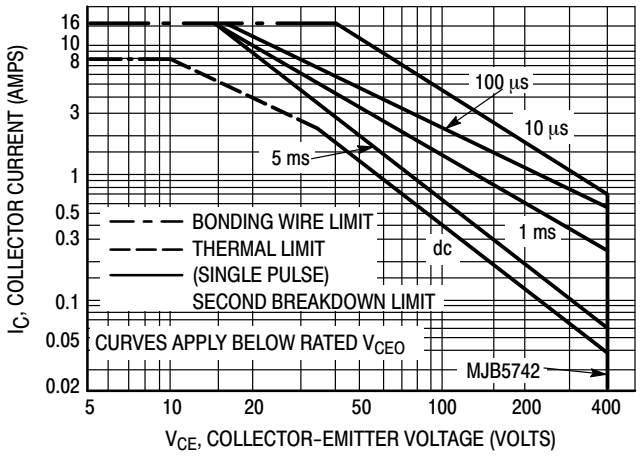


Figure 6. Forward Bias Safe Operating Area

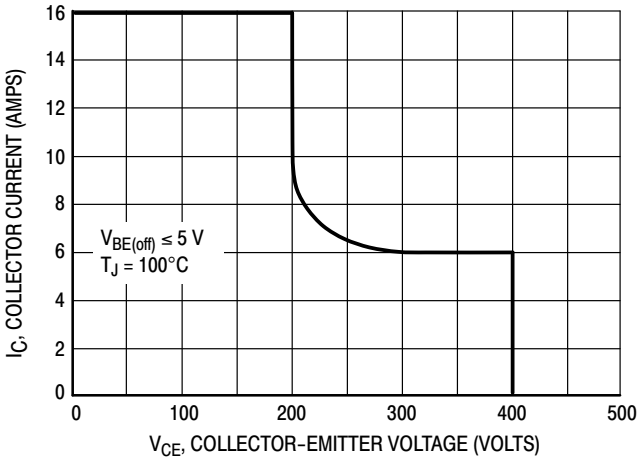


Figure 7. Reverse Bias Safe Operating Area

RESISTIVE SWITCHING PERFORMANCE

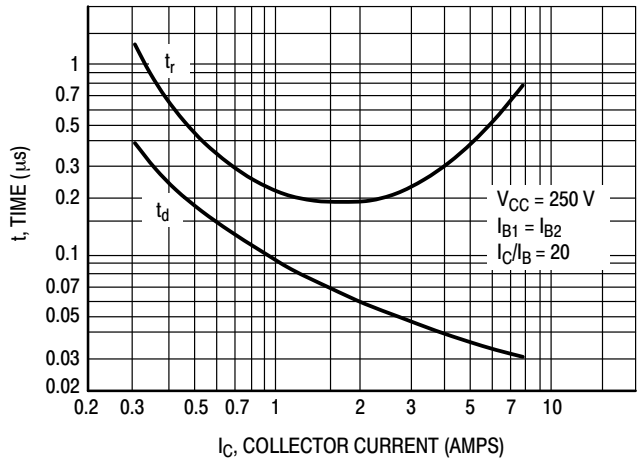


Figure 8. Turn−On Time

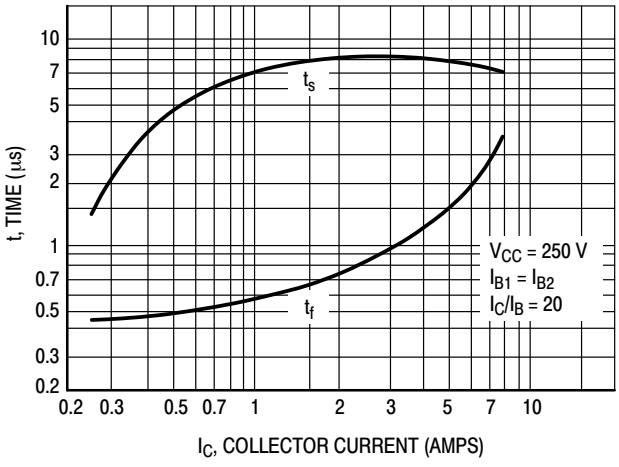
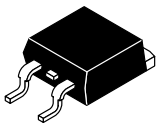


Figure 9. Turn−Off Time

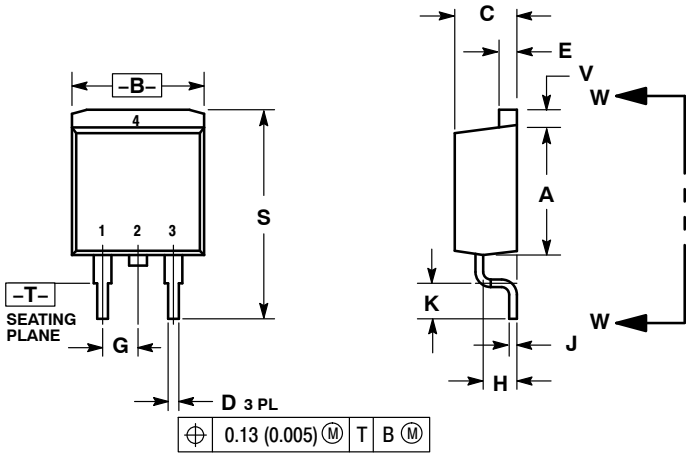
MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS



D²PAK 3
CASE 418B–04
ISSUE L

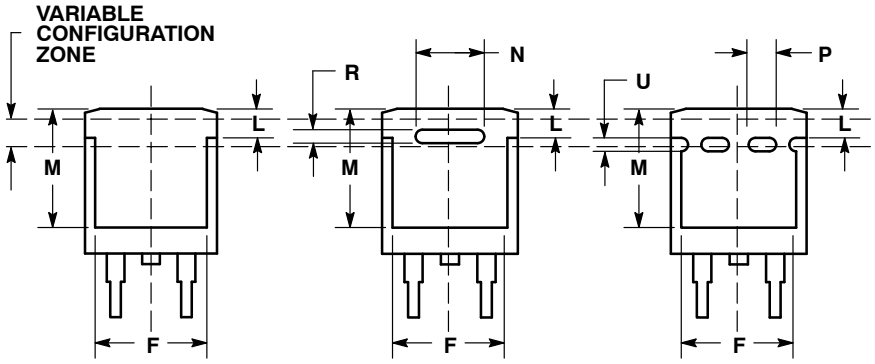
DATE 17 FEB 2015

SCALE 1:1



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 418B–01 THRU 418B–03 OBSOLETE, NEW STANDARD 418B–04.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.340	0.380	8.64	9.65
B	0.380	0.405	9.65	10.29
C	0.160	0.190	4.06	4.83
D	0.020	0.035	0.51	0.89
E	0.045	0.055	1.14	1.40
F	0.310	0.350	7.87	8.89
G	0.100 BSC		2.54 BSC	
H	0.080	0.110	2.03	2.79
J	0.018	0.025	0.46	0.64
K	0.090	0.110	2.29	2.79
L	0.052	0.072	1.32	1.83
M	0.280	0.320	7.11	8.13
N	0.197 REF		5.00 REF	
P	0.079 REF		2.00 REF	
R	0.039 REF		0.99 REF	
S	0.575	0.625	14.60	15.88
V	0.045	0.055	1.14	1.40



VIEW W–W 1 | VIEW W–W 2 | VIEW W–W 3

STYLE 1:
PIN 1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

STYLE 2:
PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

STYLE 3:
PIN 1. ANODE
2. CATHODE
3. ANODE
4. CATHODE

STYLE 4:
PIN 1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

STYLE 5:
PIN 1. CATHODE
2. ANODE
3. CATHODE
4. ANODE

STYLE 6:
PIN 1. NO CONNECT
2. CATHODE
3. ANODE
4. CATHODE

MARKING INFORMATION AND FOOTPRINT ON PAGE 2

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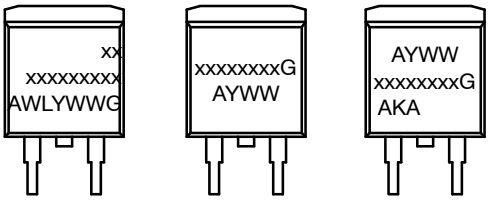
DESCRIPTION:	**D²PAK 3**		**PAGE 1 OF 2**

D²PAK 3
CASE 418B−04
ISSUE L

DATE 17 FEB 2015

GENERIC MARKING DIAGRAM*

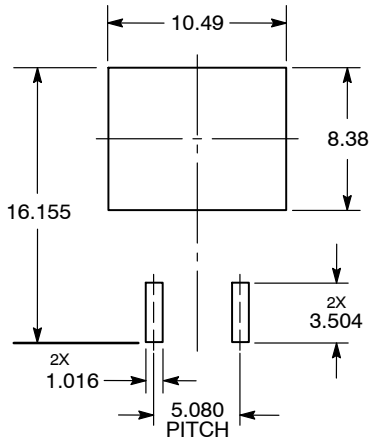


IC **Standard** **Rectifier**

xx	= Specific Device Code
A	= Assembly Location
WL	= Wafer Lot
Y	= Year
WW	= Work Week
G	= Pb−Free Package
AKA	= Polarity Indicator

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb−Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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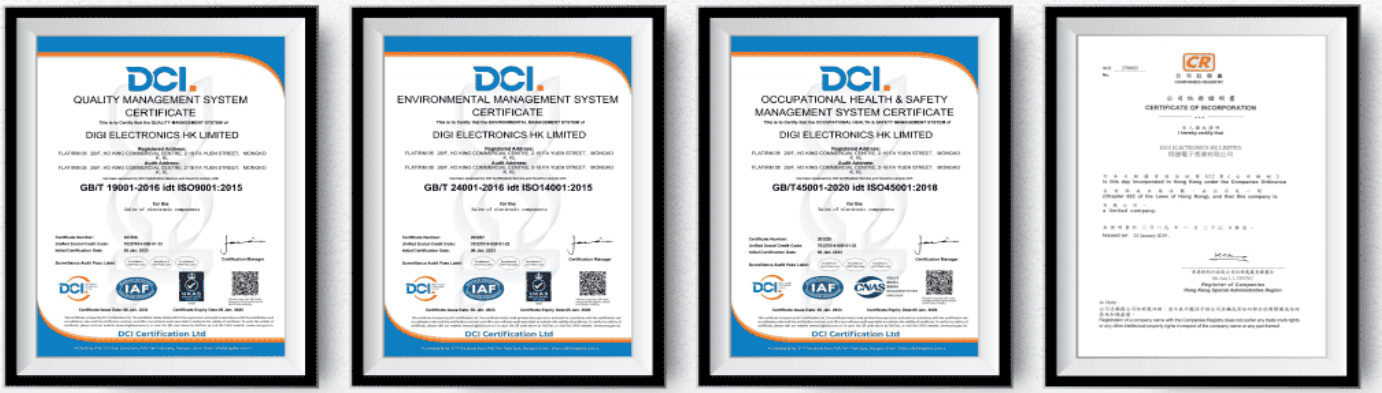

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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