

MJL21195G Datasheet

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DiGi Electronics Part Number	MJL21195G-DG
Manufacturer	onsemi
Manufacturer Product Number	MJL21195G
Description	TRANS PNP 250V 16A TO264
Detailed Description	Bipolar (BJT) Transistor PNP 250 V 16 A 4MHz 200 W Through Hole TO-264

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
MJL21195G	onsemi
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
PNP	16 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
250 V	4V @ 3.2A, 16A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
100µA	25 @ 8A, 5V
Power - Max:	Frequency - Transition:
200 W	4MHz
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Through Hole
Package / Case:	Supplier Device Package:
TO-264-3, TO-264AA	TO-264
Base Product Number:	
MJL21195	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	Not Applicable
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	

onsemi

Silicon Power Transistors

MJL21195 (PNP), MJL21196 (NPN)

The MJL21195 and MJL21196 utilize Perforated Emitter technology and are specifically designed for high power audio output, disk head positioners and linear applications.

Features

- Total Harmonic Distortion Characterized
- High DC Current Gain
- Excellent Gain Linearity
- High SOA
- Epoxy Meets UL 94, V-0 @ 0.125 in
- These Devices are Pb-Free and are RoHS Compliant*

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	250	Vdc
Collector-Base Voltage	V _{CBO}	400	Vdc
Emitter-Base Voltage	V _{EBO}	5	Vdc
Collector-Emitter Voltage - 1.5 V	V _{CEX}	400	Vdc
Collector Current – Continuous	Ι _C	16	Adc
Collector Current – Peak (Note 1)	I _{CM}	30	Adc
Base Current – Continuous	Ι _Β	5	Adc
Total Power Dissipation @ T _C = 25°C Derate Above 25°C	PD	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	– 65 to +150	°C
ESD – Human Body Model	HBM	3B	V
ESD – Machine Model	MM	С	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

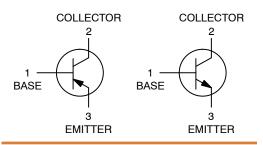
1. Pulse Test: Pulse Width = 5.0 μ s, Duty Cycle \leq 10%.

THERMAL CHARACTERISTICS

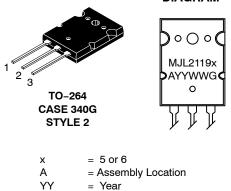
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.7	°C/W



COMPLEMENTARY



MARKING DIAGRAM



WW = Work Week

G

= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJL21195G	TO-264 (Pb-Free)	25 Units / Rail
MJL21196G	TO-264 (Pb-Free)	25 Units / Rail

*For additional information on our Pb–Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

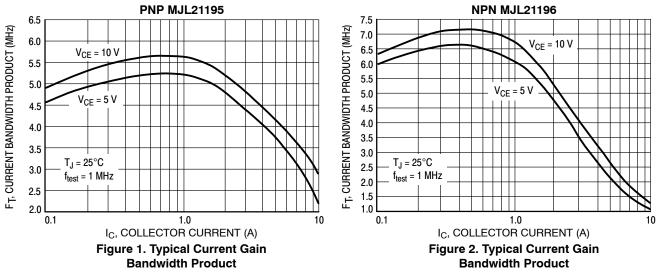
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typical	Max	Unit
OFF CHARACTERISTICS (Note 2)	•				
Collector-Emitter Sustaining Voltage $(I_{C} = 100 \text{ mAdc}, I_{B} = 0)$	V _{CEO(sus)}	250	_	_	Vdc
Collector Cutoff Current ($V_{CE} = 200 \text{ Vdc}, I_B = 0$)	ICEO	-	_	100	μAdc
OFF CHARACTERISTICS (Note 3)					
Emitter Cutoff Current ($V_{CE} = 5 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	_	100	μAdc
Collector Cutoff Current (V _{CE} = 250 Vdc, V _{BE(off)} = 1.5 Vdc)	ICEX	_	_	100	μAdc
SECOND BREAKDOWN (Note 3)					
Second Breakdown Collector Current with Base Forward Biased ($V_{CE} = 50 \text{ Vdc}, t = 1 \text{ s}$ (Nonrepetitive) ($V_{CE} = 80 \text{ Vdc}, t = 1 \text{ s}$ (Nonrepetitive)	I _{S/b}	4.0 2.25			Adc
ON CHARACTERISTICS (Note 3)					
DC Current Gain ($I_C = 8 \text{ Adc}, V_{CE} = 5 \text{ Vdc}$) ($I_C = 16 \text{ Adc}, I_B = 5 \text{ Adc}$)	h _{FE}	25 8.0		100 -	-
Base-Emitter On Voltage (I _C = 8 Adc, V _{CE} = 5 Vdc)	V _{BE(on)}	-	_	2.2	Vdc
Collector–Emitter Saturation Voltage ($I_C = 8 \text{ Adc}, I_B = 0.8 \text{ Adc}$) ($I_C = 16 \text{ Adc}, I_B = 3.2 \text{ Adc}$)	V _{CE(sat)}	-		1.4 4	Vdc
DYNAMIC CHARACTERISTICS (Note 3)			•	•	•
Total Harmonic Distortion at the Output	T _{HD}				%
(V _{RMS} = 28.3 V, f = 1 kHz, P _{LOAD} = 100 W _{RMS}) h _{FE} unmatched (Matched pair h _{FE} = 50 @ 5 A/5 V)		-	0.8	-	
h _{FE} matched		_	0.08	-	
Current Gain Bandwidth Product $(I_C = 1 \text{ Adc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 1 \text{ MHz})$	fT	4	_	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f _{test} = 1 MHz)	C _{ob}	_	_	500	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width = 5.0 μ s, Duty Cycle \leq 10%.

3. Pulse Test: Pulse Width = 300 μ s, Duty Cycle < 2%.



TYPICAL CHARACTERISTICS

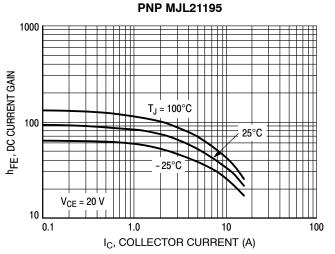
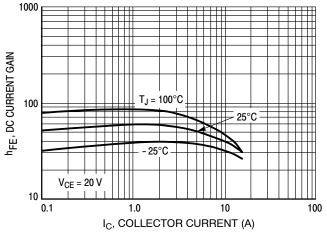


Figure 3. DC Current Gain, V_{CE} = 20 V



NPN MJL21196

Figure 4. DC Current Gain, V_{CE} = 20 V

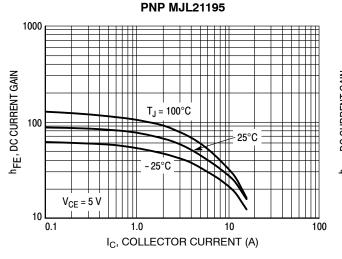
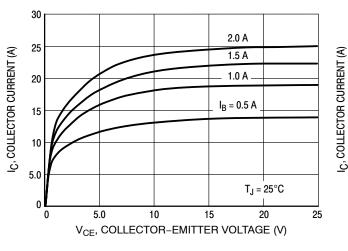
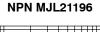
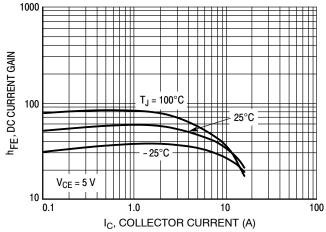


Figure 5. DC Current Gain, $V_{CE} = 5 V$

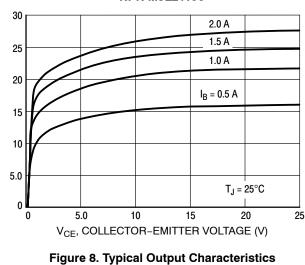












PNP MJL21195

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TYPICAL CHARACTERISTICS

1.4

1.2

1.0

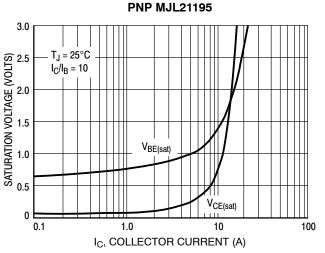
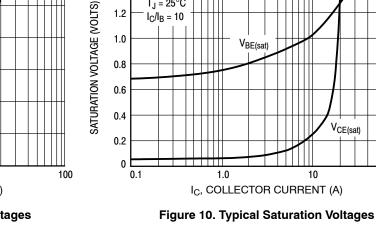


Figure 9. Typical Saturation Voltages

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 $T_J = 25^{\circ}C$

 $I_{\rm C}/I_{\rm B} = 10$



NPN MJL21196

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V_{CE(sat)}

100

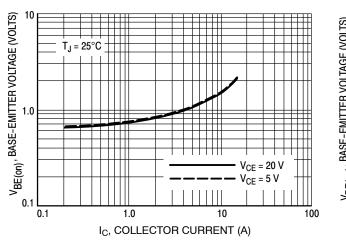


Figure 11. Typical Base-Emitter Voltage

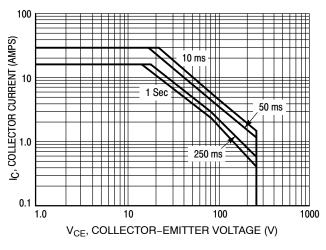


Figure 13. Active Region Safe Operating Area

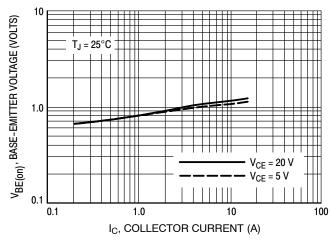


Figure 12. Typical Base-Emitter Voltage

There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 13 is based on $T_{J(pk)} = 150^{\circ}C$; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.

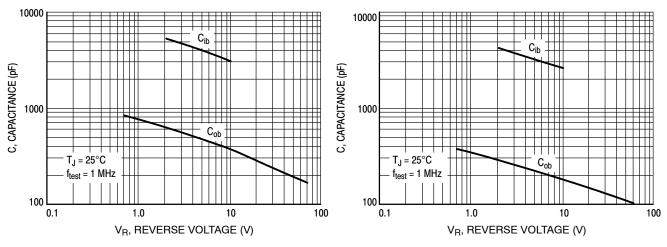
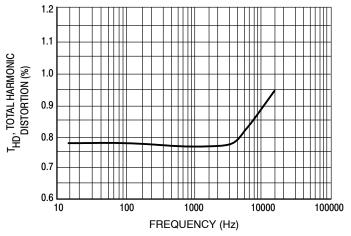


Figure 14. MJL21195 Typical Capacitance

Figure 15. MJL21196 Typical Capacitance





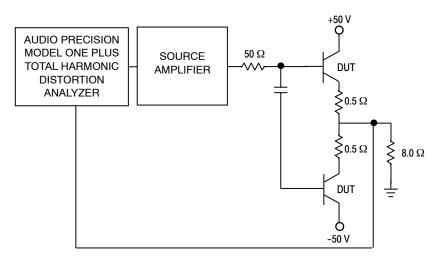
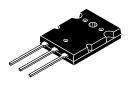


Figure 17. Total Harmonic Distortion Test Circuit



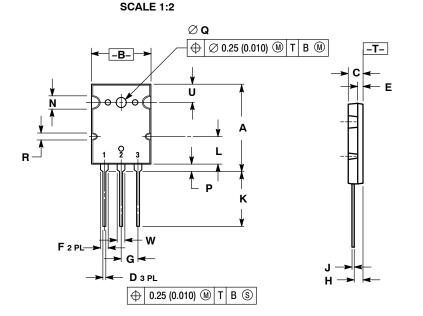
MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS



TO-3BPL (TO-264) CASE 340G-02 **ISSUE J**

DATE 17 DEC 2004



NOTES:

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	28.0	29.0	1.102	1.142
в	19.3	20.3	0.760	0.800
С	4.7	5.3	0.185	0.209
D	0.93	1.48	0.037	0.058
Е	1.9	2.1	0.075	0.083
F	2.2	2.4	0.087	0.102
G	5.45	BSC	0.215 BSC	
н	2.6	3.0	0.102	0.118
J	0.43	0.78	0.017	0.031
к	17.6	18.8	0.693	0.740
L	11.2	REF	0.411	REF
Ν	4.35	REF	0.172	REF
Ρ	2.2	2.6	0.087	0.102
Q	3.1	3.5	0.122	0.137
R	2.25	2.25 REF		REF
U	6.3	REF	0.248	REF
w	2.8	3.2	0.110	0.125

GENERIC **MARKING DIAGRAM***

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:	[
PIN 1. GATE 2. DRAIN	PIN 1. BASE 2. COLLECTOR	PIN 1. GATE 2. SOURCE	PIN 1. DRAIN 2. SOURCE	PIN 1. GATE 2. COLLECTOR	0000
3. SOURCE	3. EMITTER	3. DRAIN	3. GATE	3. EMITTER	

XXXXXX = Specific Device Code А = Location Code YY = Year WW = Work Week

0

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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DESCRIPTION:	TO-3BPL (TO-264)		PAGE 1 OF 1

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MJL21195G onsemi TRANS PNP 250V 16A TO264

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