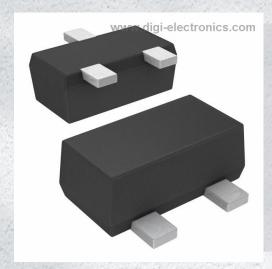


# **MMBT2222AT Datasheet**



https://www.DiGi-Electronics.com

DiGi Electronics Part Number MMBT2222AT-DG

Manufacturer onsemi

Manufacturer Product Number MMBT2222AT

Description TRANS NPN 40V 0.6A SC89-3

Detailed Description Bipolar (BJT) Transistor NPN 40 V 600 mA 300MHz 2

50 mW Surface Mount SC-89-3



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
MMBT2222AT	onsemi
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
NPN	600 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
40 V	1V @ 50mA, 500mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
	100 @ 150mA, 1V
Power - Max:	Frequency - Transition:
250 mW	300MHz
Operating Temperature:	Mounting Type:
150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
SC-89, SOT-490	SC-89-3
Base Product Number:	
MMBT2222	

## **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



## **General Purpose Transistor**

#### **NPN Silicon**

## MMBT2222ATT1G, **NSVMMBT2222ATT1G**

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

#### **Features**

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

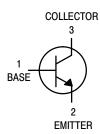
Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	40	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	75	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	Vdc
Collector Current - Continuous	Ic	600	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1)  T <sub>A</sub> = 25°C	P <sub>D</sub>	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	833	°C/W
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.





**CASE 463** SOT-416/SC-75 STYLE 1

#### **MARKING DIAGRAM**



1P = Specific Device Code

= Date Code Μ

= Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBT2222ATT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel
NSVMMBT2222ATT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Charac	eteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				•	
Collector – Emitter Breakdown Voltage (No $(I_C = 10 \text{ mAdc}, I_B = 0)$	te 1)	V <sub>(BR)CEO</sub>	40	_	Vdc
Collector – Base Breakdown Voltage ( $I_C = 10 \mu Adc, I_E = 0$ )		V <sub>(BR)CBO</sub>	75	-	Vdc
Emitter – Base Breakdown Voltage ( $I_E = 10 \mu Adc, I_C = 0$ )		V <sub>(BR)EBO</sub>	6.0	-	Vdc
Base Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>EB</sub> = 3.0 Vdc)		I <sub>BL</sub>	-	20	nAdc
Collector Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>EB</sub> = 3.0 Vdc)		I <sub>CEX</sub>	-	10	nAdc
ON CHARACTERISTICS (Note 2)					
DC Current Gain $ \begin{aligned} &(I_C=0.1 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=1.0 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=10 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=150 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=500 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \end{aligned} $		H <sub>FE</sub>	35 50 75 100 40	- - - -	_
Collector – Emitter Saturation Voltage ( $I_C$ = 150 mAdc, $I_B$ = 15 mAdc) ( $I_C$ = 500 mAdc, $I_B$ = 50 mAdc)		V <sub>CE(sat)</sub>	- -	0.3 1.0	Vdc
Base – Emitter Saturation Voltage (I <sub>C</sub> = 150 mAdc, I <sub>B</sub> = 15 mAdc) (I <sub>C</sub> = 500 mAdc, I <sub>B</sub> = 50 mAdc)		V <sub>BE(sat)</sub>	0.6	1.2 2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product (I <sub>C</sub> = 20 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 M	Hz)	f <sub>T</sub>	300	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)		C <sub>obo</sub>	-	8.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)		C <sub>ibo</sub>	-	30	pF
Input Impedance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kHz)		h <sub>ie</sub>	0.25	1.25	kΩ
Voltage Feedback Ratio ( $V_{CE}$ = 10 Vdc, $I_{C}$ = 10 mAdc, f = 1.0 kH	z)	h <sub>re</sub>	-	4.0	X 10 <sup>-4</sup>
Small – Signal Current Gain (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kH	z)	h <sub>fe</sub>	75	375	-
Output Admittance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kHz)		h <sub>oe</sub>	25	200	μmhos
Noise Figure ( $V_{CE}$ = 10 Vdc, $I_{C}$ = 100 $\mu$ Adc, $R_{S}$ = 1.0	k ohms, f = 1.0 kHz)	NF	-	4.0	dB
SWITCHING CHARACTERISTICS					
Delay Time	$(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = -0.5 \text{ Vdc},$ $I_{C} = 150 \text{ mAdc}, I_{B1} = 15 \text{ mAdc})$	t <sub>d</sub>	_	10	ne
Rise Time		t <sub>r</sub>	_	25	ns
Storage Time	(V <sub>CC</sub> = 30 Vdc, I <sub>C</sub> = 150 mAdc,	t <sub>s</sub>	-	225	ns
Fall Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$	t <sub>f</sub>	-	60	

Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.
 Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

#### **SWITCHING TIME EQUIVALENT TEST CIRCUITS**

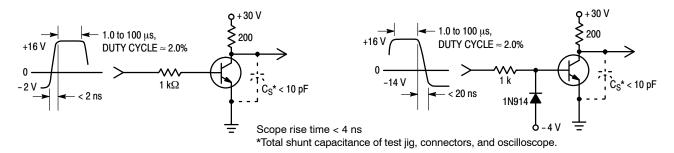


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

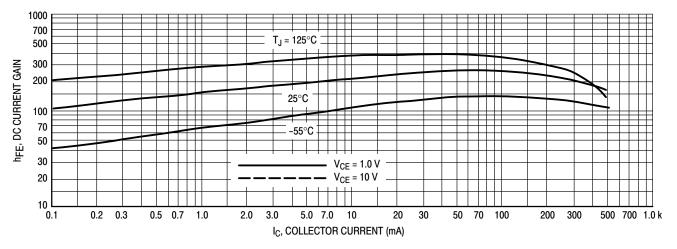


Figure 3. DC Current Gain

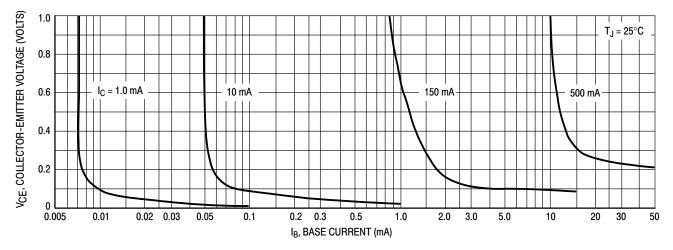
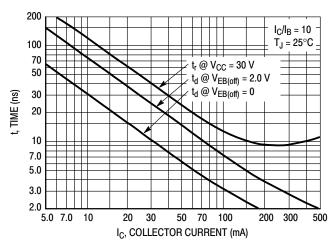


Figure 4. Collector Saturation Region

300



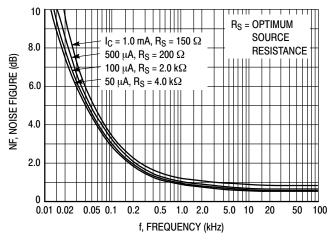
 $t'_{s} = t_{s} - 1/8 t_{f}$ 200  $\mathsf{I}_{\mathsf{B}1} = \mathsf{I}_{\mathsf{B}2}$  $T_J = 25^{\circ}C$ 100 t, TIME (ns) 70 50 30 20 10 7.0 5.0 5.0 7.0 10 50 70 100 30 200 300 500

 $V_{CC} = 30 \text{ V}$  $I_C/I_B = 10$ 

Figure 5. Turn - On Time

I<sub>C</sub>, COLLECTOR CURRENT (mA)

Figure 6. Turn – Off Time



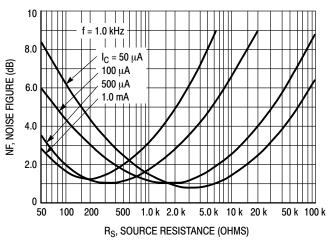
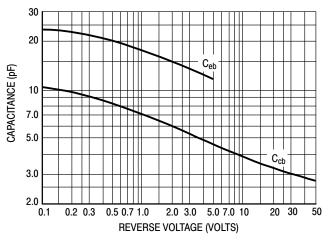


Figure 7. Frequency Effects

Figure 8. Source Resistance Effects



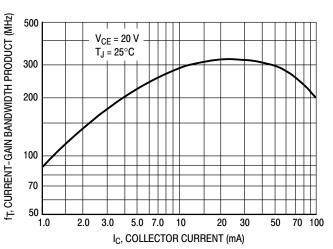


Figure 9. Capacitances

Figure 10. Current-Gain Bandwidth Product

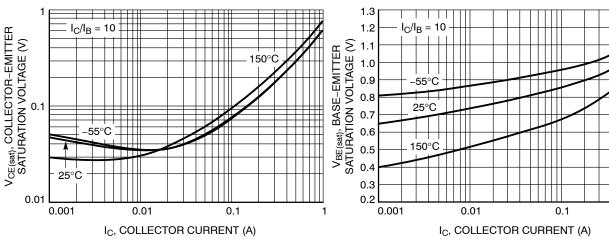


Figure 11. Collector Emitter Saturation Voltage vs. Collector Current

Figure 12. Base Emitter Saturation Voltage vs. **Collector Current** 

 $R_{\theta VC}$  for  $V_{CE(sat)}$ 

 $R_{\theta VB}$  for  $V_{BE}$ 

50

100 200

500

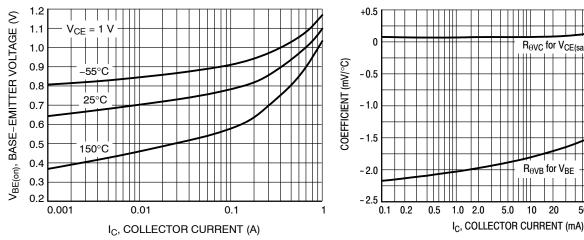


Figure 13. Base Emitter Voltage vs. Collector Current

Figure 14. Temperature Coefficients

5.0 10

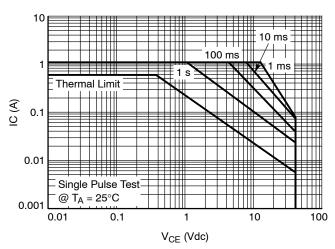


Figure 15. Safe Operating Area



#### **MECHANICAL CASE OUTLINE**

**PACKAGE DIMENSIONS** 

#### SC75-3 1.60x0.80x0.80, 1.00P

**CASE 463 ISSUE H** 

**DATE 01 FEB 2024** 

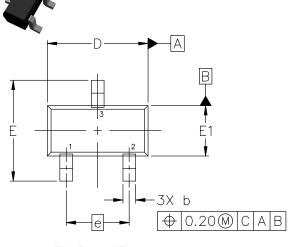
#### NOTES:

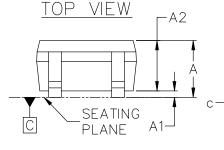
- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.

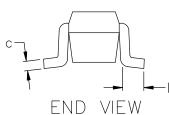
DIM	MILLIMETERS			
DIIVI	MIN.	NOM.	MAX.	
А	0.70	0.80	0.90	
A1	0.00	0.05	0.10	
A2	0.80 REF.			
Ь	0.15	0.20	0.30	
С	0.10	0.15	0.25	
D	1.55	1.60	1.65	
E	1.50	1.60	1.70	
E1	0.70	0.80	0.90	
е	1.00 BSC			
L	0.10	0.15	0.20	

-0.356

0.787







# SIDE VIEW

### **GENERIC MARKING DIAGRAM\***



XX = Specific Device Code

Μ = Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	
PIN 1. BASE	
2 EMITTED	

STYLE 2: PIN 1. ANODE

STYLE 3: PIN 1. ANODE 2. ANODE 3 CATHODE

FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

1.000

RECOMMENDED MOUNTING FOOTPRINT\*

1.803

0.508

3. COLLECTOR	3
STYLE 4: PIN 1. CATHODE	

2. CATHODE 3. ANODE

STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

2. N/C

3. CATHODE

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